

HER3001PT - HER3008PT

30.0AMPS. Glass Passivated High Efficient Rectifiers TO-3P/TO-247AD

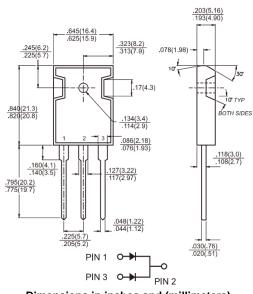


Features

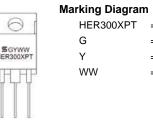
- ♦ UL Recognized File # E-326243
- ♦ Dual rectifier construction, positive center-tap
- ♦ Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- ♦ Glass passivated chip junctions
- ♦ Superfast recovery time, high voltage
- ♦ Low forward voltage, high current capability
- ♦ Low thermal resistance
- ♦ Low power loss, high efficiency
- ♦ High temperature soldering guaranteed: 260°C, 0.16"(4.06mm) from case for 10 seconds
- ☆ Green compound with suffix "G" on packing code & prefix "G" on datecode

Mechanical Data

- ♦ Cases: TO-3P/TO-247AD Molded plastic
- ✤ Terminals: Pure tin plated, lead free, solderable per MIL-STD-750, Method 2026
- ♦ Polarity: As marked
- ♦ Mounting position: Any
- ♦ Mounting torque: 10in-lbs Max
- ♦ Weight: 5.6 grams



Dimensions in inches and (millimeters)



- = Specific Device Code = Green Compound
- = Year
- = Work Week

Maximum Ratings and Electrical Characteristics

Rating at 25 $^\circ\!C$ ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%

V _{RRM}	50		3003P	3004P	3005P	3006P	3007P	3008PT	Units
	50	100	200	300	400	600	800	1000	V
V _{RMS}	35	70	140	210	280	420	560	700	V
V _{DC}	50	100	200	300	400	600	800	1000	V
I _{F(AV)}	30							A	
I _{FSM}	300								A
V_{F}	1.0 1.3 1.7					V			
1						0			
'R	500								uA
Trr	50 80						nS		
Cj	175 145					pF			
TJ	- 55 to + 150							°C	
T _{STG}	- 55 to + 150						°C		
	V _{DC} F(AV) I _{FSM} V _F I _R Trr Cj T _J	NMS 20 VDC 50 F(AV) I IFSM V IR I Trr Cj T_J I	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	NMS C F F V _{DC} 50 100 200 F(AV) I I I V _F 1.0 I I I _R I I I I Trr 50 50 175 I T _J I I I I I	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

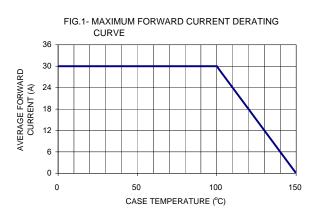
Note 1: Pulse Test with PW=300 usec, 1% Duty Cycle

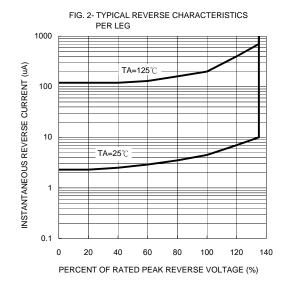
Note 2: Reverse Recovery Test Conditions: IF=0.5A, IR=1.0A, IRR=0.25A

Note 3: Measured at 1 MHz and Applied Reverse Voltage of 4.0Volts.



RATINGS AND CHARACTERISTIC CURVES (HER3001PT THRU HER3008PT)





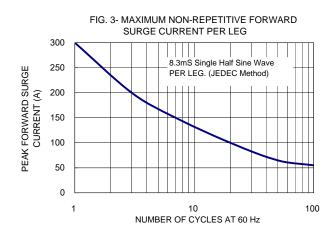
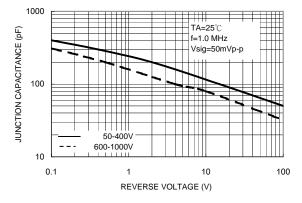


FIG. 4- TYPICAL JUNCTION CAPACITANCE PER LEG



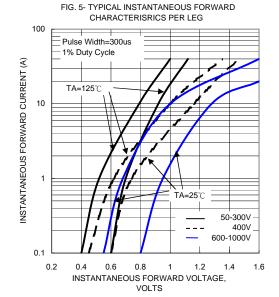
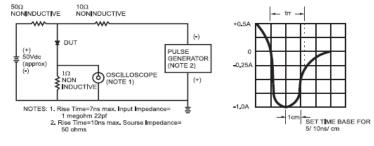


FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM



Version:C10