

# NTC thermistors for temperature measurement

SMD NTC thermistors, case size 0402 (1005), automotive series

Series/Type: B572\*\*V5

Date: October 2012

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B572\*\*V5

## SMD NTC thermistors, case size 0402 (1005)

Automotive series

## **SMD**

## **Applications**

Temperature measurement and compensation

#### **Features**

- Qualification based on AEC-Q200 Rev-D
- Multilayer SMD NTC with inner electrodes
- Nickel barrier termination
- For temperature measurement up to 150 °C
- Excellent long-term aging stability in high temperature and high humidity environment

#### **Options**

Alternative resistance ratings, resistance tolerances and B value tolerances available on request.

## Delivery mode

Cardboard tape, 180-mm reel

#### General technical data

0.	5±0.0	5 <b>—</b>	0.6 max	
1.0±0.1		0.25±0.15		

Termination TNT0395-Q-E

Dimensions in mm Approx. weight 4.5 mg

Dimensional drawing

Operating temperature range		T <sub>op</sub>	40 150	°C
Max. power	(at 25 °C, on PCB)	P <sub>25</sub> <sup>1)</sup>	150	mW
Resistance tolerance		$DR_R/R_R$	±1, ±5	%
Rated temperature		$T_R$	25	°C
Dissipation factor	(on PCB)	$d_{th}^{1)}$	approx. 2.5	mW/K
Thermal cooling time constant	(on PCB)	t c 1)	approx. 3	S
Heat capacity		$C_{th}^{1)}$	approx. 7.5	mJ/K

## Electrical specification and ordering codes

R <sub>25</sub> W	DR <sub>R</sub> /R <sub>R</sub> %	No. of R/T characteristic	B <sub>25/50</sub> K	B <sub>25/85</sub> K	B <sub>25/100</sub> K	Ordering code
4.7 k	±5	8502	3940	3980	4000 ±3%	B57251V5472J060
10 k	±5	8509	3380	3435	3455 ±1%	B57232V5103+360
10 k	±1	8509	3380	3435	3455 ±1%	B57232V5103F360
10 k	±5	8502	3940	3980	4000 ±3%	B57251V5103J060

## + = Resistance tolerance

 $F = \pm 1\%$  (only for B57232V5103F360)

 $H = \pm 3\%$ 

 $J = \pm 5\%$ 

<sup>1)</sup> Depends on mounting situation



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Reliability data only for type B57232V5103+360 and B57232V5103F360

Tests of SMD NTC thermistors are based on AEC-Q200 Rev-D. The parts are mounted on standardized PCB.

Test	Standard	Test conditions	DR <sub>25</sub> /R <sub>25</sub> (typical)	Remarks
Pre- and post-stress electrical test		Resistance at: 25 °C and 100 °C		
High temperature exposure (storage)	MIL-STD-202, method 108	Test temperature: 150 °C Duration: 1000 h Unpowered	< 1%	
Temperature cycling	JESD22, method JA-104	Lower test temperature: 40 °C Upper test temperature: 150 °C Number of cycles: 1000 Transfer time: < 10 s Dwell time: 15 min Air Air	< 3%	Temperature cycling is performed acc. MIL-STD-202 method 107. No warrant will be assumed for the reliability of solder joint.
Biased humidity	MIL-STD-202, method 103	Test temperature: 85 °C Rel. humidity of air: 85% Duration: 1000 h Test voltage: V <sub>NTC</sub> = 0.3 V DC	< 3%	
Operational life	MIL-STD-202, method 108	Test temperature: 150 °C P <sub>max</sub> = 0.35 mW Duration: 1000 h	< 3%	
External visual	MIL-STD-883E, method 2009	Visual inspection		
Physical dimensions	JESD22, method JB-100	Measured with calibers		Within the specified values
Resistance to solvents	MIL-STD-202, method 215	Not applicable for SMD thermistors (component has no marking, color coding or coating)		
Mechanical shock	MIL-STD-202, method 213	Peak value: 1500 g Half sine Condition F	< 1%	



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Test	Standard	Test conditions	DR <sub>25</sub> /R <sub>25</sub> (typical)	Remarks
Vibration	MIL-STD-202, method 204	Acceleration: 5 g Sweep time: 20 min Frequency range: 10 2000 Hz 3 ´ 12 cycles	< 1%	
Resistance to soldering heat	MIL-STD-202, method 210	Dip: 260 °C; 10 s 1 heat cycle	< 1%	
ESD	AEC-Q200-002, method -002	Discharge capacitance: 150 pF Discharge resistance: 2 kW Charging voltage: 6 kV Contact discharge 2 pulses in each polarity	< 3%	
Solderability	J-STD-002	a) Dip: 235 °C; 5 s: aging 4 h @ 155 °C b) Dip: 215 °C; 5 s: steam aging 8 h @ 92 °C c) Dip: 260 °C; 7 s: steam aging 8 h @ 92 °C		95% of termination wetted
Electrical characterization		R(25 °C), R(100 °C), B(25/100)		Within the specified values
Flammability	UL-94, V-0 or V-1	Not applicable for SMD thermistors (component is not coated or encapsulated with plastic materials)		
Board flex	AEC-Q200-005, method -005	Max. bending: 1 mm Duration @ max. bending: 60 s	< 2%	
Terminal strength	AEC-Q200-006, method -006	Max. F: 5 N	< 2%	
Resistance drift after soldering		Reflow soldering profile Wave soldering profile	< 1%	



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Pre- and post-stress electrical test		Resistance at: 25 °C and 100 °C		
High temperature exposure (storage)	MIL-STD-202, method 108	Test temperature: 150 °C Duration: 1000 h Unpowered	< 5%	
Temperature cycling	JESD22, method JA-104	Lower test temperature: 40 °C Upper test temperature: 150 °C Number of cycles: 1000 Transfer time: < 10 s Dwell time: 15 min Air Air	< 5%	
Biased humidity	MIL-STD-202, method 103	Test temperature: 85 °C Rel. humidity of air: 85% Duration: 1000 h Test voltage: V <sub>NTC</sub> = 0.3 V DC	< 5%	
Operational life	MIL-STD-202, method 108	Test temperature: 150 °C P <sub>max</sub> = 0.35 mW Duration: 1000 h	< 5%	
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		Contact discharge 2 pulses in each polarity		
Solderability	J-STD-002	a) Dip: 235 °C; 5 s: aging 4 h @ 155 °C b) Dip: 215 °C; 5 s: steam aging 8 h @ 92 °C c) Dip: 260 °C; 7 s: steam aging 8 h @ 92 °C		95% of termination wetted
Electrical characterization		R(25 °C), R(100 °C), B(25/100)		Within the specified values
Flammability	UL-94, V-0 or V-1	Not applicable for SMD thermistors (component is not coated or encapsulated with plastic materials)		
Board flex	AEC-Q200-005 method -005	Max. bending: 1 mm Duration @ max. bending: 60 s	< 5%	
Terminal strength	AEC-Q200-006 method -006	Max. F: 5 N	< 5%	
Resistance drift after soldering		Reflow soldering profile Wave soldering profile	< 1%	



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## **SMD**

## R/T characteristics

R/T No.	8502		8509	
T (°C)	$B_{25/100} = 4000 \text{ K}$		$B_{25/100} = 3455 \text{ K}$	(
	R <sub>T</sub> /R <sub>25</sub>	a (%/K)	R <sub>T</sub> /R <sub>25</sub>	a (%/K)
55.0	96.158	7.4	44.605	5.9
50.0	66.892	7.1	33.281	5.8
45.0	47.127	6.9	25.044	5.6
40.0	33.606	6.6	19.003	5.4
35.0	24.243	6.4	14.536	5.3
30.0	17.681	6.2	11.206	5.1
25.0	13.032	6.0	8.7041	5.0
20.0	9.702	5.8	6.8104	4.8
15.0	7.2923	5.6	5.3665	4.7
10.0	5.5314	5.4	4.2576	4.6
5.0	4.2325	5.3	3.4001	4.4
0.0	3.2657	5.1	2.7326	4.3
5.0	2.54	4.9	2.2096	4.2
10.0	1.9907	4.8	1.7973	4.1
15.0	1.5716	4.7	1.4703	4.0
20.0	1.2494	4.5	1.2093	3.9
25.0	1.0000	4.4	1.0000	3.7
30.0	0.80552	4.3	0.83113	3.6
35.0	0.65288	4.1	0.69418	3.6
40.0	0.53229	4.0	0.58255	3.5
45.0	0.43645	3.9	0.49112	3.4
50.0	0.35981	3.8	0.41587	3.3
55.0	0.29819	3.7	0.35365	3.2
60.0	0.24837	3.6	0.30197	3.1
65.0	0.20787	3.5	0.25888	3.0
70.0	0.17479	3.4	0.22278	3.0
75.0	0.14763	3.3	0.19243	2.9
80.0	0.12523	3.2	0.16681	2.8
85.0	0.10667	3.2	0.1451	2.8
90.0	0.091227	3.1	0.12663	2.7
95.0	0.078319	3.0	0.11088	2.6
100.0	0.067488	2.9	0.097381	2.6
105.0	0.058363	2.9	0.085788	2.5
110.0	0.050647	2.8	0.075795	2.4
115.0	0.044098	2.7	0.067155	2.4
120.0	0.03852	2.7	0.059663	2.3
125.0	0.033752	2.6	0.053146	2.3
130.0	0.029663	2.6	0.047463	2.2
135.0	0.026146	2.5	0.042493	2.2
140.0	0.023111	2.4	0.038134	2.1
145.0	0.020484	2.4	0.034302	2.1
150.0	0.018203	2.3	0.030925	2.1



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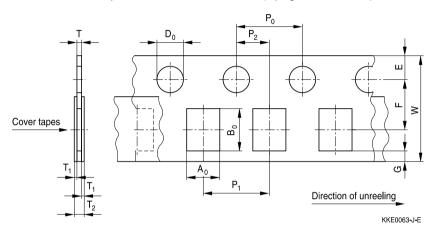
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## **SMD**

## Taping and packing

## 1 Taping of SMD NTC thermistors

## 1.1 Cardboard tape for case size 0402 and 0603 (taping to IEC 60286-3)



## Dimensions (mm)

	Case size 0402 (8-mm tape)	Case size 0603 (8-mm tape)	Tolerance
$\overline{A_0'B_0}$	0.60 ′ 1.15	0.95 ′ 1.80	±0.2
$T_2$	0.70	1.10	
T	0.60	0.90	max.
$D_0$	1.50	1.50	±0.10
P <sub>0</sub>	4.00	4.00	±0.10 <sup>1)</sup>
$P_2$	2.00	2.00	±0.05
P <sub>1</sub>	2.00	4.00	±0.10
W	8.00	8.00	±0.30
E	1.75	1.75	±0.10
F	3.50	3.50	±0.05
G	0.75	0.75	min.

<sup>1) £0.2</sup> mm over 10 sprocket holes.

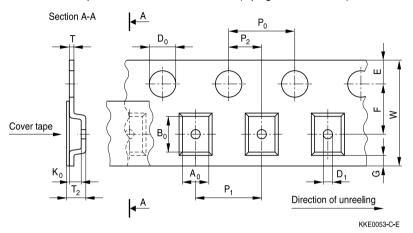
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## **SMD**

## 1.2 Blister tape for case size 0805 and 1206 (taping to IEC 60286-3)



Dimensions (mm)

	Case size	Case size	Tolerance	
	0805	1206		
	(8-mm tape)	(8-mm tape)		
$A_0$ $B_0$	1.60 ′ 2.40	1.90 ′ 3.50	±0.2	
$K_0$	1.40	1.40	max.	
T <sub>2</sub>	2.5	2.5	max.	
$D_0$	1.50	1.50	+0.10/-0	
D <sub>1</sub>	1.00	1.00	min.	
$P_0$	4.00	4.00	±0.10 <sup>2)</sup>	
$P_2$	2.00	2.00	±0.05	
P <sub>1</sub>	4.00	4.00	±0.10	
W	8.00	8.00	±0.30	
E	1.75	1.75	±0.10	
F	3.50	3.50	±0.05	
G	0.75	0.75	min.	

<sup>2) £0.2</sup> mm over 10 sprocket holes.



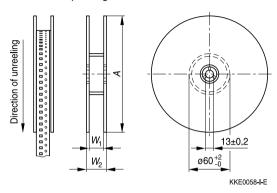
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## **SMD**

## 1.3 Reel packing



## Packing survey

Case size	Chip thick- ness <sup>3)</sup> mm	8-mm t	ape	Reel dimensions					Packing pcs./reel	units
		Blister	Card- board	А	Tol.	W1	Tol.	W2	180-mm reel	330-mm reel
0402	0.5		х	180	3/+0	8.4	+1.5/ 0	14.4 max.	10000	
0603	0.8		х	180	3/+0	8.4	+1.5/ 0	14.4 max.	4000	
				330	±2.0	12.4	+1.5/ 0	18.4 max.		16000
0805	0.8	х		180	3/+0	8.4	+1.5/ 0	14.4 max.	4000	16000
	1.2	х		330	±2.0	12.4	+1.5/ 0	18.4 max.	3000	12000
1206	0.8	х		180	2/+0	8.4	+1.5/ 0	14.4 max.	4000	
	1.2	х		180	2/+0	8.4	+1.5/ 0	14.4 max.	2000	

<sup>3)</sup> Chip thickness depends on the resistance value.



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## **SMD**

## 2 Packing codes

The last two digits of the complete ordering code state the packing mode:

Last two digits			
60	SMD	Cardboard tape	180-mm reel packing
62	SMD	Blister tape	180-mm reel packing
70	SMD	Cardboard tape	330-mm reel packing
72	SMD	Blister tape	330-mm reel packing



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## **SMD**

## Mounting instructions

## 1 Soldering

#### 1.1 SMD NTC thermistors

SMD NTC thermistors can be provided with a nickel barrier termination or on special request with silver-palladium termination. The usage of mild, non-activated fluxes for soldering is recommended as well as a proper cleaning of the PCB.

The nickel barrier layer of the silver/nickel/tin termination (see figure 1) prevents leaching of the silver base metalization layer. This allows great flexibility in the selection of soldering parameters.

The tin prevents the nickel layer from oxidizing and thus ensures better wetting by the solder. The nickel barrier termination is suitable for all commonly-used soldering methods.

Note: SMD NTCs with AgPd termination are not approved for lead-free soldering.

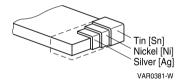


Figure 1
SMD NTC thermistors, structure of nickel barrier termination

## 1.1.1 Solderability (test to IEC 60068-2-58)

Preconditioning: Immersion into flux F-SW 32.

Evaluation criterion: Wetting of soldering areas 3 95%.

Solder	Bath temperature (°C)	Dwell time (s)
SnPb 60/40	215 ±3	3 ±0.3
SnAg (3.0 4.0), Cu (0.5 0.9)	245 ±3	3 ±0.3

## 1.1.2 Resistance to soldering heat (test to IEC 60068-2-58)

Preconditioning: Immersion into flux F-SW 32. Evaluation criterion: Leaching of side edges £1/3.

Solder	Bath temperature (°C)	Dwell time (s)
SnPb 60/40	260 ±5	10 ±1
SnAg (3.0 4.0), Cu (0.5 0.9)	260 ±5	10 ±1



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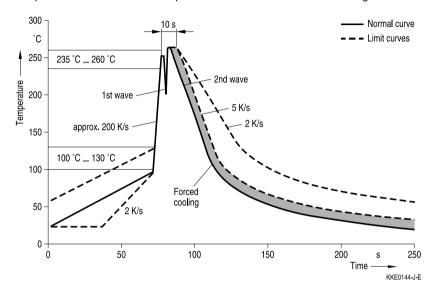
SMD NTC thermistors, case size 0402 (1005)

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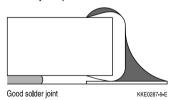
## **SMD**

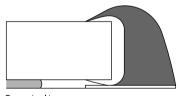
## Wave soldering

Temperature characteristic at component terminal with dual wave soldering



## Solder joint profiles for silver/nickel/tin terminations





Too much solder Pad geometry too large, not soldered in preferred direction

KKE0288-H-E



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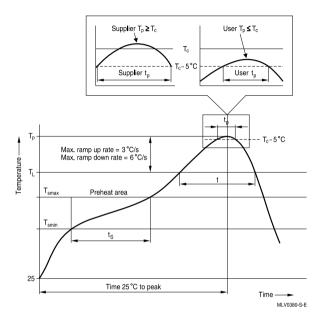
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## **SMD**

## Reflow soldering

Recommended temperature characteristic for reflow soldering following JEDEC J-STD-020D



Profile feature		Sn-Pb eutectic assembly	Pb-free assembly	
Preheat and soak				
- Temperature min	T <sub>smin</sub>	100 °C	150 °C	
- Temperature max	T <sub>smax</sub>	150 °C	200 °C	
- Time	$t_{smin}$ to $t_{smax}$	60 120 s	60 180 s	
Average ramp-up rate	$T_{smax}$ to $T_p$	3 °C/ s max.	3 °C/ s max.	
Liquidous temperature	TL	183 °C	217 °C	
Time at liquidous	t∟	60 150 s	60 150 s	
Peak package body temperature	$T_p^{-1)}$	220 °C 235 °C <sup>2)</sup>	245 °C 260 °C <sup>2)</sup>	
Time (t <sub>P</sub> ) <sup>3)</sup> within 5 °C of specified		20 s <sup>3)</sup>	30 s <sup>3)</sup>	
classification temperature (T <sub>c</sub> )	cation temperature (T <sub>c</sub> )		30 S <sup>9</sup> /	
Average ramp-down rate	T <sub>p</sub> to T <sub>smax</sub>	6 °C/ s max.	6 °C/ s max.	
Time 25 °C to peak temperature		maximum 6 min	maximum 8 min	

<sup>1)</sup> Tolerance for peak profile temperature (T<sub>P</sub>) is defined as a supplier minimum and a user maximum.

Note: All temperatures refer to topside of the package, measured on the package body surface. Number of reflow cycles: 3

<sup>2)</sup> Depending on package thickness. For details please refer to JEDEC J-STD-020D.

<sup>3)</sup> Tolerance for time at peak profile temperature (t<sub>P</sub>) is defined as a supplier minimum and a user maximum.



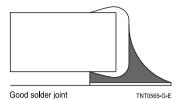
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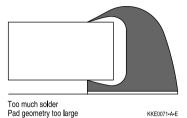
SMD NTC thermistors, case size 0402 (1005)

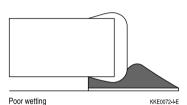
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## **SMD**

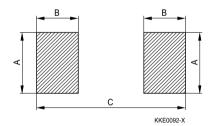
Solder joint profiles for silver/nickel/tin terminations







## 1.1.3 Recommended geometry of solder pads



## Recommended maximum dimensions (mm)

Case size inch/mm	A	В	С
0402/1005	0.6	0.6	1.7
0603/1608	1.0	1.0	3.0
0805/2012	1.3	1.2	3.4
1206/3216	1.8	1.2	4.5

## 1.1.4 Notes

Iron soldering should be avoided, hot air methods are recommended for repair purposes.



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#### **SMD**

#### 2 Conductive adhesion

An alternative to soldering is the gluing of thermistors with conductive adhesives. The benefit of this method is that it involves no thermal stress. The adhesives used must be chemically inert.

## 3 Clamp contacting

Pressure contacting by means of clamps is particularly suitable for applications involving frequent switching and high turn-on powers.

## 4 Sealing and potting

When thermistors are sealed, potted or overmolded, there must be no mechanical stress caused by thermal expansion during the production process (curing / overmolding process) and during later operation. The upper category temperature of the thermistor must not be exceeded. Ensure that the materials used (sealing / potting compound and plastic material) are chemically neutral.

## 5 Cleaning

If cleaning is necessary, mild cleaning agents such as ethyl alcohol and cleaning gasoline are recommended. Cleaning agents based on water are not allowed. Ultrasonic cleaning methods are permissible.

#### 6 Storage

In order to maintain their solderability, thermistors must be stored in a non-corrosive atmosphere. Humidity, temperature and container materials are critical factors.

Do not store SMDs where they are exposed to heat or direct sunlight. Otherwise, the packing material may be deformed or SMDs may stick together, causing problems during mounting. After opening the factory seals, such as polyvinyl-sealed packages, use the SMDs as soon as possible.

The components should be left in the original packing. Touching the metallization of unsoldered thermistors may change their soldering properties.

Storage temperature: 25 °C up to 45 °C

Relative humidity (without condensation): £75% annual mean

<95%, maximum 30 days per annum

Solder the thermistors listed in this data book after shipment from EPCOS within the time specified:

SMDs: 12 months for Ni-barrier termination

6 months for AqPd termination



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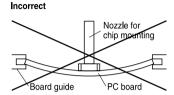
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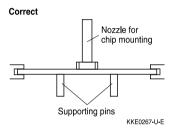
## **SMD**

#### 7 Placement and orientation of SMD NTC thermistors on PCB

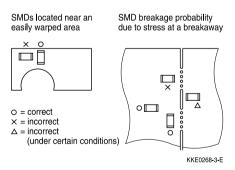
## a) Component placement



It is recommended that the PC board should be held by means of some adequate supporting pins such as shown left to prevent the SMDs from being damaged or cracked.

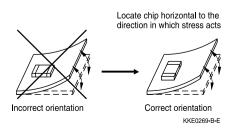


## b) Cracks



When placing a component near an area which is apt to bend or a grid groove on the PC board, it is advisable to have both electrodes subjected to uniform stress, or to position the component's electrodes at right angles to the grid groove or bending line (see c) Component orientation).

## c) Component orientation



Choose a mounting position that minimizes the stress imposed on the chip during flexing or bending of the board.



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## **SMD**

Cautions and warnings

#### General

See "Important notes".

## Storage

- Store thermistors only in original packaging. Do not open the package prior to storage.
- Storage conditions in original packaging: storage temperature 25 °C ... +45 °C, relative humidity £75% annual mean, <95% maximum 30 days per annum, dew precipitation is inadmissible.</p>
- Do not store thermistors where they are exposed to heat or direct sunlight. Otherwise, the packing material may be deformed or components may stick together, causing problems during mounting.
- Avoid contamination of thermistor surface during storage, handling and processing.
- Avoid storage of thermistors in harmful environments like corrosive gases (SO<sub>x</sub>, Cl etc).
- Use the components as soon as possible after opening the factory seals, i.e. the polyvinyl-sealed packages.
- Solder SMD NTC thermistors with nickel barrier termination within 12 months after shipment from EPCOS

## Handling

- NTC thermistors must not be dropped. Chip-offs or any other damage must not be caused during handling of NTCs.
- Do not touch components with bare hands. Gloves are recommended.
- Avoid contamination of thermistor surface during handling.
- Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.

## Soldering

- Use resin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended.
- Complete removal of flux is recommended.



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SMD NTC thermistors, case size 0402 (1005)

Automotive series

## **SMD**

#### Mounting

- Ensure that no thermo-mechanical stress occurs due to production processes (curing or overmolding processes) when thermistors are sealed, potted or overmolded or during their subsequent operation. The maximum temperature of the thermistor must not be exceeded. Ensure that the materials used (sealing/potting compound and plastic material) are chemically neutral.
- Electrodes/contacts must not be scratched or damaged before/during/after the mounting process.
- Contacts and housing used for assembly with the thermistor must be clean before mounting.
- Ensure that adjacent materials are designed for operation at temperatures comparable to the surface temperature of the thermistor. Be sure that surrounding parts and materials can withstand the temperature.
- Avoid contamination of the thermistor surface during processing.
- The connections of sensors (e.g. cable end, wire end, plug terminal) may only be exposed to an environment with normal atmospheric conditions.
- Avoid using chemical substances as mounting aids. It must be ensured that no water or other liquids enter the NTC thermistors (e.g. through plug terminals). In particular, water based substances (e.g. soap suds) must not be used as mounting aids for sensors.

#### Operation

- Use thermistors only within the specified operating temperature range.
- Use thermistors only within the specified power range.
- Environmental conditions must not harm the thermistors. Only use the thermistors under normal atmospheric conditions or within the specified conditions.
- Contact of NTC thermistors with any liquids and solvents should be prevented. It must be ensured that no water enters the NTC thermistors (e.g. through plug terminals). For measurement purposes (checking the specified resistance vs. temperature), the component must not be immersed in water but in suitable liquids (e.g. Galden).
- Avoid dewing and condensation unless thermistor is specified for these conditions.
- Be sure to provide an appropriate fail-safe function to prevent secondary product damage caused by malfunction.

This listing does not claim to be complete, but merely reflects the experience of EPCOS AG.



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## **SMD**

## Symbols and terms

Symbol	English	German
Α	Area	Fläche
B B <sub>25/100</sub>	B value B value determined by resistance measurement at 25 °C and 100 °C	B-Wert B-Wert, ermittelt durch Widerstands- messungen bei 25 °C und 100 °C
$C_{th}$	Heat capacitance	Wärmekapazität
I	Current	Strom
N	Number (integer)	Anzahl (ganzzahliger Wert)
$\begin{aligned} P_{25} \\ P_{diss} \\ P_{el} \\ P_{max} \end{aligned}$	Maximum power at 25 °C Power dissipation Electrical power Maximum power within stated temperature range	Maximale Leistung bei 25 °C Verlustleistung Elektrische Leistung Maximale Leistung im angegebenenTemperaturbereich
$\begin{array}{l} DR_{\text{B}}/R_{\text{B}} \\ \\ R_{\text{ins}} \\ R_{\text{P}} \\ \\ R_{\text{R}} \\ DR_{\text{R}}/R_{\text{R}} \\ \\ R_{\text{S}} \\ \\ R_{\text{T}} \end{array}$	Resistance tolerance caused by spread of B value Insulation resistance Parallel resistance Rated resistance Resistance tolerance Series resistance Resistance at temperature T (e.g. R <sub>25</sub> = resistance at 25 °C)	Widerstandstoleranz, die durch die Streuung des B-Wertes verursacht wird Isolationswiderstand Parallelwiderstand Nennwiderstand Widerstandstoleranz Serienwiderstand Widerstand bei Temperatur T (z.B. R <sub>25</sub> = Widerstand bei 25 °C)
$T \\ DT \\ t \\ T_A \\ T_{max} \\ T_{min}$	Temperature Temperature tolerance Time Ambient temperature Upper category temperature Lower category temperature	Temperatur Temperaturtoleranz Zeit Umgebungstemperatur Obere Grenztemperatur (Kategorietemperatur) Untere Grenztemperatur (Kategorietemperatur)
$T_{op}$ $T_{R}$ $T_{surf}$	Operating temperature Rated temperature Surface temperature	Betriebstemperatur Nenntemperatur Oberflächentemperatur
$V$ $V_{ins}$ $V_{op}$ $V_{test}$	Voltage Insulation test voltage Operating voltage Test voltage	Spannung Isolationsprüfspannung Betriebsspannung Prüfspannung



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Symbol	English	German
а	Temperature coefficient	Temperaturkoeffizient
D	Tolerance, change	Toleranz, Änderung
$d_{\text{th}}$	Dissipation factor	Wärmeleitwert
t <sub>c</sub> t <sub>a</sub>	Thermal cooling time constant Thermal time constant	Thermische Abkühlzeitkonstante Thermische Zeitkonstante

## Abbreviations / Notes

Symbol	English	German
SMD	Surface-mounted devices	Oberflächenmontierbares Bauelement
*	To be replaced by a number in ordering codes, type designations etc.	Platzhalter für Zahl im Bestellnummern- code oder für die Typenbezeichnung.
+	To be replaced by a letter.	Platzhalter für einen Buchstaben.
	All dimensions are given in mm.	Alle Maße sind in mm angegeben.
	The commas used in numerical values denote decimal points.	Verwendete Kommas in Zahlenwerten bezeichnen Dezimalpunkte.

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