

140 COMMERCE DRIVE MONTGOMERYVILLE, PA 18936-1013

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### **MSC1175M**

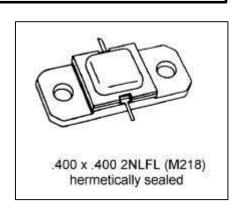
# RF & MICROWAVE TRANSISTORS AVIONICS APPLICATIONS

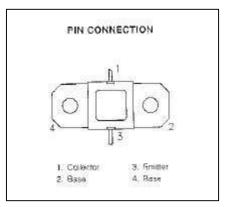
### **Features**

- 1025 1150 MHz
- 50 VOLTS
- INTERNAL INPUT/OUTPUT MATCHING
- P<sub>OUT</sub> = 175 WATTS
- G<sub>P</sub> = 7.7 dB MINIMUM
- COMMON BASE CONFIGURATION



The MSC1175M is a NPN bipolar transistor specifically designed for high peak pulse power applications such as DME/TACAN. This device is capable of withstanding a minimum 20:1 load VSWR at any phase angle under full rated conditions. Internal impedance matching provides consistent broadband performance.





# ABSOLUTE MAXIMUM RATINGS (Tcase = $25^{\circ}$ C)

Symbol	Parameter	Value	Unit
P <sub>DISS</sub>	Power Dissipation	400	W
Ic	Device Current	12	Α
V <sub>cc</sub>	Collector-Supply Voltage*	55	V
TJ	Junction Temperature	250	°C
T <sub>STG</sub>	Storage Temperature	-65 to +200	°C

## Thermal Data

R <sub>TH(J-C)</sub>	Thermal Resistance Junction-case	0.3	°C/W
••IH(J-C)	Thomas Hoolotanoo Ganotion Gaoo	0.0	<b>O</b> / <b>11</b>



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# ELECTRICAL SPECIFICATIONS (Tcase = $25^{\circ}$ C)

# STATIC

Cumbal	Test Conditions			Value		
Symbol			Min.	Тур.	Max.	Unit
BV <sub>CBO</sub>	I <sub>C</sub> = 10 mA	I <sub>E</sub> = 0 mA	65			V
BV <sub>EBO</sub>	I <sub>E</sub> = 1 mA	I <sub>C</sub> = 0 mA	3.5			V
BV <sub>CER</sub>	I <sub>C</sub> = 15 mA	$R_{BE} = 10 \Omega$	65			V
I <sub>CES</sub>	V <sub>CE</sub> = 50 V				12.5	mA
h <sub>FE</sub>	V <sub>CE</sub> = 5 V	I <sub>C</sub> = 1 A	15		120	

### **DYNAMIC**

Symbol	Test Conditions		Value			Unit	
Symbol rest Conditions		Min.	Тур.	Max.	Oilit		
P <sub>OUT</sub>	f = 1025 - 1150 MHz	P <sub>IN</sub> = 30 W	V <sub>CC</sub> = 50 V	175	190		W
ης	f = 1025 - 1150 MHz	$P_{IN} = 30 \text{ W}$	$V_{CC} = 50 \text{ V}$	40	42		%
G <sub>P</sub>	f = 1025 - 1150 MHz	P <sub>IN</sub> = 30 W	V <sub>CC</sub> = 50 V	7.7	8.0		dB
Conditions	Pulse Width = 10μS	Duty Cycle = 1%	, <b>5</b>				



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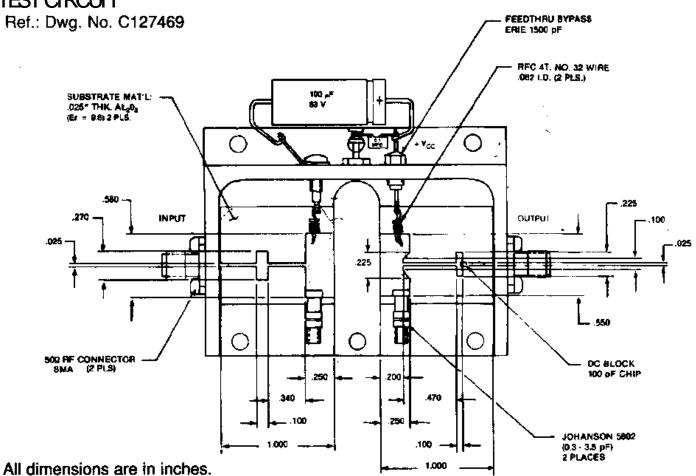
### **IMPEDANCE DATA**

FREQ	<b>Z</b> <sub>IN</sub> (Ω)	$Z_{CL}(\Omega)$
1025 MHz	2.3 + j5.1	2.4 - j4.2
1090 MHz	2.0 + j4.5	2.0 - j3.5
1150 MHz	2.2 + j3.3	2.5 - j2.5

 $V_{CC} = 50V$  $P_{IN} = 30W$ 

Normalized to  $50\Omega$ 

# **TEST CIRCUIT**







# PACKAGE MECHANI CAL DATA

