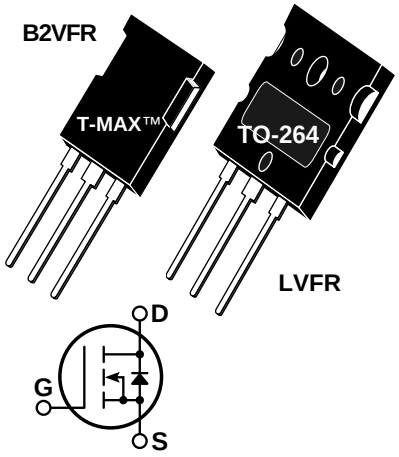
APT10040B2VFR
APT10040LVFR
1000V 25A 0.400Ω

POWER MOS V®

FREDFET



Power MOS V® is a new generation of high voltage N-Channel enhancement mode power MOSFETs. This new technology minimizes the JFET effect, increases packing density and reduces the on-resistance. Power MOS V® also achieves faster switching speeds through optimized gate layout.

- **Identical Specifications: T-MAX™ or TO-264 Package**
- **Lower Leakage**
- **Faster Switching**
- **Fast Recovery Body Diode**

MAXIMUM RATINGS All Ratings: T_C = 25℃ unless otherwise specified.

Symbol	Parameter	APT10040	UNIT
V_{DSS}	Drain-Source Voltage	1000	Volts
I_D	Continuous Drain Current @ T_C = 25℃	25	Amps
I_{DM}	Pulsed Drain Current ①	100	
V_{GS}	Gate-Source Voltage Continuous	±30	Volts
V_{GSM}	Gate-Source Voltage Transient	±40	
P_D	Total Power Dissipation @ T_C = 25℃	625	Watts
	Linear Derating Factor	5.0	W/℃
T_J,T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	℃
T_L	Lead Temperature: 0.063" from Case for 10 Sec.	300	
I_{AR}	Avalanche Current ① (Repetitive and Non-Repetitive)	33	Amps
E_{AR}	Repetitive Avalanche Energy ①	50	mJ
E_{AS}	Single Pulse Avalanche Energy ④	3000	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage (V_{GS} = 0V, I_D = 250μA)	1000			Volts
$I_{D(on)}$	On State Drain Current ② (V_{DS} > $I_{D(on)}$ x $R_{DS(on)}$ Max, V_{GS} = 10V)	25			Amps
$R_{DS(on)}$	Drain-Source On-State Resistance ② (V_{GS} = 10V, 0.5 $I_{D[Cont.]}$)			0.40	Ohms
I_{DSS}	Zero Gate Voltage Drain Current (V_{DS} = V_{DSS}, V_{GS} = 0V)			250	μA
	Zero Gate Voltage Drain Current (V_{DS} = 0.8 V_{DSS}, V_{GS} = 0V, T_C = 125℃)			1000	
I_{GSS}	Gate-Source Leakage Current (V_{GS} = ±30V, V_{DS} = 0V)			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage (V_{DS} = V_{GS}, I_D = 2.5mA)	2		4	Volts

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

APT Website - http://www.advancedpower.com

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DYNAMIC CHARACTERISTICS

APT10040 B2VFR - LVFR

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{iss}	Input Capacitance	V_{GS} = 0V		7830	9400	pF
C_{oss}	Output Capacitance	V_{DS} = 25V		715	1010	
C_{rss}	Reverse Transfer Capacitance	f = 1 MHz		386	580	
Q_g	Total Gate Charge ③	V_{GS} = 10V		415	630	nC
Q_{gs}	Gate-Source Charge	V_{DD} = 0.5 V_{DSS}		37	45	
Q_{gd}	Gate-Drain ("Miller") Charge	I_D = I_D [Cont.] @ 25℃		216	330	
$t_d(on)$	Turn-on Delay Time	V_{GS} = 15V		13	26	ns
t_r	Rise Time	V_{DD} = 0.5 V_{DSS}		13	26	
$t_d(off)$	Turn-off Delay Time	I_D = I_D [Cont.] @ 25℃		57	86	
t_f	Fall Time	R_G = 0.6Ω		9	20	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic / Test Conditions		MIN	TYP	MAX	UNIT
I_S	Continuous Source Current (Body Diode)				25	Amps
I_{SM}	Pulsed Source Current ① (Body Diode)				100	
V_{SD}	Diode Forward Voltage ② (V_{GS} = 0V, I_S = -I_D [Cont.])				1.3	Volts
$^{dv}/_{dt}$	Peak Diode Recovery $^{dv}/_{dt}$ ⑤				18	V/ns
t_{rr}	Reverse Recovery Time	T_j = 25℃			320	ns
	(I_S = -I_D [Cont.], $^{di}/_{dt}$ = 100A/µs)	T_j = 125℃			650	
Q_{rr}	Reverse Recovery Charge	T_j = 25℃		3.1		µC
	(I_S = -I_D [Cont.], $^{di}/_{dt}$ = 100A/µs)	T_j = 125℃		8.7		
I_{RRM}	Peak Recovery Current	T_j = 25℃		15.8		Amps
	(I_S = -I_D [Cont.], $^{di}/_{dt}$ = 100A/µs)	T_j = 125℃		24.8		

THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			0.20	℃/W
$R_{\theta JA}$	Junction to Ambient			40	

① Repetitive Rating: Pulse width limited by maximum junction temperature.

② Pulse Test: Pulse width < 380 µS, Duty Cycle < 2%

③ See MIL-STD-750 Method 3471

④ Starting T_j = +25℃, L = 9.60mH, R_G = 25Ω, Peak I_L = 25A

⑤ $I_S \leq -I_D$ [Cont.], $^{di}/_{dt}$ = 700A/µs, $T_j \leq 150$℃, R_G = 2.0Ω, V_R = 200V.

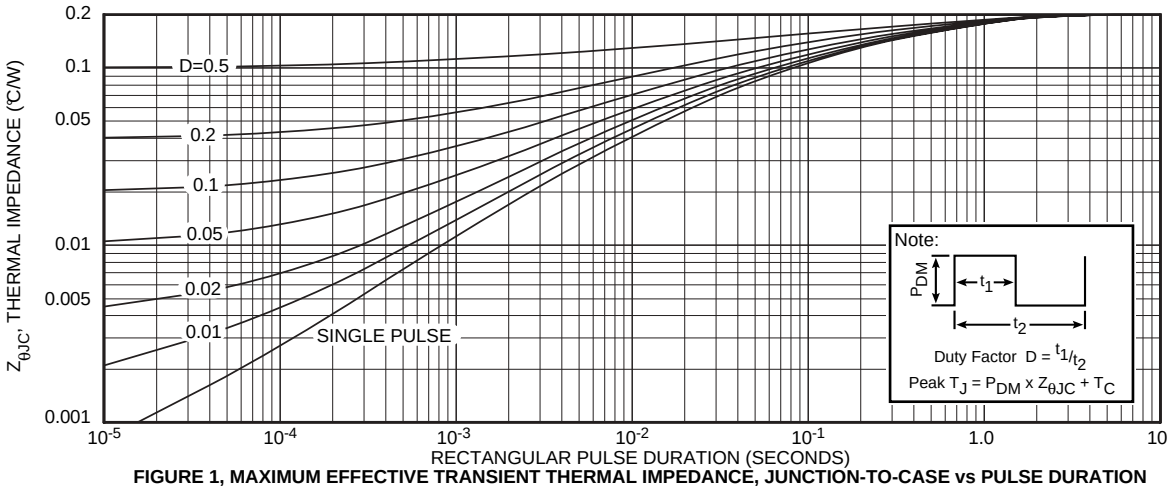


FIGURE 1, MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs PULSE DURATION

Typical Preformance Curves

Graph Deleted

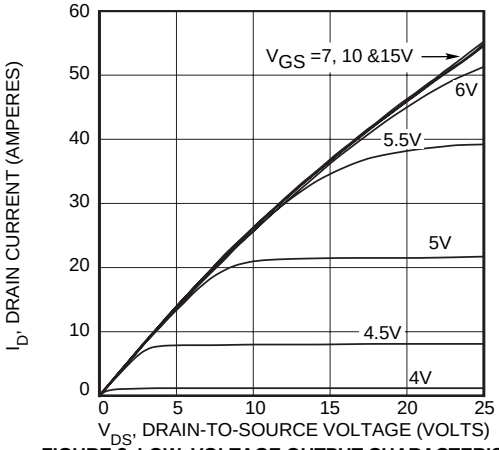


FIGURE 3, LOW VOLTAGE OUTPUT CHARACTERISTICS

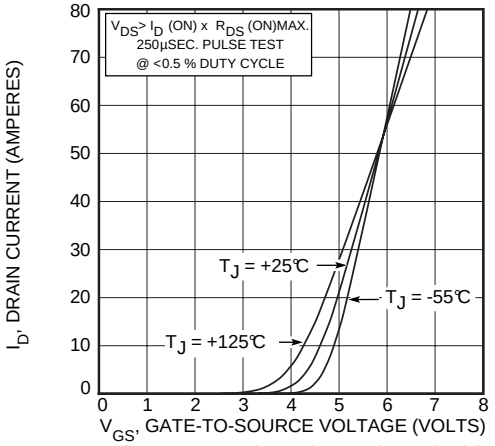


FIGURE 4, TRANSFER CHARACTERISTICS

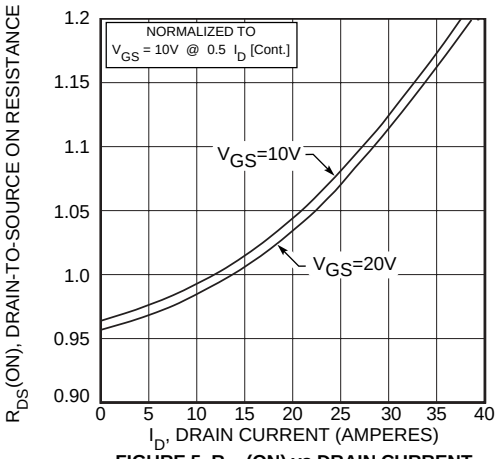


FIGURE 5, R_{DS}(ON) vs DRAIN CURRENT

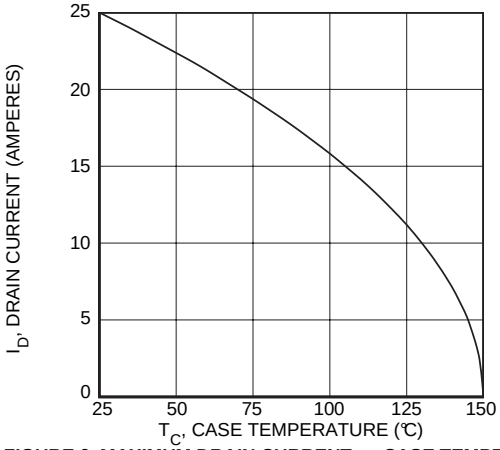


FIGURE 6, MAXIMUM DRAIN CURRENT vs CASE TEMPERATURE

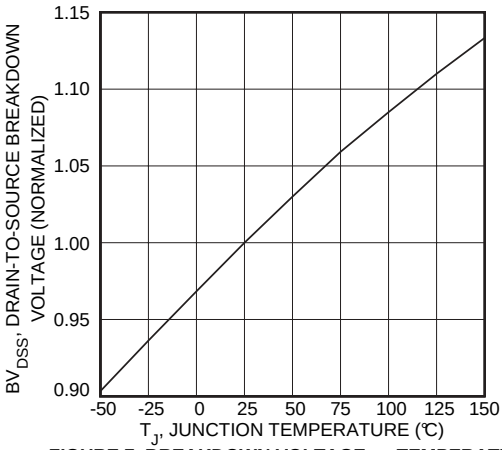


FIGURE 7, BREAKDOWN VOLTAGE vs TEMPERATURE

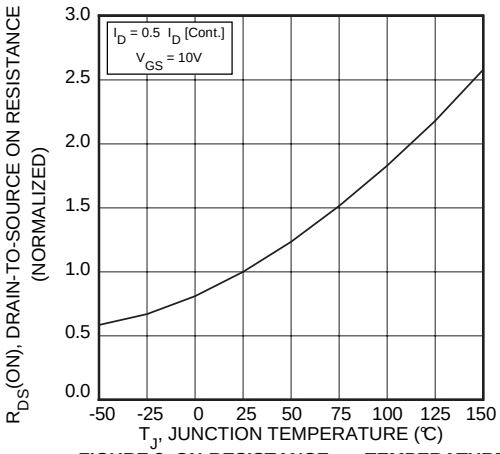


FIGURE 8, ON-RESISTANCE vs. TEMPERATURE

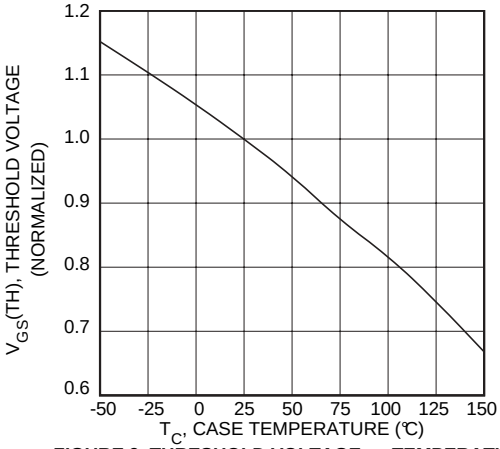


FIGURE 9, THRESHOLD VOLTAGE vs TEMPERATURE

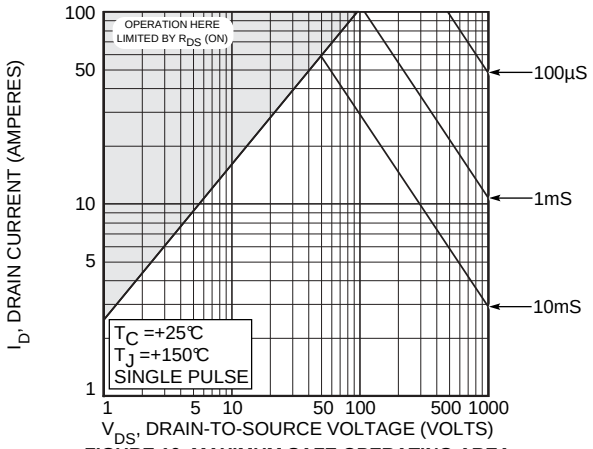


FIGURE 10, MAXIMUM SAFE OPERATING AREA

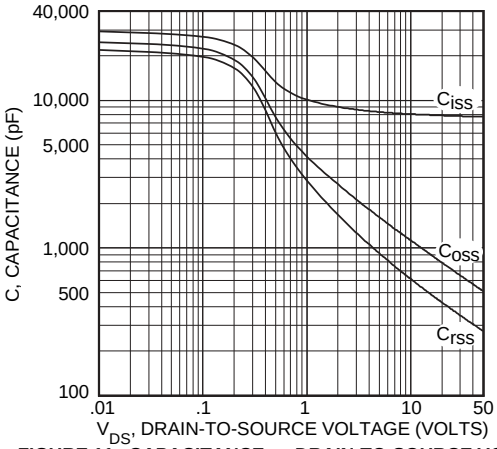


FIGURE 11, CAPACITANCE vs DRAIN-TO-SOURCE VOLTAGE

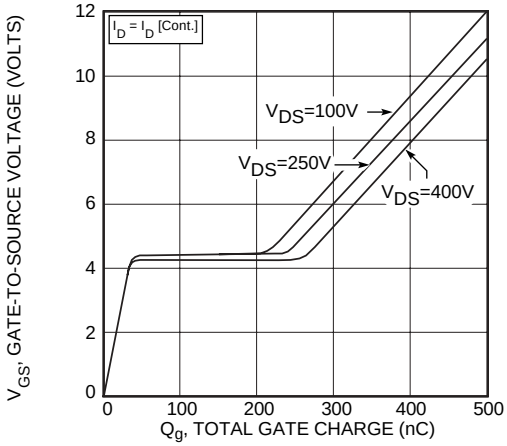


FIGURE 12, GATE CHARGE vs GATE-TO-SOURCE VOLTAGE

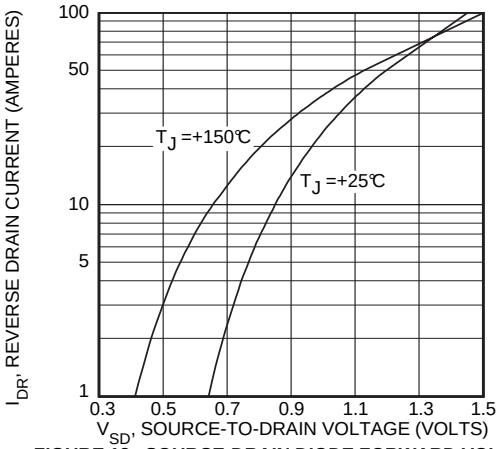
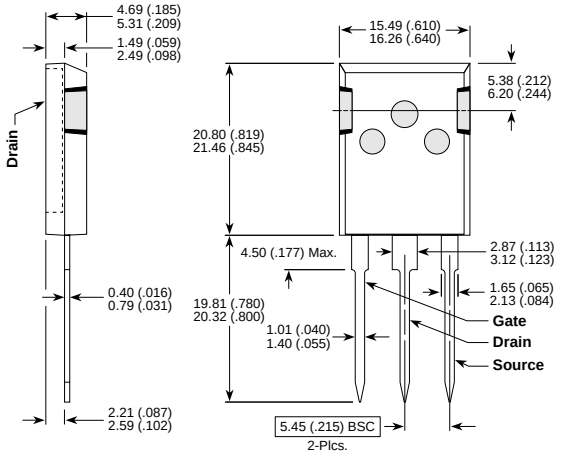


FIGURE 13, SOURCE-DRAIN DIODE FORWARD VOLTAGE

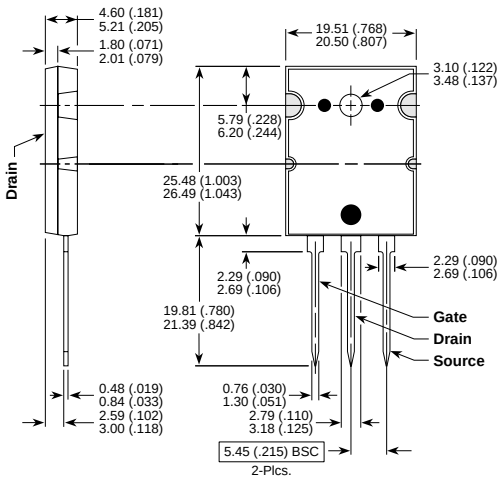
T-MAX™ (B2) Package Outline



These dimensions are equal to the TO-247 without the mounting hole.

Dimensions in Millimeters and (Inches)

TO-264 (L) Package Outline



Dimensions in Millimeters and (Inches)