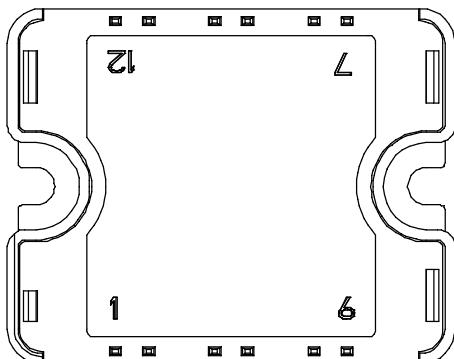
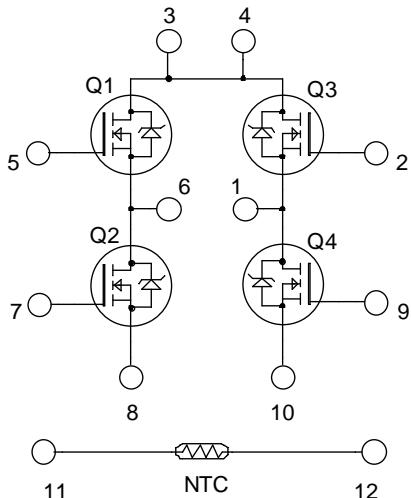


**Full - Bridge  
MOSFET Power Module**

$V_{DSS} = 500V$   
 $R_{DSon} = 130m\Omega$  typ @  $T_j = 25^\circ C$   
 $I_D = 25A$  @  $T_c = 25^\circ C$



Pins 3/4 must be shorted together

**Application**

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

**Features**

- Power MOS 8™ FREDFETs
  - Low  $R_{DSon}$
  - Low input and Miller capacitance
  - Low gate charge
  - Fast intrinsic reverse diode
  - Avalanche energy rated
  - Very rugged
- Very low stray inductance
  - Symmetrical design
- Internal thermistor for temperature monitoring
- High level of integration

**Benefits**

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- Each leg can be easily paralleled to achieve a phase leg of twice the current capability
- RoHS Compliant

**Absolute maximum ratings**

Symbol	Parameter	Max ratings	Unit
$V_{DSS}$	Drain - Source Breakdown Voltage	500	V
$I_D$	Continuous Drain Current	$T_c = 25^\circ C$	A
		$T_c = 80^\circ C$	
$I_{DM}$	Pulsed Drain current	135	
$V_{GS}$	Gate - Source Voltage	$\pm 30$	V
$R_{DSon}$	Drain - Source ON Resistance	156	$m\Omega$
$P_D$	Maximum Power Dissipation	$T_c = 25^\circ C$	W
$I_{AR}$	Avalanche current (repetitive and non repetitive)	21	A

 **CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on [www.microsemi.com](http://www.microsemi.com)

All ratings @  $T_j = 25^\circ\text{C}$  unless otherwise specified

**Electrical Characteristics**

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 500V V <sub>GS</sub> = 0V	T <sub>j</sub> = 25°C T <sub>j</sub> = 125°C			250	μA	
R <sub>DS(on)</sub>	Drain – Source on Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 21A			130	156	μΩ	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 1mA		3	4	5	V	
I <sub>GSS</sub>	Gate – Source Leakage Current	V <sub>GS</sub> = ±30 V				±100	nA	

**Dynamic Characteristics**

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V V <sub>DS</sub> = 25V f = 1MHz		5448			pF
C <sub>oss</sub>	Output Capacitance			735			
C <sub>rss</sub>	Reverse Transfer Capacitance			72			
Q <sub>g</sub>	Total gate Charge	V <sub>GS</sub> = 10V V <sub>Bus</sub> = 250V I <sub>D</sub> = 21A		170			nC
Q <sub>gs</sub>	Gate – Source Charge			38			
Q <sub>gd</sub>	Gate – Drain Charge			80			
T <sub>d(on)</sub>	Turn-on Delay Time	Resistive switching @ 25°C V <sub>GS</sub> = 15V V <sub>Bus</sub> = 333V I <sub>D</sub> = 21A R <sub>G</sub> = 4.7Ω		29			ns
T <sub>r</sub>	Rise Time			35			
T <sub>d(off)</sub>	Turn-off Delay Time			80			
T <sub>f</sub>	Fall Time			26			

**Source - Drain diode ratings and characteristics**

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
I <sub>S</sub>	Continuous Source current (Body diode)		T <sub>c</sub> = 25°C			25	A
			T <sub>c</sub> = 80°C			19	
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = - 21A				1	V
dv/dt	Peak Diode Recovery ①					30	V/ns
t <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> = - 21A V <sub>R</sub> = 100V dI <sub>S</sub> /dt = 100A/μs	T <sub>j</sub> = 25°C			215	ns
			T <sub>j</sub> = 125°C			370	
Q <sub>rr</sub>	Reverse Recovery Charge		T <sub>j</sub> = 25°C		0.90		μC
			T <sub>j</sub> = 125°C		2.6		

① dv/dt numbers reflect the limitations of the circuit rather than the device itself.

 I<sub>S</sub> ≤ - 21A    di/dt ≤ 1000A/μs    V<sub>DD</sub> ≤ 333V    T<sub>j</sub> ≤ 125°C

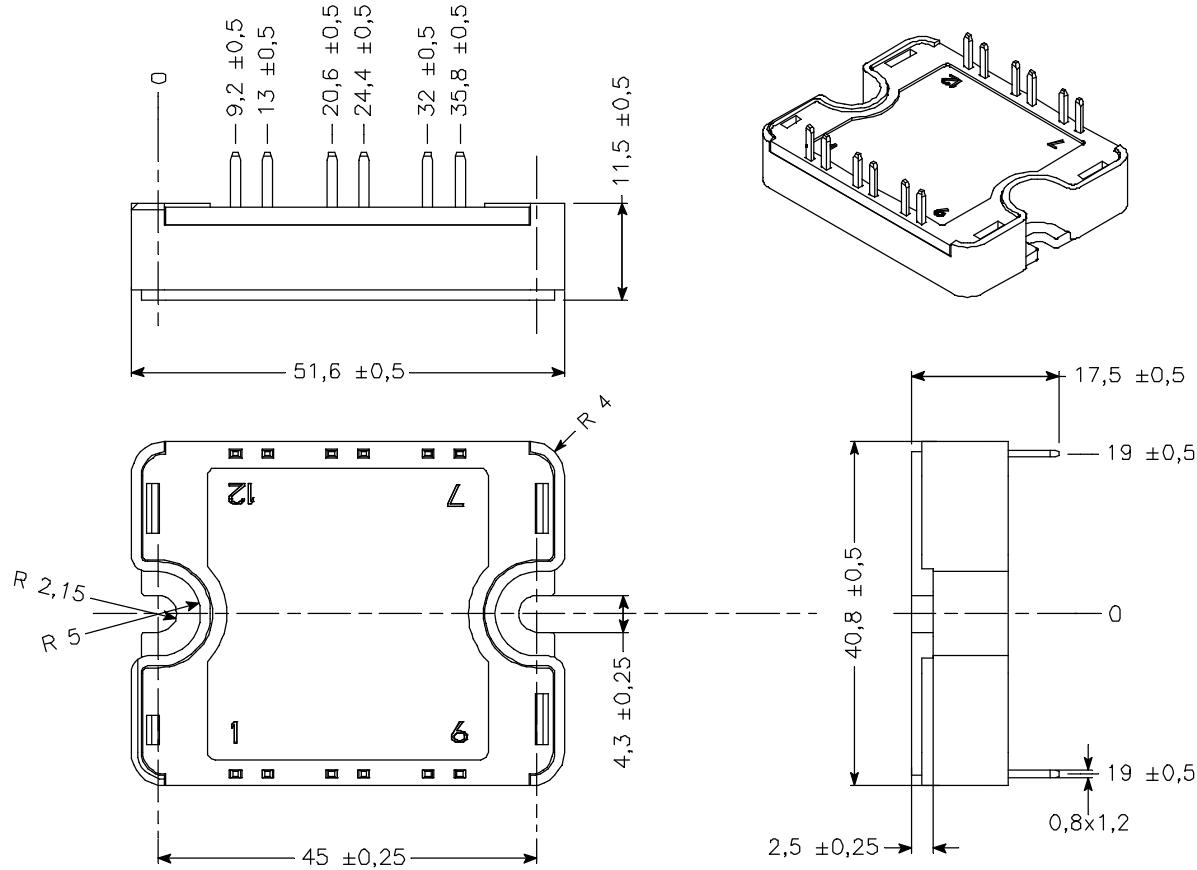
**Thermal and package characteristics**

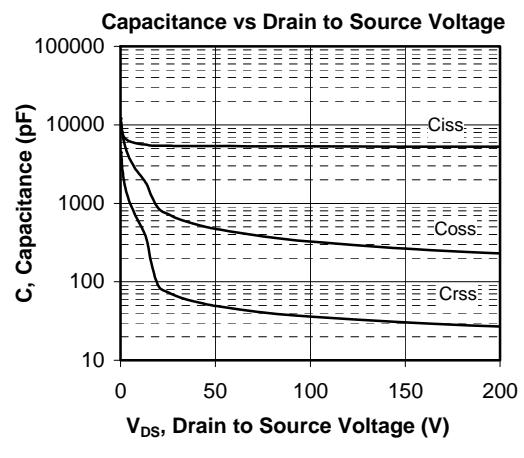
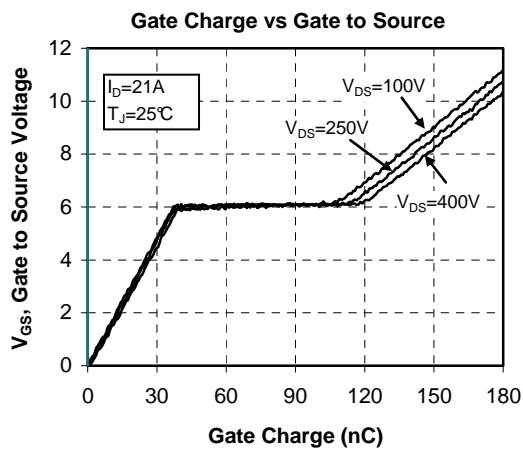
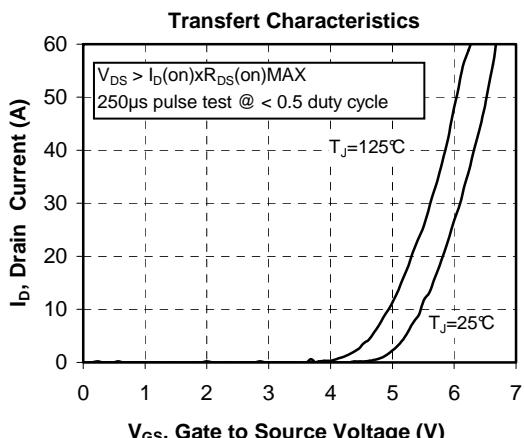
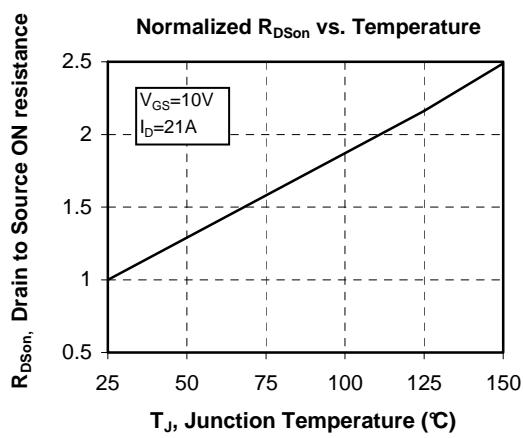
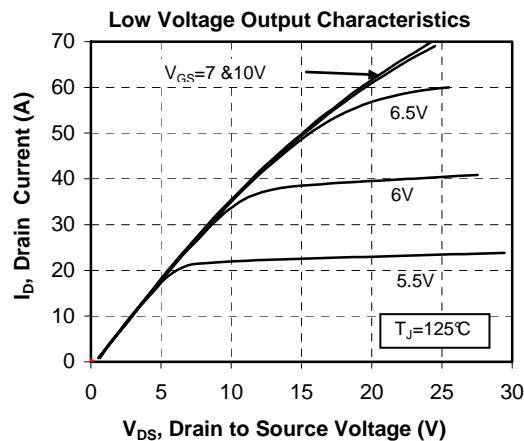
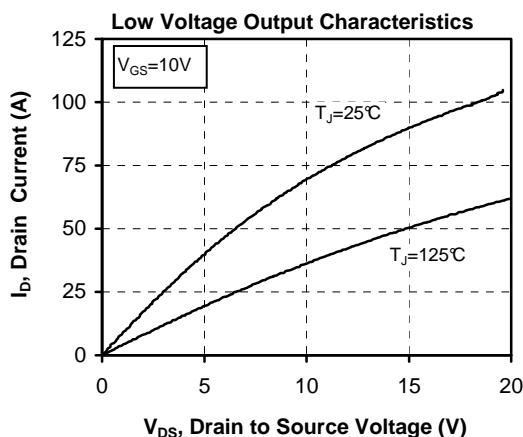
Symbol	Characteristic		Min	Typ	Max	Unit
R <sub>thJC</sub>	Junction to Case Thermal Resistance			0.6		°C/W
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t = 1 min, I <sub>isol</sub> <1mA, 50/60Hz		4000			V
T <sub>J</sub>	Operating junction temperature range		-40		150	
T <sub>STG</sub>	Storage Temperature Range		-40		125	°C
T <sub>C</sub>	Operating Case Temperature		-40		100	
Torque	Mounting torque	To heatsink	M4	2.5	4.7	N.m
Wt	Package Weight				80	g

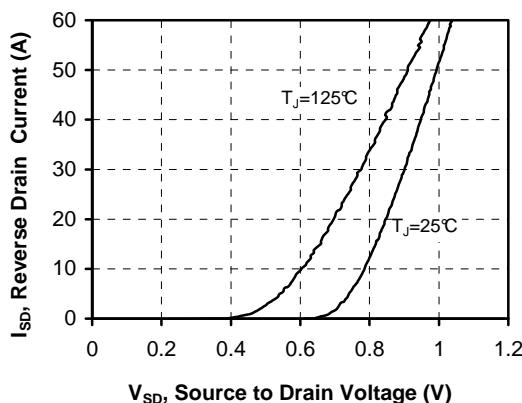
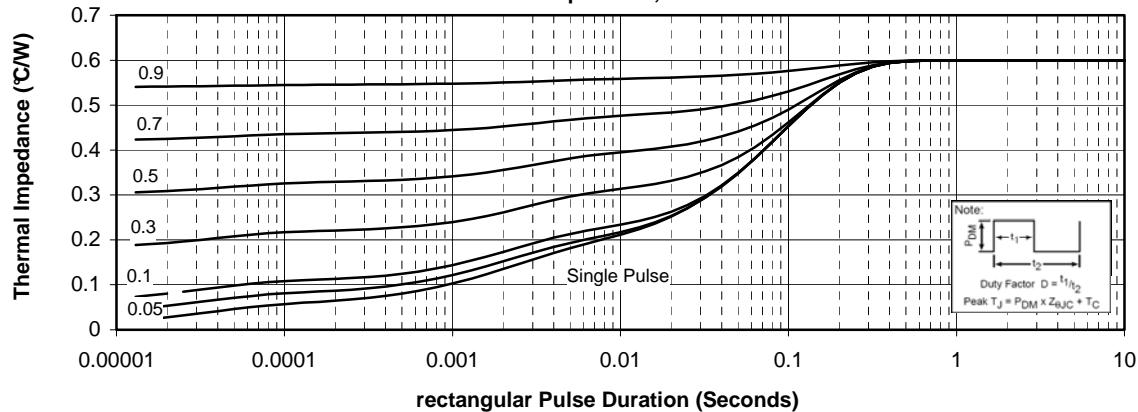
**Temperature sensor NTC** (see application note APT0406 on www.microsemi.com for more information).

Symbol	Characteristic		Min	Typ	Max	Unit
R <sub>25</sub>	Resistance @ 25°C			50		kΩ
ΔR <sub>25</sub> /R <sub>25</sub>				5		%
B <sub>25/85</sub>	T <sub>25</sub> = 298.15 K			3952		K
ΔB/B		T <sub>C</sub> =100°C		4		%

$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]} \quad T: \text{ Thermistor temperature} \\ R_T: \text{ Thermistor value at } T$$

**SP1 Package outline** (dimensions in mm)

See application note 1904 - Mounting Instructions for SP1 Power Modules on [www.microsemi.com](http://www.microsemi.com)

**Typical Performance Curve**


**Drain Current vs Source to Drain Voltage**

**Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration**


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