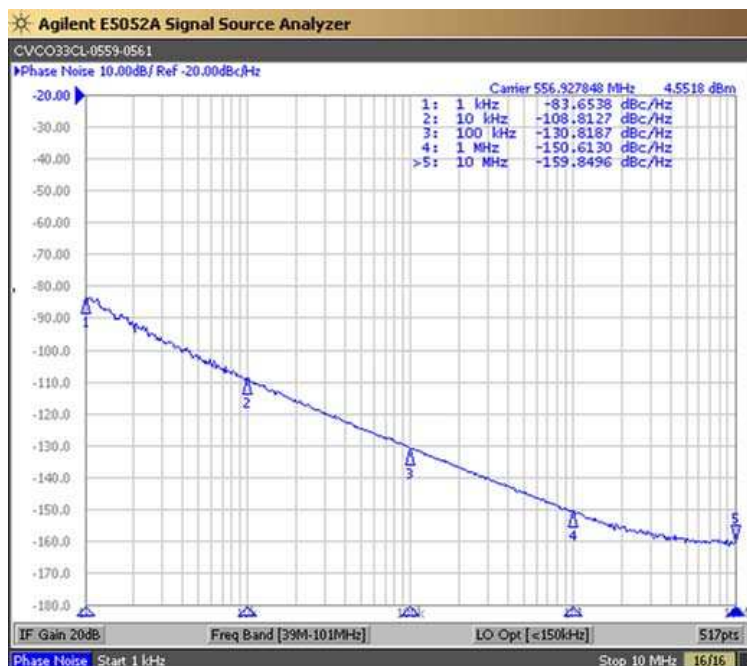


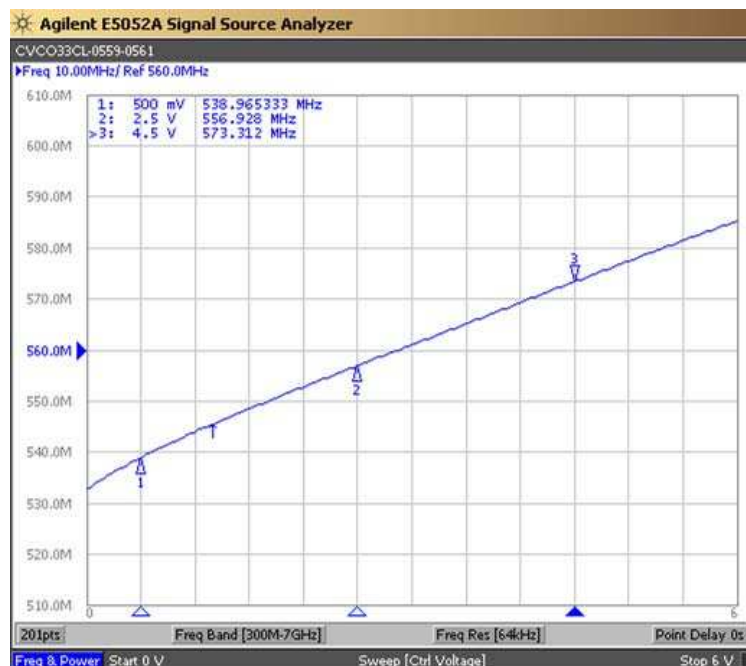


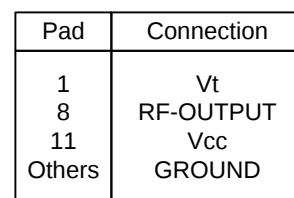
PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			559	MHz
Upper Frequency:	561			MHz
Tuning Voltage:	0.5		4.5	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	0	+2.0	+4.0	dBm
Supply Current:		15		mA
Harmonic Suppression (2 nd Harmonic):		-15		dBc
Pushing:		1.0		MHz/V
Pulling, all Phases:		3.0		MHz pk-pk
Tuning Sensitivity:		12		MHz/V
Phase Noise @ 10kHz offset:		-95		dBc/Hz
Phase Noise @ 100kHz offset:		-117		dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			100	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$

Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)





- Unless otherwise specified,
Dimensions are in: IN
(mm)
- Pad Location
Dimensions are in: Inches

Technical drawing of a rectangular plate with the following dimensions and features:

- Overall width: 0.630 ± 0.012 (16.00 $\pm$ 0.30)
- Overall height: 0.157 ± 0.004 (4.00 $\pm$ 0.10)
- Top edge features: A row of 15 small circles. The first 12 are labeled $0.059 \varnothing$ TYP (1.50 $\varnothing$ TYP). The last 3 are labeled 0.157 ± 0.004 (4.00 $\pm$ 0.10).
- Internal features: Five rectangular frames, each containing a small black circle. The distance between the centers of the first two frames is 0.472 (12.00).
- Direction of Feed: Indicated by an arrow pointing to the right.

Drawing not to scale

Crystek Part Number:	CVCO33CL-0559-0561	Release Date:	05-Apr-12
Revision Level:	D	Responsible:	C. Vales

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