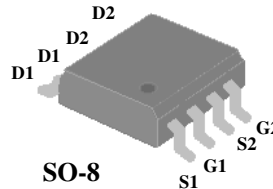




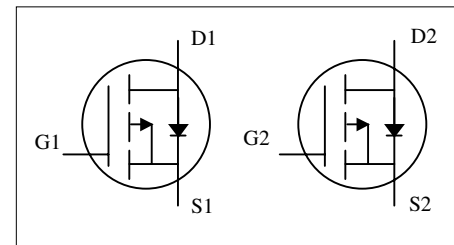
- ▼ Low On-Resistance
- ▼ Simple Drive Requirement
- ▼ Dual P MOSFET Package



$BV_{DSS}$	-30V
$R_{DS(ON)}$	26mΩ
$I_D$	-7.4A

### Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, ultra low on-resistance and cost-effectiveness.



### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	±20	V
$I_D@T_A=25^\circ C$	Continuous Drain Current <sup>3</sup>	-7.4	A
$I_D@T_A=70^\circ C$	Continuous Drain Current <sup>3</sup>	-5.9	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	-30	A
$P_D@T_A=25^\circ C$	Total Power Dissipation	2	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Value	Unit
$R_{thj-a}$	Maximum Thermal Resistance, Junction-ambient <sup>3</sup>	62.5	°C/W



# AP4957AGM

## Electrical Characteristics@T<sub>j</sub>=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-30	-	-	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-7A	-	-	26	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5A	-	-	36	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1	-	-3	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>D</sub> =-7A	-	7	-	S
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	-	-	-1	uA
	Drain-Source Leakage Current (T <sub>j</sub> =70°C)	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V	-	-	-25	uA
I <sub>GSS</sub>	Gate-Source Leakage	V <sub>GS</sub> =±20V	-	-	±100	nA
Q <sub>g</sub>	Total Gate Charge <sup>2</sup>	I <sub>D</sub> =-7A	-	16	26	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =-24V	-	2.8	-	nC
Q <sub>gd</sub>	Gate-Drain ("Miller") Charge	V <sub>GS</sub> =-4.5V	-	9.3	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time <sup>2</sup>	V <sub>DS</sub> =-15V	-	9	-	ns
t <sub>r</sub>	Rise Time	I <sub>D</sub> =-1A	-	6.5	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time	R <sub>G</sub> =3.3Ω, V <sub>GS</sub> =-10V	-	40	-	ns
t <sub>f</sub>	Fall Time	R <sub>D</sub> =15Ω	-	26	-	ns
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V	-	1215	1950	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =-25V	-	190	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f=1.0MHz	-	185	-	pF
R <sub>g</sub>	Gate Resistance	f=1.0MHz	-	5.3	8	Ω

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V <sub>SD</sub>	Forward On Voltage <sup>2</sup>	I <sub>S</sub> =-1.7A, V <sub>GS</sub> =0V	-	-	-1.2	V
t <sub>rr</sub>	Reverse Recovery Time <sup>2</sup>	I <sub>S</sub> =-7A, V <sub>GS</sub> =0V,	-	22	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/μs	-	14	-	nC

### Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board ; 135 °C/W when mounted on Min. copper pad.

THIS PRODUCT IS AN ELECTROSTATIC SENSITIVE, PLEASE HANDLE WITH CAUTION.

THIS PRODUCT HAS BEEN QUALIFIED FOR CONSUMER MARKET. APPLICATIONS OR USES AS CRITERIAL COMPONENT IN LIFE SUPPORT DEVICE OR SYSTEM ARE NOT AUTHORIZED.

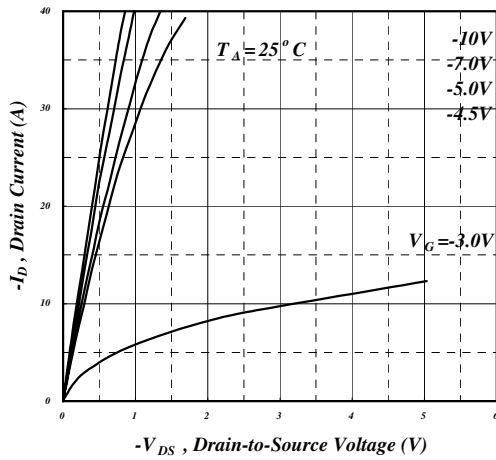


Fig 1. Typical Output Characteristics

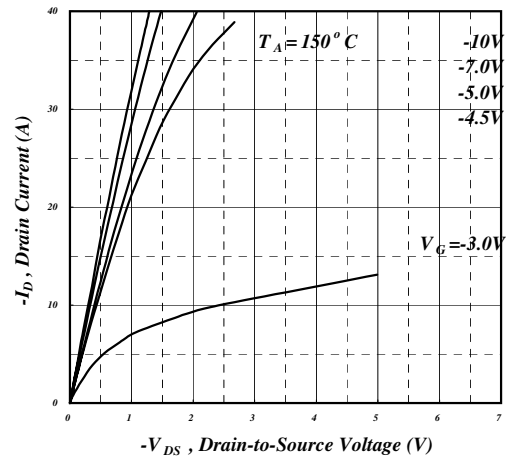


Fig 2. Typical Output Characteristics

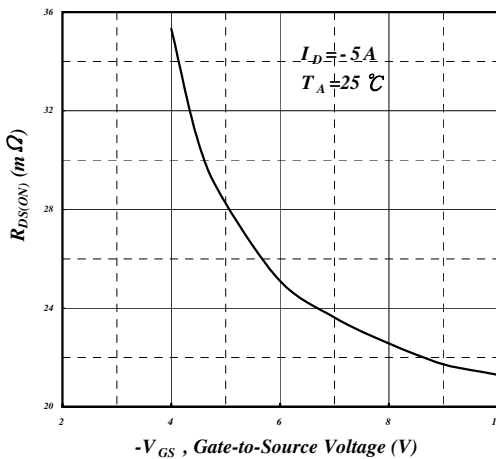


Fig 3. On-Resistance v.s. Gate Voltage

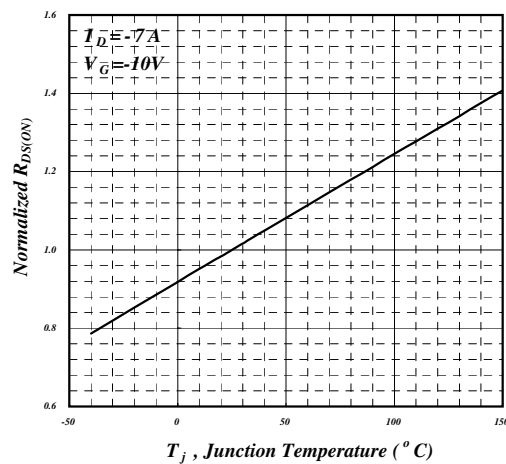


Fig 4. Normalized On-Resistance v.s. Junction Temperature

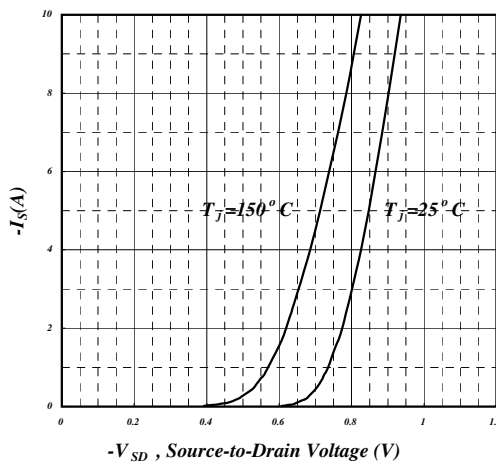


Fig 5. Forward Characteristic of Reverse Diode

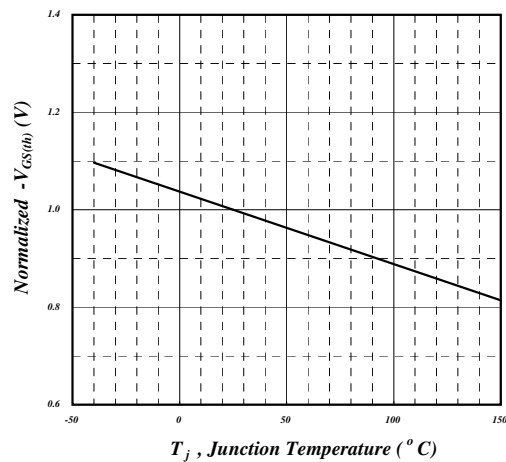


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

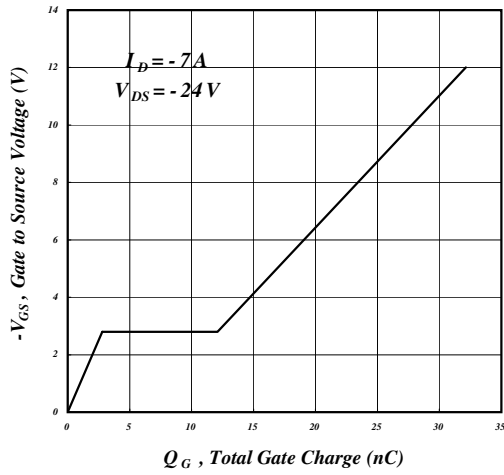


Fig 7. Gate Charge Characteristics

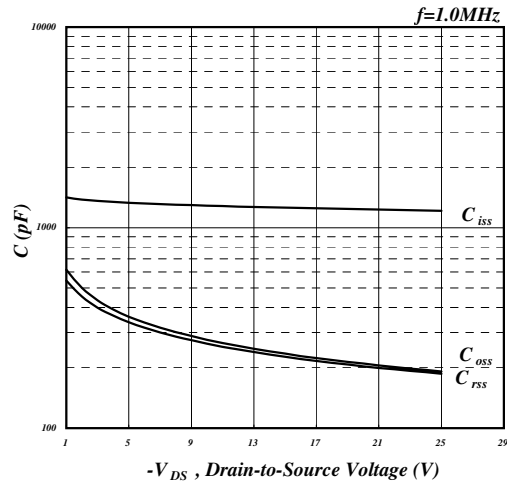


Fig 8. Typical Capacitance Characteristics

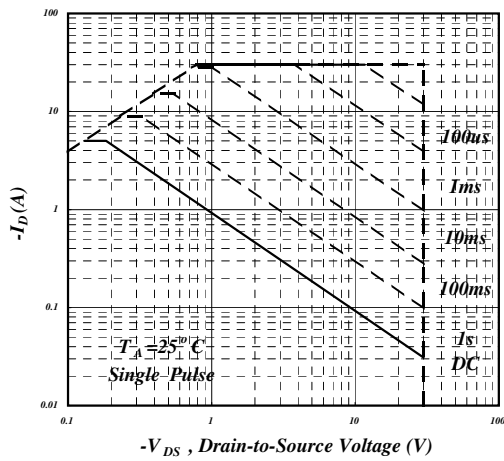


Fig 9. Maximum Safe Operating Area

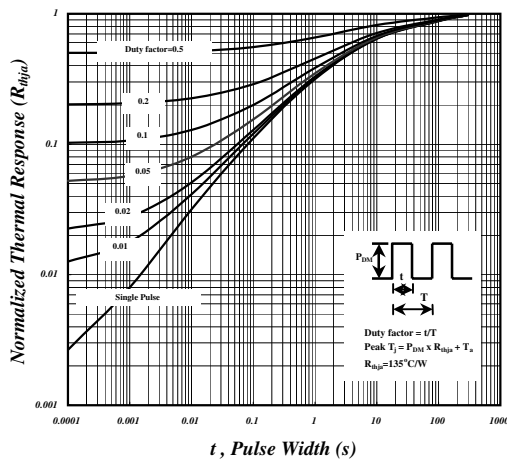


Fig 10. Effective Transient Thermal Impedance

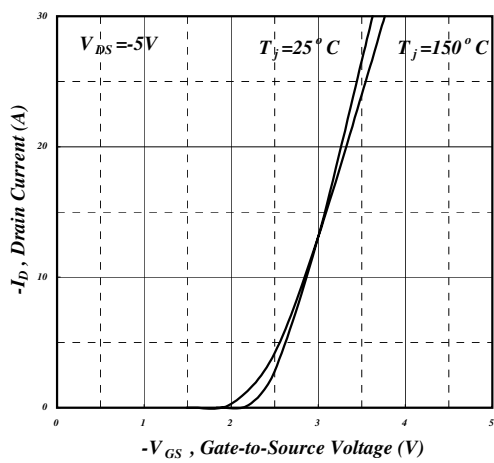


Fig 11. Transfer Characteristics

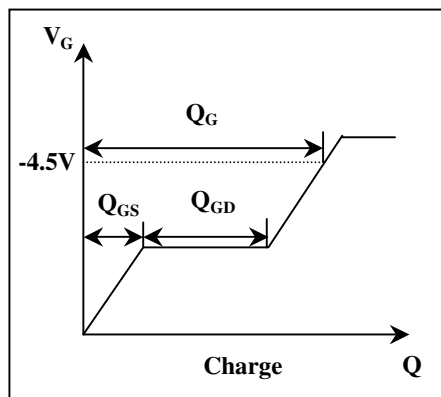
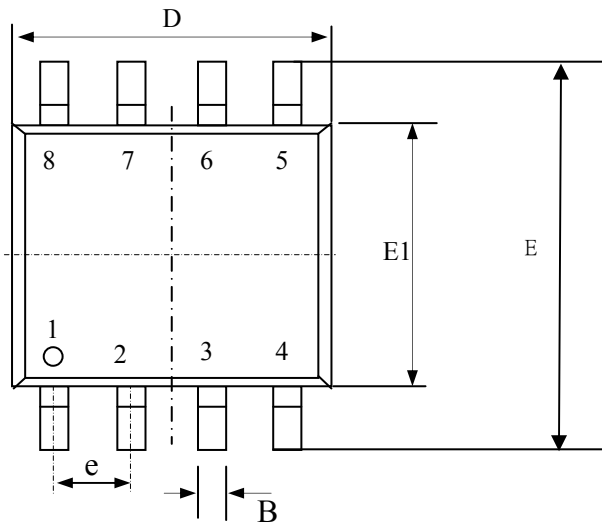


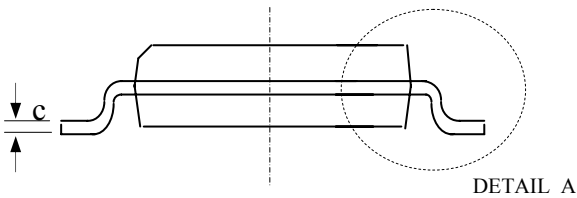
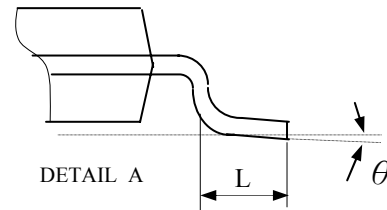
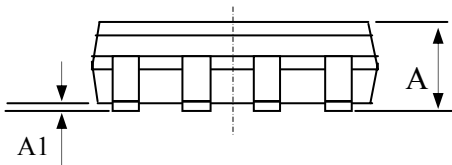
Fig 12. Gate Charge Waveform



## Package Outline : SO-8

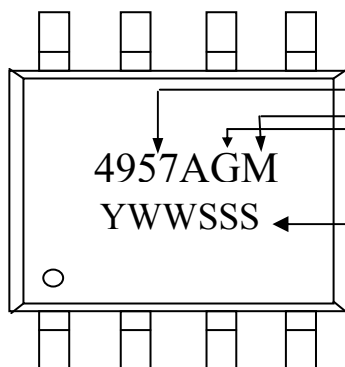


SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	1.35	1.55	1.75
A1	0.10	0.18	0.25
B	0.33	0.41	0.51
C	0.19	0.22	0.25
D	4.80	4.90	5.00
E1	3.80	3.90	4.00
E	5.80	6.15	6.50
L	0.38	0.71	1.27
$\theta$	0	4.00	8.00
e	1.27 TYP		



1. All Dimension Are In Millimeters.
2. Dimension Does Not Include Mold Protrusions.

## Part Marking Information & Packing : SO-8



Part Number — Package Code — meet Rohs requirement

Date Code (YWWSSS)

Y : Last Digit Of The Year

WW : Week

SSS : Sequence