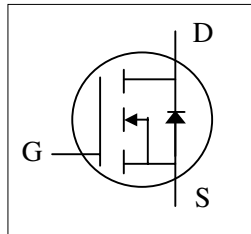




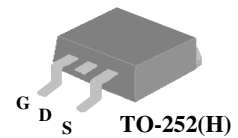
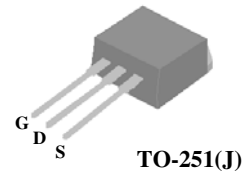
- ▼ 100% Avalanche Test
- ▼ Fast Switching Characteristics
- ▼ Simple Drive Requirement



BV_{DSS}	600V
$R_{DS(ON)}$	5 Ω
I_D	2A

Description

AP4002 series are specially designed as chopper regulator, DC/DC converter and power drive application. The APEC MOSFET provide the best combination of fast switching, ruggedized design and cost-effectiveness.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	600	V
V_{GS}	Gate-Source Voltage	± 30	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, V_{GS} @ 10V	2	A
I_{DM}	Pulsed Drain Current ¹	8	A
$P_D@T_C=25^\circ C$	Total Power Dissipation	20	W
	Linear Derating Factor	0.16	W/ $^\circ C$
E_{AS}	Single Pulse Avalanche Energy ²	20	mJ
I_{AR}	Avalanche Current	2	A
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Value	Unit
Rthj-c	Maximum Thermal Resistance, Junction-case	6.25	$^\circ C/W$
Rthj-a	Maximum Thermal Resistance, Junction-ambient	110	$^\circ C/W$



Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =1mA	600	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =1.0A	-	-	5	Ω
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2	-	4	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =2.0A	-	1.5	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =600V, V _{GS} =0V	-	-	100	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±30V	-	-	±1	uA
Q _g	Total Gate Charge ³	I _D =2A	-	12	19	nC
Q _{gs}	Gate-Source Charge	V _{DS} =480V	-	2	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =10V	-	5.5	-	nC
t _{d(on)}	Turn-on Delay Time ³	V _{DD} =200V	-	10	-	ns
t _r	Rise Time	I _D =1A	-	12	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =50Ω, V _{GS} =10V	-	52	-	ns
t _f	Fall Time	R _D =200Ω	-	19	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	375	600	pF
C _{oss}	Output Capacitance	V _{DS} =10V	-	170	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	45	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ³	T _j =25°C, I _S =2A, V _{GS} =0V	-	-	1.5	V
t _{rr}	Reverse Recovery Time ³	I _S =2A, V _{GS} =0V,	-	340	-	ns
Q _{rr}	Reverse Recovery Charge	di/dt=100A/μs	-	2.2	-	uC

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Starting T_j=25°C , V_{DD}=50V , L=10mH , R_G=25Ω
- 3.Pulse test

THIS PRODUCT IS ELECTROSTATIC SENSITIVE, PLEASE HANDLE WITH CAUTION.

THIS PRODUCT HAS BEEN QUALIFIED FOR USE IN CONSUMER APPLICATIONS. APPLICATIONS OR USE IN LIFE SUPPORT OR OTHER SIMILAR MISSION-CRITICAL DEVICES OR SYSTEMS ARE NOT AUTHORIZED.

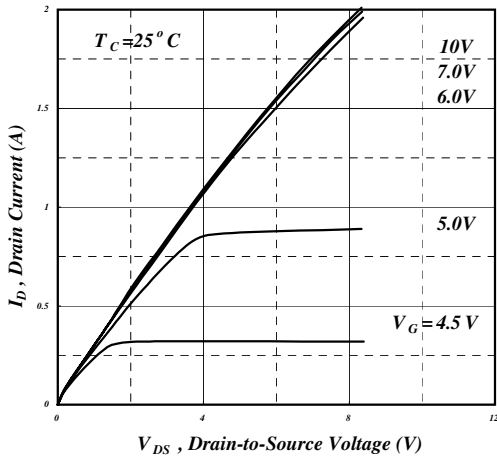


Fig 1. Typical Output Characteristics

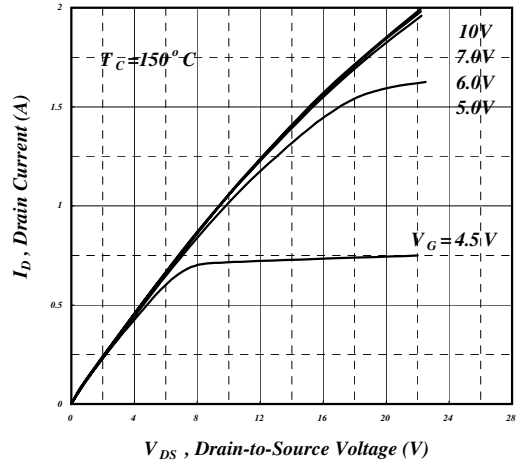


Fig 2. Typical Output Characteristics

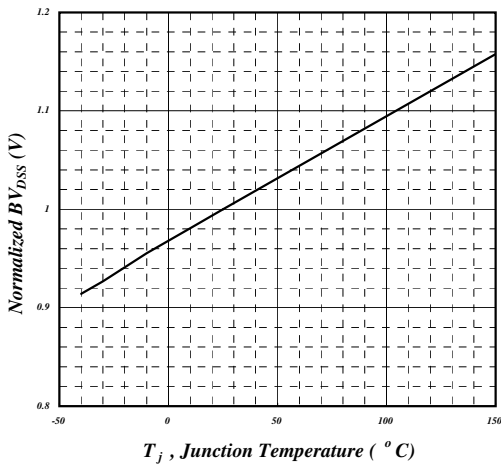


Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

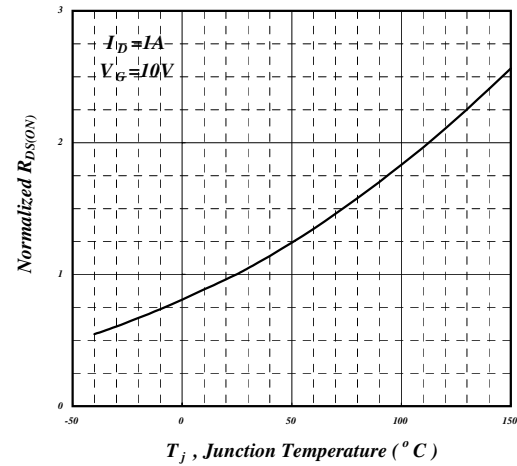


Fig 4. Normalized On-Resistance v.s. Junction Temperature

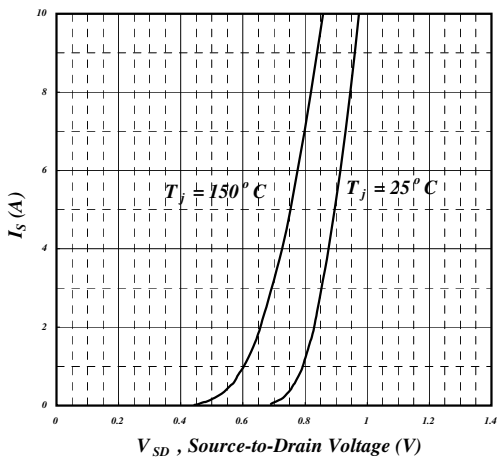


Fig 5. Forward Characteristic of Reverse Diode

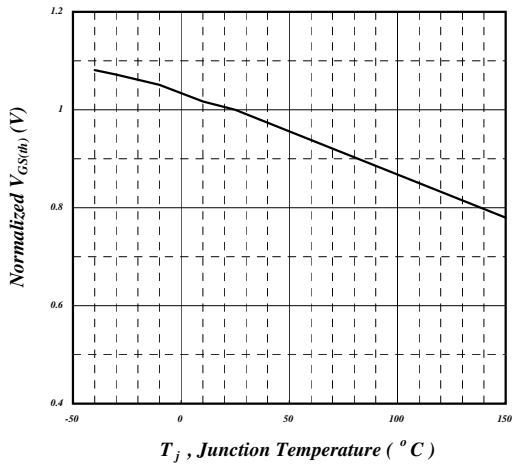


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

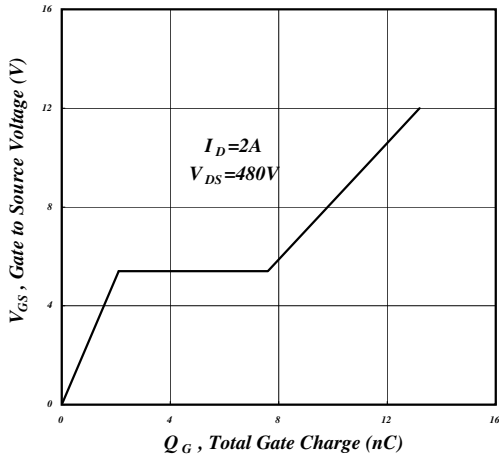


Fig 7. Gate Charge Characteristics

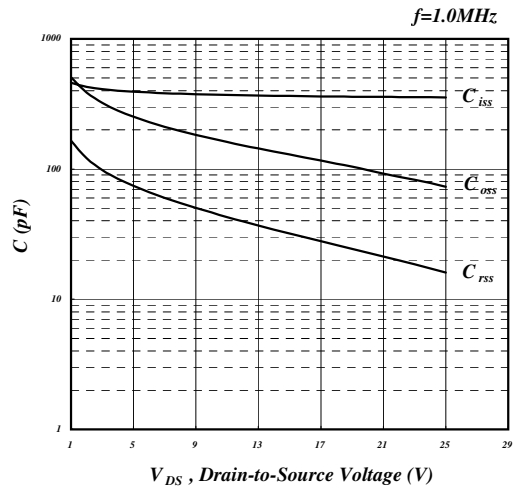


Fig 8. Typical Capacitance Characteristics

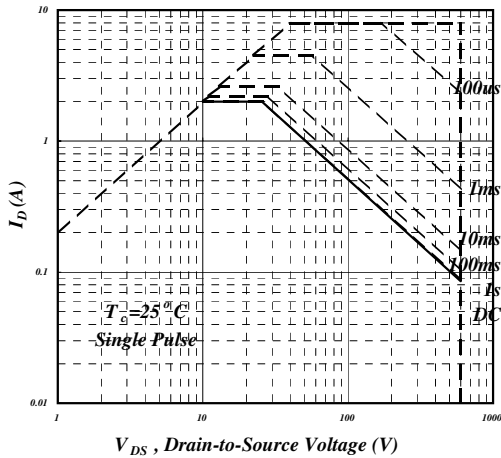


Fig 9. Maximum Safe Operating Area

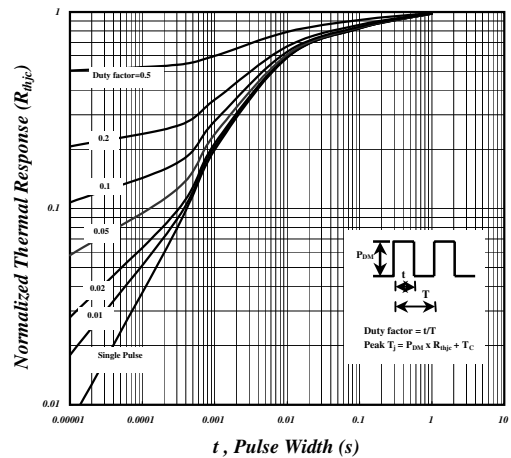


Fig 10. Effective Transient Thermal Impedance

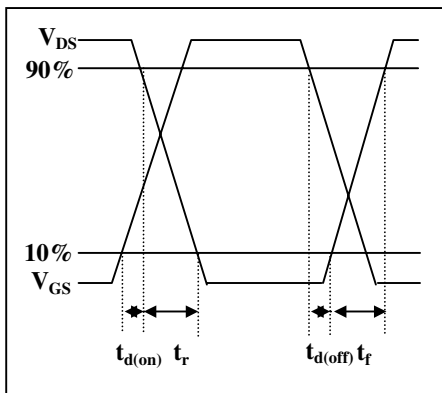


Fig 11. Switching Time Waveform

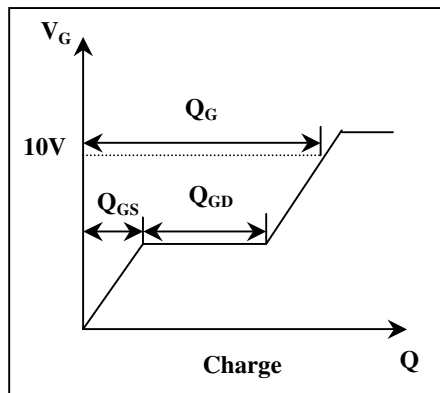
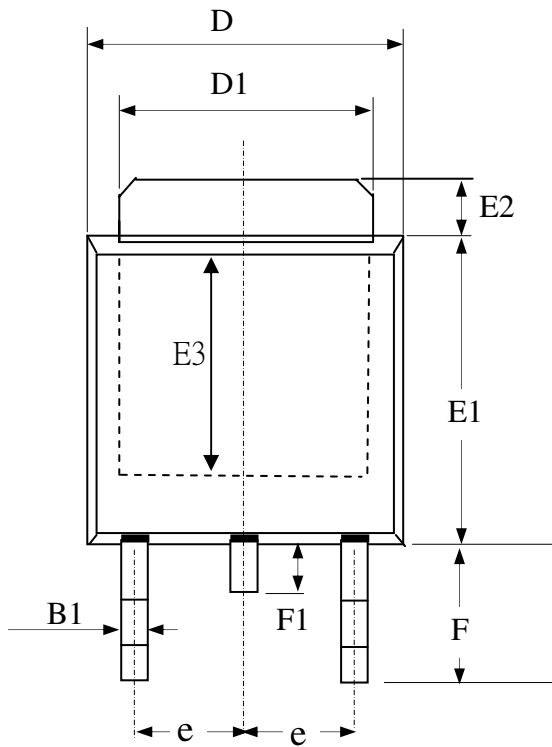


Fig 12. Gate Charge Waveform

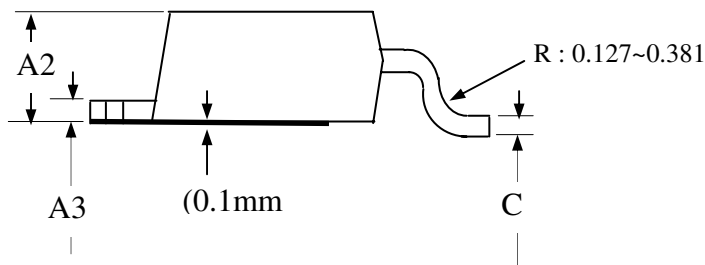


Package Outline : TO-252

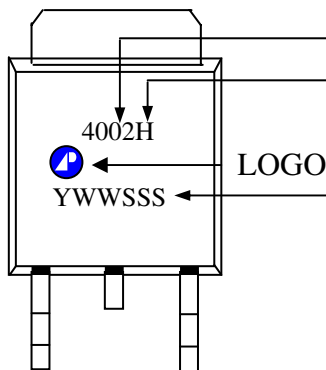


SYMBOLS	Millimeters		
	MIN	NOM	MAX
A2	1.80	2.30	2.80
A3	0.40	0.50	0.60
B1	0.40	0.70	1.00
D	6.00	6.50	7.00
D1	4.80	5.35	5.90
E3	3.50	4.00	4.50
F	2.20	2.63	3.05
F1	0.5	0.85	1.20
E1	5.10	5.70	6.30
E2	0.50	1.10	1.80
e	--	2.30	--
C	0.35	0.50	0.65

- 1.All Dimensions Are in Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.



Part Marking Information & Packing : TO-252



Part Number

Package Code

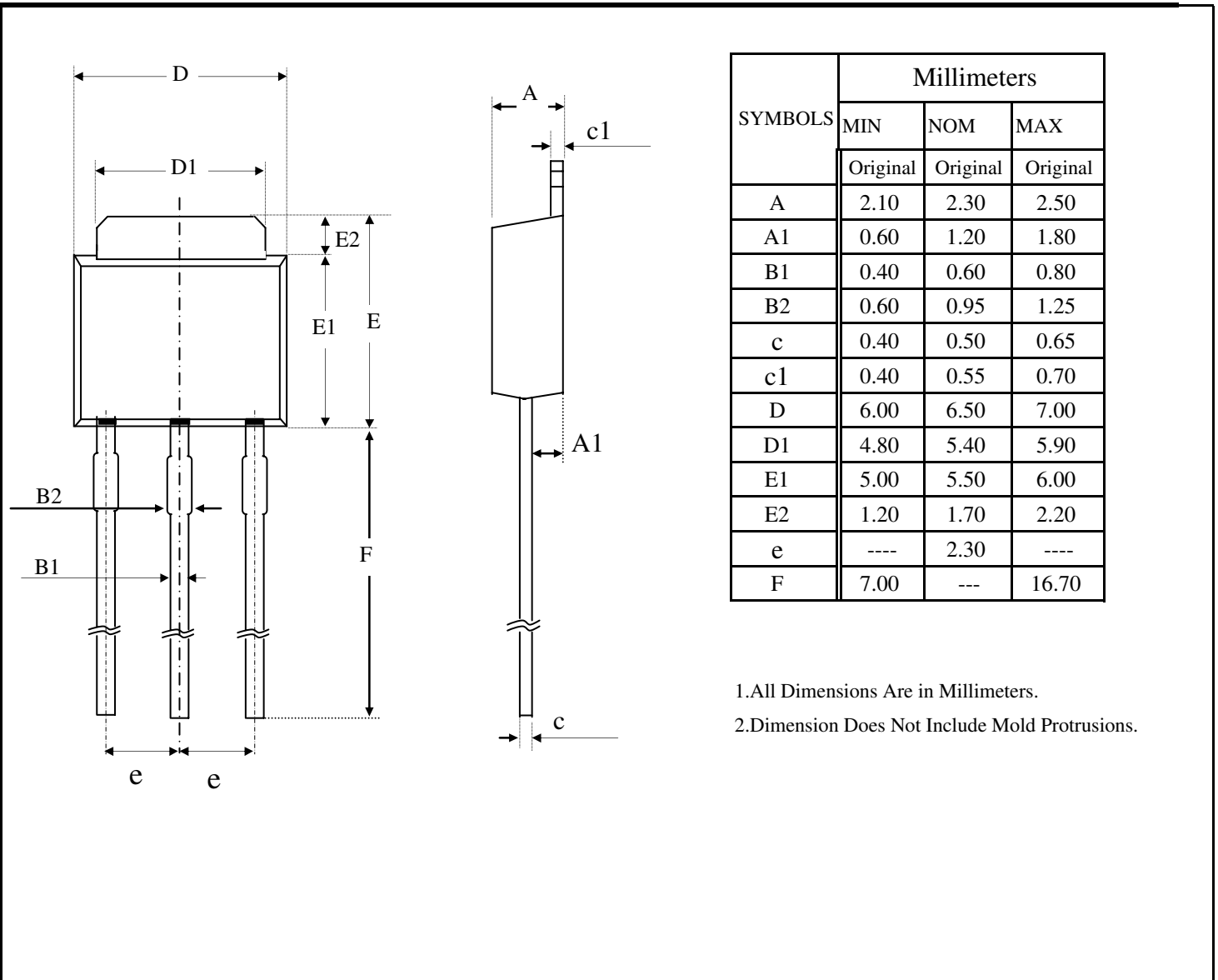
LOGO

Date Code (YWWSSS)

Y : Last Digit Of The Year

WW : Week

SSS : Sequence



Part Marking Information & Packing : TO-251

