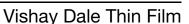
RoHS

COMPLIANT

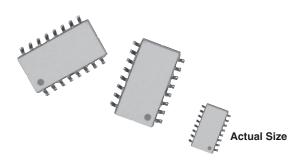
HALOGEN

FREE



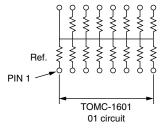


Molded, 50 mil Pitch Thin Film Resistor, Surface Mount Network



Vishay Dale Thin Film offers standard circuits in 16 pins in a medium body molded surface mount package. The networks are available over a resistance range of 100 Ω to 100 $k\Omega$. The network features tight ratio tolerances and close TCR tracking. In addition to the standards shown, custom circuits are available upon request.

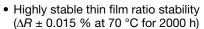
SCHEMATIC



The 01 circuit provides 15 nominally equal resistors, each connected between a common lead (16) and a discrete PC board pin.

FEATURES

- 0.090" (2.29 mm) maximum seated height
- Rugged, molded case construction (0.22" wide)



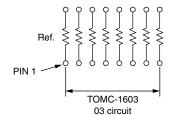
- Low temperature coefficient, ± 25 ppm/°C (- 55 °C to + 125 °C)
- \bullet Wide resistance range 100 Ω to 100 $k\Omega$
- Isolated/bussed circuits
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition

Not

Pb containing terminations are not RoHS compliant, exemptions may apply

TYPICAL PERFORMANCE

	ABSOLUTE	TRACKING
TCR	25	5
	ABSOLUTE	RATIO
TOL.	0.1	0.025



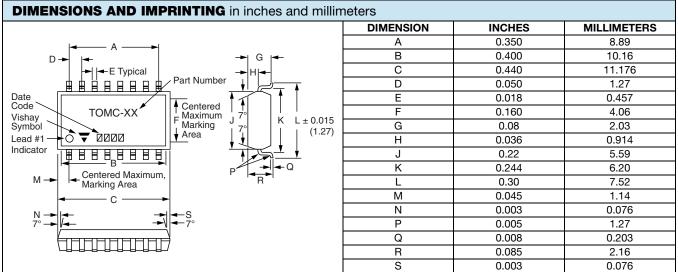
The 03 circuit provides a choice of 8 nominally equal resistors with each resistor isolated from all others and wired directly across.

STANDARD ELECTRICAL SPECIFICATIONS		
TEST	SPECIFICATIONS	CONDITIONS
Material	Passivated nichrome	-
Pin/Lead Number	16	-
Resistance Range	100 Ω to 100 k Ω per resistor	-
TCR: Absolute	± 25 ppm/°C	- 55 °C to + 125 °C
TCR: Tracking	± 5 ppm/°C	- 55 °C to + 125 °C
Tolerance: Absolute	± 0.1 % to 1 %	+ 25 °C
Tolerance: Ratio	± 0.025 % to 0.5 %	+ 25 °C
Power Rating: Resistor	50 mW = PIN 16 common 100 mW = isolated	Maximum at + 70 °C
Power Rating: Package	750 mW	Maximum at + 70 °C
Stability: Absolute	ΔR ± 0.05 %	2000 h at + 70 °C
Stability: Ratio	ΔR ± 0.015 %	2000 h at + 70 °C
Voltage Coefficient	0.1 ppm/V	-
Working Voltage	100 V max. not to exceed √P x R	-
Operating Temperature Range	- 55 °C to + 125 °C	-
Storage Temperature Range	- 55 °C to + 150 °C	-
Noise	< - 30 dB	-
Thermal EMF	0.08 μV/°C	-
Shelf Life Stability: Absolute	ΔR ± 0.01 %	1 year at + 25 °C
Shelf Life Stability: Ratio	ΔR ± 0.002 %	1 year at + 25 °C

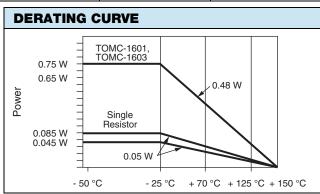
Revision: 20-Oct-11 Document Number: 60008

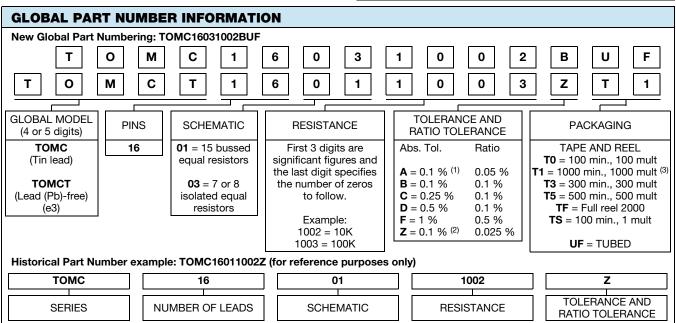


Vishay Dale Thin Film



MECHANICAL SPECIFICATIONS		
Resistive Element	Passivated nichrome	
Substrate Material	Silicon	
Body	Molded epoxy	
Terminals	Copper alloy	
Lead (Pb)-free Option	100 % matte tin	
Tin Lead Option	Sn85	
Tin Lead and Lead (Pb)-free Finish	Plated	





Notes

- (1) Tolerance available 250 and up
- (2) Tolerance available 1K and up
- (3) Preferred packaging code



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Vishay

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