

#### **Bicolor SMD LED PLCC-4**



#### **DESCRIPTION**

These devices have been designed to meet the increasing demand for surface mounting technology.

The package of the VLMSY3420 is the PLCC-4.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

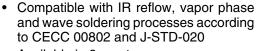
This SMD device consists of a red and yellow chip. So it is possible to choose the color in one device.

#### PRODUCT GROUP AND PACKAGE DATA

 Product group: LED Package: SMD PLCC-4 · Product series: bicolor Angle of half intensity: ± 60°

#### **FEATURES**

- SMD LED with exceptional brightness
- Multicolored
- · Luminous intensity categorized
- EIA and ICE standard package
- · Compatible with automatic placement equipment
- and wave soldering processes according





AUTOMOTIVE

- · Available in 8 mm tape
- · Low profile package
- Non-diffused lens: excellent for coupling to light pipes and backlighting
- Low power consumption
- · Luminous intensity ratio in one packaging unit  $I_{Vmax}/I_{Vmin.} \leq 1.6$
- Preconditioning: according to JEDEC level 2a
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- AEC-Q101 qualified

#### **APPLICATIONS**

- Automotive: backlighting in dashboards and switches
- · Telecommunication: indicator and backlighting in telephone and fax
- · Indicator and backlight in office equipment
- Flat backlight for LCDs, switches and symbols
- · General use

PARTS TABLE					
PART	COLOR, LUMINOUS INTENSITY	TECHNOLOGY			
VLMSY3420-GS08	Red, I <sub>V</sub> = (224 to 900) mcd Yellow, I <sub>V</sub> = (280 to 1120) mcd	AllnGaP on GaAs			
VLMSY3420-GS18	Red, $I_V = (224 \text{ to } 900) \text{ mcd}$ Yellow, $I_V = (280 \text{ to } 1120) \text{ mcd}$	AllnGaP on GaAs			

<sup>\*\*</sup> Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902



ABSOLUTE MAXIMUM RATINGS 1) VLMSY3420							
PARAMETER	TEST CONDITION		SYMBOL	VALUE	UNIT		
Reverse voltage per diode <sup>2)</sup>	I <sub>R</sub> = 10 μ	Α	V <sub>R</sub>	5	V		
DC forward current per diode	T <sub>amb</sub> ≤ 65	°C	I <sub>F</sub>	50	mA		
Surge forward current per diode			I <sub>FSM</sub>	0.1	Α		
Power dissipation per diode			$P_V$	130	mW		
Junction temperature			T <sub>j</sub>	125	°C		
Operating temperature range			T <sub>amb</sub>	- 40 to + 100	°C		
Storage temperature range			T <sub>stg</sub>	- 40 to + 100	°C		
Thermal resistance junction/ ambient	Mounted on PC board (pad size > 16 mm <sup>2</sup> )	1 chip on 2 chips on	R <sub>thJA</sub>	480 650	K/W		

#### Notes:

- T<sub>amb</sub> = 25 °C, unless otherwise specified
   Driving the LED in reverse direction is suitable for short term application

OPTICAL AND ELECTRICAL CHARACTERISTICS 1) VLMSY3420, SUPER RED							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I <sub>F</sub> = 50 mA	VLMSY3420	I <sub>V</sub>	224		900	mcd
Dominant wavelength	I <sub>F</sub> = 50 mA		$\lambda_{d}$		630		nm
Peak wavelength	I <sub>F</sub> = 50 mA		$\lambda_{p}$		643		nm
Angle of half intensity	I <sub>F</sub> = 50 mA		φ		± 60		deg
Forward voltage	I <sub>F</sub> = 50 mA		V <sub>F</sub>		2.1	2.6	V
Reverse current	V <sub>R</sub> = 5 V		I <sub>R</sub>			10	μΑ
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		C <sub>j</sub>		15		pF

#### Note:

<sup>1)</sup>  $T_{amb} = 25$  °C, unless otherwise specified

OPTICAL AND ELECTRICAL CHARACTERISTICS 1) VLMSY3420, YELLOW							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I <sub>F</sub> = 50 mA	VLMSY3420	I <sub>V</sub>	280		1120	mcd
Dominant wavelength	I <sub>F</sub> = 50 mA		$\lambda_{d}$	581	588	594	nm
Peak wavelength	I <sub>F</sub> = 50 mA		$\lambda_{p}$		590		nm
Angle of half intensity	I <sub>F</sub> = 50 mA		φ		± 60		deg
Forward voltage	I <sub>F</sub> = 50 mA		V <sub>F</sub>		2.1	2.6	V
Reverse current	V <sub>R</sub> = 5 V		I <sub>R</sub>			10	μΑ
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		C <sub>j</sub>		15		pF

#### Note:

<sup>1)</sup>  $T_{amb} = 25$  °C, unless otherwise specified



L	LUMINOUS INTENSITY CLASSIFICATION AND GROUP COMBINATIONS 1) VLMSY3420								
			RED						
		\$2 224 to 280 mcd	T1 280 to 355 mcd	T2 355 to 450 mcd	U1 450 to 560 mcd	U2 560 to 710 mcd	V1 710 to 900 mcd		
	T1 280 to 355 mcd	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420		
Y E	T2 355 to 450 mcd	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420		
L L	U1 450 to 560 mcd	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420		
O W	U2 560 to 710 mcd	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420		
	V1 710 to 900 mcd	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420		
	V2 900 to 1120 mcd	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420	VLMSY3420		

Note:

<sup>1)</sup> Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of  $\pm$  11 %

COLOR CLASSIFICATION						
	DOMINANT WAVELENGTH (nm)					
GROUP	YELLOW					
	MIN.	MAX.				
1	581	584				
2	583	586				
3	585	588				
4	587	590				
5	589	592				
6	591	594				

Note:

Wavelengths are tested at a current pulse duration of 25 ms.

#### **TYPICAL CHARACTERISTICS**

 $T_{amb}$  = 25 °C, unless otherwise specified

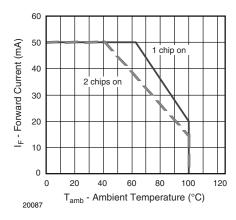


Figure 1. Forward Current vs. Ambient Temperature

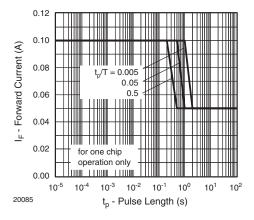


Figure 2. Forward Current vs. Pulse Duration



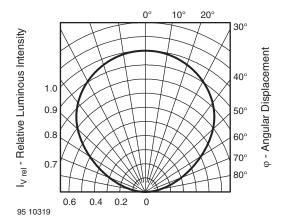


Figure 3. Rel. Luminous Intensity vs. Angular Displacement

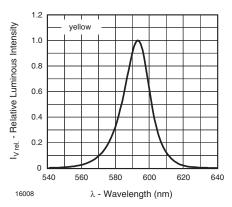


Figure 4. Relative Intensity vs. Wavelength

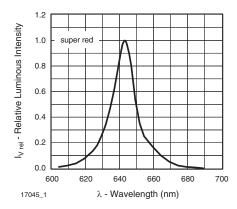


Figure 5. Relative Intensity vs. Wavelength

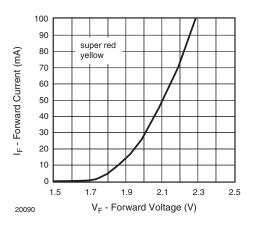


Figure 6. Relative Forward Voltage vs. Ambient Temperature

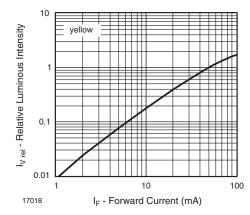


Figure 7. Relative Luminous Intensity vs. Forward Current

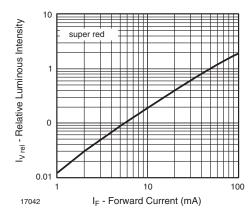
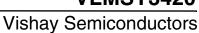


Figure 8. Relative Luminous Intensity vs. Forward Current





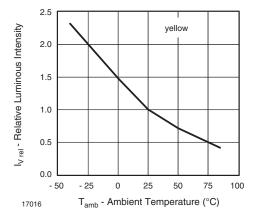


Figure 9. Rel. Luminous Intensity vs. Ambient Temperature

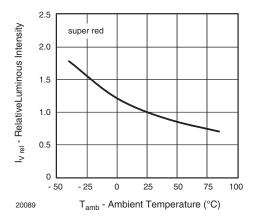


Figure 10. Rel. Luminous Intensity vs. Ambient Temperature

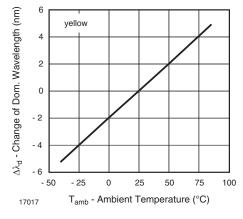


Figure 11. Rel. Luminous Intensity vs. Ambient Temperature

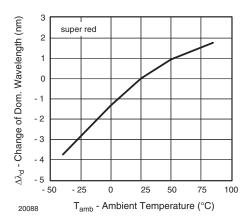


Figure 12. Change of Dominant Wavelength vs.
Ambient Temperature

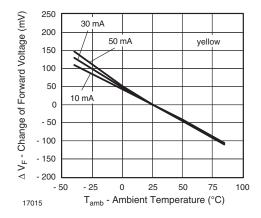


Figure 13. Change of Forward Voltage vs. Ambient Temperature

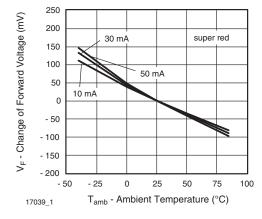
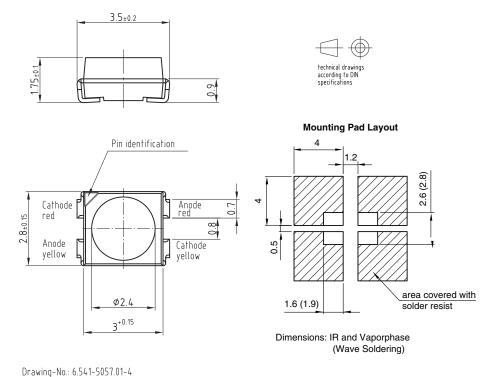


Figure 14. Change of Forward Voltage vs. Ambient Temperature

# VISHAY.

#### **PACKAGE DIMENSIONS** in millimeters

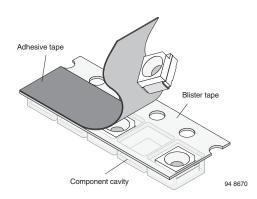


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#### **METHOD OF TAPING/POLARITY AND TAPE AND REEL**

#### **SMD LED (VLM.3-SERIES)**

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



#### **TAPING OF VLM.3...**

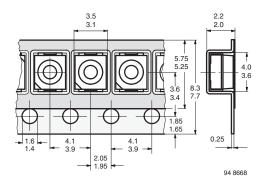


Figure 15. Tape Dimensions in mm for PLCC-2



#### REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

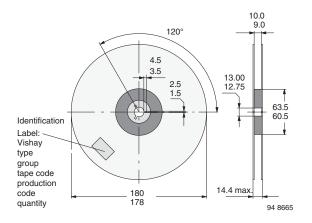


Figure 16. Reel Dimensions - GS08

#### REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED

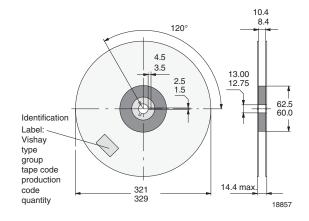


Figure 17. Reel Dimensions - GS18

#### **SOLDERING PROFILE**

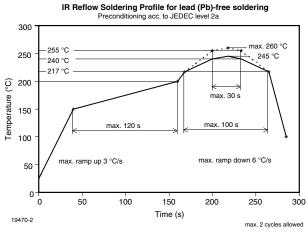


Figure 18. Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

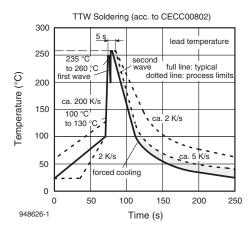
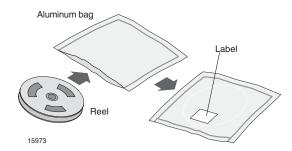


Figure 19. Double Wave Soldering of Opto Devices (all Packages)

#### **DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.





The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

#### RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminium bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

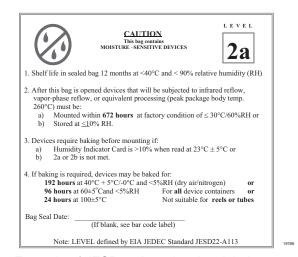
192 h at 40  $^{\circ}$ C + 5  $^{\circ}$ C/- 0  $^{\circ}$ C and < 5  $^{\circ}$  RH (dry air/ nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.





Example of JESD22-A112 level 2a label

#### **ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

# VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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