



N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^d	Q _g (Typ.)		
20	0.028 at V _{GS} = 4.5 V	7.9			
	0.032 at V _{GS} = 2.5 V	7.4	6.7 nC		
	0.038 at V _{GS} = 1.8 V	6.8			

TSOP-6 Top View

2.85 mm -

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC

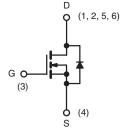


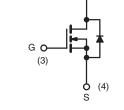
COMPLIANT HALOGEN FREE

APPLICATIONS

- DC/DC Converters
- **Boost Converters**
- Load Switch

Lot Traceability and Date Code





Ordering Information: Si3460DDV-T1-GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	20	V	
Gate-Source Voltage		V _{GS}	± 8		
	T _C = 25 °C		7.9		
Continuous Drain Current /T 150 °C)	T _C = 70 °C	1 . 🗀	6.3		
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	6.2 ^{a, b}		
	T _A = 70 °C	1	5.0 ^{a, b}		
Pulsed Drain Current		I _{DM}	20	A	
Continuous Course Dunis Diada Current	T _C = 25 °C		2.2		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	1.4 ^{a, b}		
Avalanche Current		I _{AS}	8		
Single Avalanche Energy		E _{AS}	3.2	mJ	
	T _C = 25 °C		2.7		
Manifestor Barrer Biocharling	T _C = 70 °C	1 , [1.7		
Maximum Power Dissipation	T _A = 25 °C	P _D	1.7 ^{a, b}	W	
	T _A = 70 °C	1	1.1 ^{a, b}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		
Soldering Recommendations (Peak Temperature) ^{d, e}			260	°C	

Marking Code BA XXX

Part # Code

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{a, c}	t ≤ 5 s	R _{thJA}	61	74	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	38	46]	

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 5 s.
- c. Maximum under steady state conditions is 120 °C/W.
- d. Based on $T_C = 25$ °C.

Si3460DDV

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			1	1			
Drain-Source Breakdown Voltage	V_{DS}	V _{GS} = 0 V, I _D = 250 μA	20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 A		21		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 2.6			
Gate-Source Threshold Voltage	V _{GS(th)}				1.0	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V	V _{DS} = 20 V, V _{GS} = 0 V		1		
		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	20			Α	
Drain-Source On-State Resistance ^a		V _{GS} = 4.5 V, I _D = 5.1 A		0.023	0.028	1	
	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 4.7 \text{ A}$		0.027	0.032	Ω	
		$V_{GS} = 1.8 \text{ V}, I_D = 2.5 \text{ A}$		0.031	0.038	1	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 5.1 A		35		S	
Dynamic ^b					•		
Input Capacitance	C _{iss}			666		pF	
Output Capacitance	C _{oss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		93			
Reverse Transfer Capacitance	C _{rss}			41			
Total Cata Chausa		$V_{DS} = 10 \text{ V}, V_{GS} = 8 \text{ V}, I_D = 5 \text{ A}$		12	18	nC	
Total Gate Charge	Q _g	50 40 5		6.7	10.1		
Gate-Source Charge	Q_{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 5 \text{ A}$		0.95			
Gate-Drain Charge	Q_{gd}			0.5			
Gate Resistance	R_{g}	f = 1 MHz	0.4	2.1	4.2	Ω	
Turn-On Delay Time	t _{d(on)}			6	12	-	
Rise Time	t _r	$V_{DD} = 10 \text{ V}, R_{L} = 2 \Omega$		11	20		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		21	32		
Fall Time	t _f			8	16		
Turn-On Delay Time	t _{d(on)}			5	10	ns	
Rise Time	t _r	$V_{DD} = 10 \text{ V}, R_L = 2 \Omega$		12	18	- - -	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 5 \text{ A}, V_{GEN} = 8 \text{ V}, R_g = 1 \Omega$		19	29		
Fall Time	t _f			8	16		
Drain-Source Body Diode Characteristic	s		1		•		
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			2.2		
Pulse Diode Forward Current	I _{SM}				20	_ A	
Body Diode Voltage	V _{SD}	I _S = 5 A, V _{GS} = 0 V		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			11	20	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			3	6	nC	
Reverse Recovery Fall Time	t _a	$I_F = 5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		7			
Reverse Recovery Rise Time	t _b	_		4		ns	

Notes:

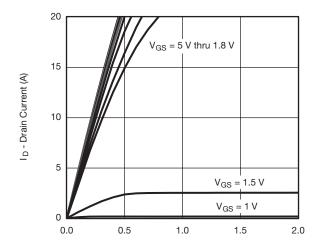
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing.

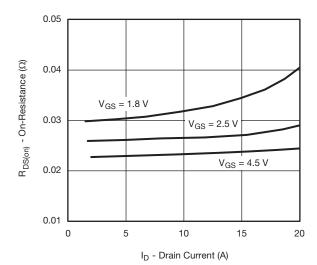


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

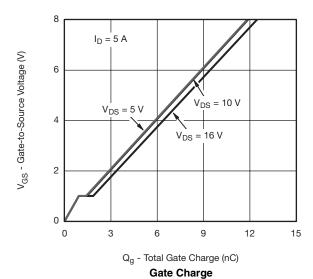


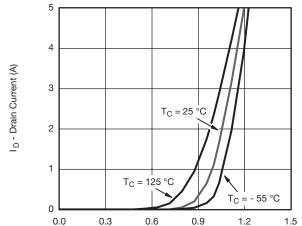
V_{DS} - Drain-to-Source Voltage (V)

Output Characteristics

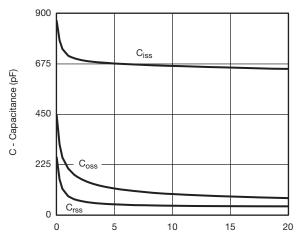


On-Resistance vs. Drain Current and Gate Voltage



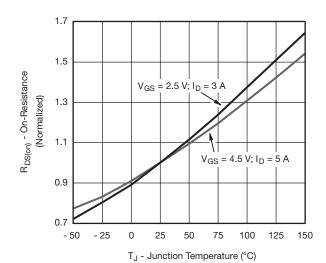


V_{GS} - Gate-to-Source Voltage (V) **Transfer Characteristics**



 V_{DS} - Drain-to-Source Voltage (V)

Capacitance

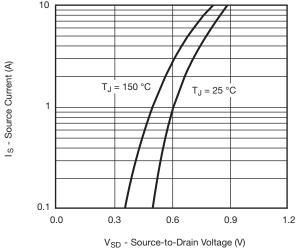


On-Resistance vs. Junction Temperature

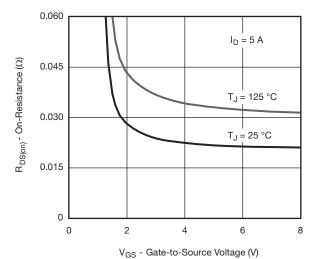
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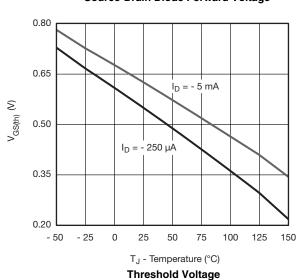
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Source-Drain Diode Forward Voltage



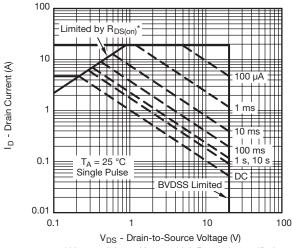
On-Resistance vs. Gate-to-Source Voltage



20 15 10 5 0 0.001 0.01 0.1 1 10 100 Time (s)

25

Single Pulse Power (Junction-to-Ambient)

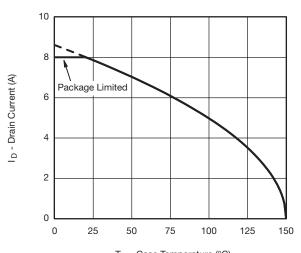


* V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

Safe Operating Area, Junction-to-Ambient

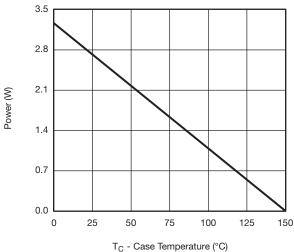


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

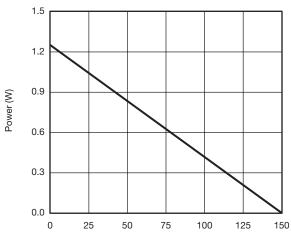


T_C - Case Temperature (°C)

Current Derating*







T_A - Ambient Temperature (°C)

Power, Junction-to-Case

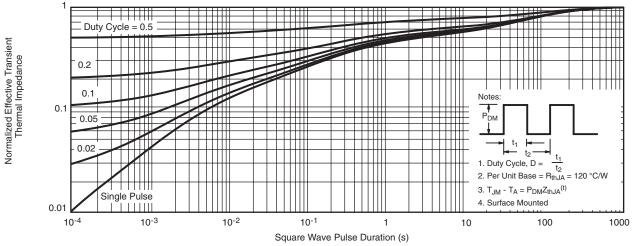
Power, Junction-to-Ambient

 $^{^*}$ The power dissipation P_D is based on $T_{J(max.)}$ = 150 $^{\circ}$ C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

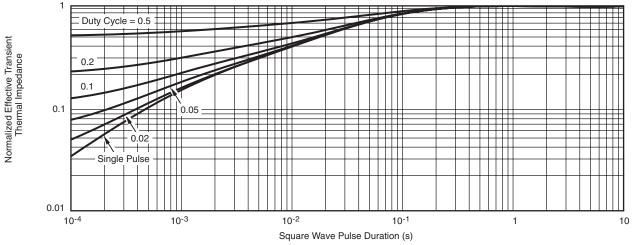
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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