



### Low-Voltage Single SPDT Analog Switch

### **DESCRIPTION**

The DG2002 is a single-pole/double-throw monolithic CMOS analog switch designed for high performance switching of analog signals. Combining low power, high speed ( $t_{ON}$ : 8 ns,  $t_{OFF}$ : 6 ns), low on-resistance ( $r_{DS(on)}$ : 7  $\Omega$ ) and small physical size (SC70), the DG2002 is ideal for portable and battery powered applications requiring high performance and efficient use of board space.

The DG2002 is built on Vishay Siliconix's low voltage Jl2 process. An epitaxial layer prevents latchup. Break-before - make is guaranteed for DG2002.

Each switch conducts equally well in both directions when on, and blocks up to the power supply level when off.

### **FEATURES**

- Low voltage operation (1.8 V to 5.5 V)
- Low on-resistance  $r_{DS(on)}$ : 7  $\Omega$
- Fast switching t<sub>ON</sub>: 8 ns, t<sub>OFF</sub>: 6 ns
- Low charge injection Q<sub>INJ</sub>: 5 pC
- · Low power consumption
- TTL/CMOS compatible
- 6-pin SC70 package

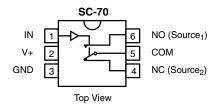
### **BENEFITS**

- · Reduced power consumption
- Simple logic interface
- High accuracy
- · Reduce board space

### **APPLICATIONS**

- · Cellular phones
- · Communication systems
- · Portable test equipment
- · Battery operated systems
- · Sample and hold circuits

### **FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION**



Device Marking: E2xx

TRUTH TABLE						
Logic NC NO						
0	ON	OFF				
1 OFF ON						

ORDERING INFORMATION							
Temp Range	Package	Part Number					
- 40 to 85 °C	SC70-6	DG2002DL-T1 DG2002DL-T1-E3					

Document Number: 71448 S-72609-Rev. C, 24-Dec-07

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply.



ABSOLUTE MAXIMUM RATINGS							
Parameter		Limit	Unit				
Referenced V+ to GND		- 0.3 to +6	V				
IN, COM, NC, NO <sup>a</sup>		- 0.3 to (V+ + 0.3)					
Continuous Current (Any Terminal)		± 50	mA				
Peak Current (Pulsed at 1 ms, 10 % of	duty cycle)	± 200	IIIA				
Storage Temperature		- 65 to + 150	°C				
Power Dissipation (Packages) <sup>b</sup>	6-Pin SC70 <sup>c</sup>	250	mW				

- a. Signals on NC, NO, or COM or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings. b. All leads welded or soldered to PC Board.
- c. Derate 3.1 mW/°C above 70 °C.

		Test Conditions Unless Otherwise Specified V+ = 2.0 V, ± 10 %		<b>Limits</b> - 40 to 85 °C			
Parameter	Symbol	$V_{1N} = 0.4 \text{ or } 1.6 \text{ V}^{e}$	Temp <sup>a</sup>	Min <sup>b</sup>	Typ <sup>c</sup>	Max <sup>b</sup>	Unit
Analog Switch						l	ı
Analog Signal Range <sup>d</sup>	$V_{NO}, V_{NC} \ V_{COM}$		Full	0		V+	V
On-Resistance	r <sub>ON</sub>	V+ = 1.8 V, V <sub>COM</sub> = 1.0 V, I <sub>NO</sub> , I <sub>NC</sub> = 10 mA	V+ = 1.8 V, V <sub>COM</sub> = 1.0 V, I <sub>NO</sub> , I <sub>NC</sub> = 10 mA Room Full <sup>d</sup>		38 39.3	46.1 47.1	Ω
r <sub>ON</sub> Flatness <sup>d</sup>	r <sub>ON</sub> Flatness	$V_{+} = 1.8 \text{ V}, V_{COM} = 0 \text{ to V+}, I_{NO}, I_{NC} = 10 \text{ mA}$ Room			21		52
Contrata Off Landson Community	I <sub>NO(off)</sub> I <sub>NC(off)</sub>	V+ = 2.2 V	Room Full <sup>d</sup>	- 250 - 3.0		250 3.0	pA nA
Switch Off Leakage Current <sup>†</sup>	I <sub>COM(off)</sub>	$V_{NO}$ , $V_{NC} = 0.5 \text{ V}/1.5 \text{ V}$ , $V_{COM} = 1.5 \text{ V}/0.5 \text{ V}$	Room Full <sup>d</sup>	- 250 - 3.0		250 3.0	pA nA
Channel-On Leakage Current <sup>f</sup>	I <sub>COM(on)</sub>	$V+ = 2.2 \text{ V}, V_{NO}, V_{NC} = V_{COM} = 0.5 \text{ V}/1.5 \text{ V}$	Room Full <sup>d</sup>	- 250 - 3.0		250 3.0	pA nA
Digital Control							
Input High Voltage	V <sub>INH</sub>			1.6			V
Input Low Voltage	V <sub>INL</sub>		Full			0.4	v
Input Capacitanced	C <sub>in</sub>		Full		3		рF
Input Current	I <sub>INL</sub> or I <sub>INH</sub>	V <sub>IN</sub> = 0 or V+	Full	- 1		1	μΑ
Dynamic Characteristics					I.		ı
Turn-On Time	t <sub>ON</sub>	V V 45VB 200 0 0 55	Room Full <sup>d</sup>		22	31 32	
Turn-Off Time	t <sub>OFF</sub>	$V_{NO}$ or $V_{NC}$ = 1.5 V, $R_L$ = 300 $\Omega$ , $C_L$ = 35 pF Figures 1 and 2	Room Full <sup>d</sup>		10	17 18	ns
Break-Before-Make Time	t <sub>d</sub>		Room	1	12		
Charge Injection <sup>d</sup>	$Q_{INJ}$	$C_L = 1 \text{ nF, } V_{GEN} = 0 \text{ V, } R_{GEN} = 0 \Omega, \text{ Figure 3}$	Room		5	10	рC
Off-Isolation <sup>d</sup>	OIRR	D 5000 5 mF f 1 MHz	Room		- 67		-10
Crosstalk <sup>d</sup>	X <sub>TALK</sub>	$R_L = 50 \Omega$ , $C_L = 5 pF$ , $f = 1 MHz$	Room		- 71		dB
NO, NC Off Capacitance <sup>d</sup>	C <sub>NO(off)</sub> C <sub>NC(off)</sub>	V <sub>IN</sub> = 0 or V+, f = 1 MHz			5		pF
Channel-On Capacitance <sup>d</sup>	C <sub>ON</sub>	Room			29		·
Power Supply							
Power Supply Range	V+			1.8		2.2	V
Power Supply Current <sup>d</sup>	l+	V <sub>IN</sub> = 0 or V+			0.01	1.0	μΑ
Power Consumption	P <sub>C</sub>	VIN = O OI V+				2.2	μW





		Test Conditions Unless Otherwise Specified V+ = 3 V, ±10 %		<b>Limits</b> - 40 to 85 °C			-	
Parameter	Symbol	$V_{IN} = 0.4 \text{ or } 2.0 \text{ V}^e$	Temp <sup>a</sup>	Min <sup>b</sup>	Typ <sup>c</sup>	Max <sup>b</sup>	Unit	
Analog Switch						l		
Analog Signal Range <sup>d</sup>	$V_{NO}, V_{NC} \ V_{COM}$		Full	0		V+	V	
On-Resistance <sup>d</sup>	r <sub>ON</sub>	$V+ = 2.7 \text{ V}, V_{COM} = 1.5 \text{ V}, I_{NO}, I_{NC} = 10 \text{ mA}$	Room Full		12.2 13	14.8 15.8	Ω	
r <sub>ON</sub> Flatness <sup>d</sup>	$r_{ON}$ Flatness $V_{+} = 2.7 \text{ V}, V_{COM} = 0 \text{ to V}_{+}, I_{NO}, I_{NC} = 10 \text{ mA}$		Room		5		\$2	
Switch Off Leakage Current <sup>f</sup>	I <sub>NO(off)</sub> I <sub>NC(off)</sub>	V+ = 3.3 V	Room Full	- 500 - 4.0		500 4.0	pA nA	
Switch On Leakage Current	I <sub>COM(off)</sub>	$V_{NO}$ , $V_{NC} = 1 \text{ V/3 V}$ , $V_{COM} = 3 \text{ V/1 V}$	Room Full	- 500 - 4.0		500 4.0	pA nA	
Channel-On Leakage Current <sup>f</sup>	I <sub>COM(on)</sub>	$V+ = 3.3 \text{ V}, V_{NO}, V_{NC} = V_{COM} = 1 \text{ V/3 V}$	Room Full	- 500 - 4.0		500 4.0	pA nA	
Digital Control			•					
Input High Voltage	V <sub>INH</sub>		Full	2			V	
Input Low Voltage	V <sub>INL</sub>		Full			0.4	V	
Input Capacitance <sup>d</sup>	C <sub>in</sub>		Full		3		pF	
Input Current	I <sub>INL</sub> or I <sub>INH</sub>	V <sub>IN</sub> = 0 or V+	Full	- 1		1	μΑ	
Dynamic Characteristics			•					
Turn-On Time <sup>d</sup>	t <sub>ON</sub>	V avV 20VD 2000 C 25 pC	Room Full		12	21 22		
Turn-Off Time <sup>d</sup>	t <sub>OFF</sub>	$V_{NO}$ or $V_{NC}$ = 2.0 V, $R_L$ = 300 $\Omega$ , $C_L$ = 35 pF Figures 1 and 2	Room Full		7	14 15	ns	
Break-Before-Make Time <sup>d</sup>	t <sub>d</sub>		Room	1	6			
Charge Injection <sup>d</sup>	$Q_{INJ}$	$C_L = 1$ nF, $V_{GEN} = 0$ V, $R_{GEN} = 0$ $\Omega$ , Figure 3	Room		5	10	рС	
Off-Isolation <sup>d</sup>	OIRR	D 5000 5 5 1 1 MHz	Room		- 67		-10	
Crosstalk <sup>d</sup>	X <sub>TALK</sub>	$R_L = 50 \Omega$ , $C_L = 5 pF$ , $f = 1 MHz$	Room		- 69		dB	
NO, NC Off Capacitance <sup>d</sup>	$C_{NO(off)} \ C_{NC(off)}$	V <sub>IN</sub> = 0 or V+, f = 1 MHz			5		pF	
Channel-On Capacitanced	C <sub>ON</sub>		Room		29			
Power Supply	L L							
Power Supply Range	V+			2.7		3.3	V	
Power Supply Current	Power Supply Current I+ V <sub>IN</sub> = 0 or V+				0.01	1.0	μΑ	
Power Consumption	Power Consumption P <sub>C</sub>					3.3	μW	



SPECIFICATIONS V+ = 5.0 V								
		Test Conditions Unless Otherwise Specified V+ = 5 V, ± 10 %		Limits - 40 to 85 °C			-	
Parameter	Symbol	$V_{IN} = 0.8 \text{ or } 2.4 \text{ V}^{e}$	Temp <sup>a</sup>	Min <sup>b</sup>	Typ <sup>c</sup>	Max <sup>b</sup>	Unit	
Analog Switch			•		•	•		
Analog Signal Range <sup>d</sup>	$V_{NO}, V_{NC,} V_{COM}$		Full	0		V+	V	
On-Resistance	r <sub>ON</sub>	V+ = 4.5 V, V <sub>COM</sub> = 3 V, I <sub>NO</sub> , I <sub>NC</sub> = 10 mA	Room Full		6.4 7.4	7.8 8.8	Ω	
$r_{ON}$ Flatness $V_{+} = 4.5$ V, $V_{COM} = 0$ to $V_{+}$ , $I_{NO}$ , $I_{NC} = 10$ mA		Room		3		1 22		
Switch Off Leakage Current	I <sub>NO(off),</sub> I <sub>NC(off)</sub>	V+ = 5.5 V	Room Full	- 1.0 - 4.0		1.0 4.0		
Switch Off Leakage Current	I <sub>COM(off)</sub>	$V_{NO}$ , $V_{NC} = 1 \text{ V}/4.5 \text{ V}$ , $V_{COM} = 4.5 \text{ V}/1 \text{ V}$	Room Full	- 1.0 - 4.0		1.0 4.0	nA	
Channel-On Leakage Current	I <sub>COM(on)</sub>	V+ = 5.5 V, V+ = 5.5 V $V_{NO}, V_{NC} = V_{COM} = 1 V/4.5 V$	Room Full	- 1.0 - 4.0		1.0 4.0		
Digital Control			1					
Input High Voltage	$V_{INH}$		Full	2.4			.,	
Input Low Voltage	V <sub>INL</sub>		Full			0.8	V	
Input Capacitance	C <sub>in</sub>		Full		3		pF	
Input Current	I <sub>INL</sub> or I <sub>INH</sub>	$V_{IN} = 0$ or V+	Full	- 1		1	μΑ	
Dynamic Characteristics			•		•	•		
Turn-On Time <sup>d</sup>	t <sub>ON</sub>	V 22 V 0 V D 000 0 0 05 75	Room Full		8	15 16		
Turn-Off Time <sup>d</sup>	t <sub>OFF</sub>	$V_{NO}$ or $V_{NC}$ = 3 V, $R_L$ = 300 $\Omega$ , $C_L$ = 35 pF Figures 1 and 2	Room Full		6	13 14	ns	
Break-Before-Make Time <sup>d</sup>	t <sub>d</sub>		Room	1	4			
Charge Injection <sup>d</sup>	$Q_{INJ}$	$C_L = 1$ nF, $V_{GEN} = 0$ V, $R_{GEN} = 0$ $\Omega$ , Figure 3	Room		5	10	рС	
Off-Isolation <sup>d</sup>	OIRR	D 5000 5 m f 1 MHz	Room		- 69		-ID	
Crosstalk <sup>d</sup>	X <sub>TALK</sub>	$R_L = 50 \Omega$ , $C_L = 5 pF$ , $f = 1 MHz$	Room		- 69		dB	
Source-Off Capacitance <sup>d</sup>	C <sub>NO(off),</sub> C <sub>NC(off)</sub>	V <sub>IN</sub> = 0 or V+, f = 1 MHz	Room		5		pF	
Channel-On Capacitance <sup>d</sup>	C <sub>ON</sub>	<del></del>	Room		29		]	
Power Supply								
Power Supply Range	V+			4.5		5.5	V	
Power Supply Current	l+	V <sub>IN</sub> = 0 or V+			0.01	1.0	μΑ	
Power Consumption	$P_{C}$	VIN - O OI VT				5.5	μW	

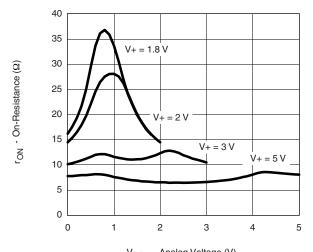
#### Notes:

- a. Room = 25 °C, Full = as determined by the operating suffix.
- b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- c. Typical values are for design aid only, not guaranteed nor subject to production testing.
- d. Guarantee by design, nor subjected to production test.
- e.  $V_{IN}$  = input voltage to perform proper function.
- f. Guaranteed by 5 V leakage testing, not production tested.

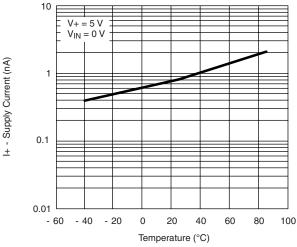
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



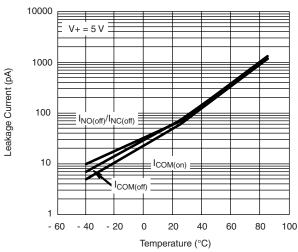
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



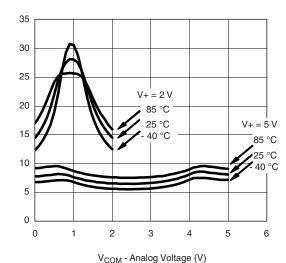
 $\label{eq:VCOM} \mbox{$^{\circ}$} V_{COM} \mbox{$^{\circ}$} - \mbox{$^{\circ}$} Analog \mbox{$Voltage$} (V) \\ \mbox{$^{\circ}$} r_{ON} \mbox{ $vs.$} V_{COM} \mbox{ and Supply Voltage}$ 



Supply Current vs. Temperature



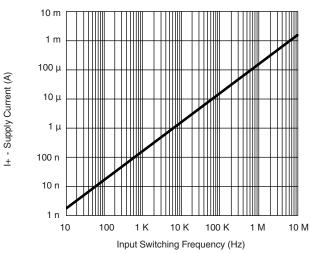
Leakage Current vs. Temperature



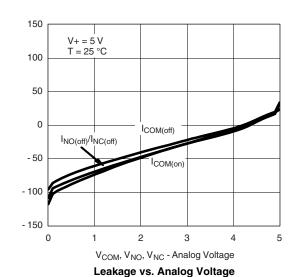
 $r_{\text{ON}}$  - On-Resistance ( $\Omega$ )

Leakage Current (pA)

r<sub>ON</sub> vs. Analog Voltage and Temperature

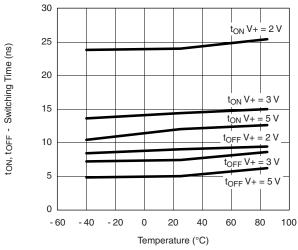


Supply Current vs. Input Switching Frequency

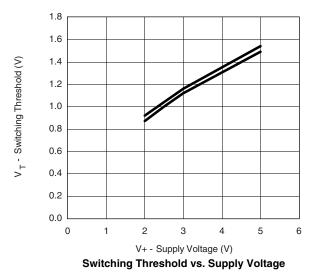


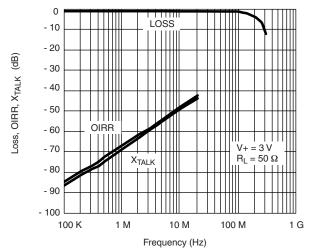
# VISHAY

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

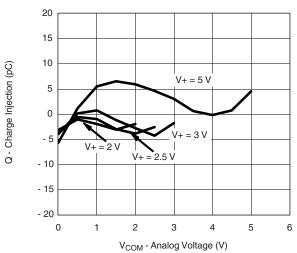


Switching Time vs. Temperature and Supply Voltage





Insertion Loss, Off -Isolation Crosstalk vs. Frequency





### **TEST CIRCUITS**

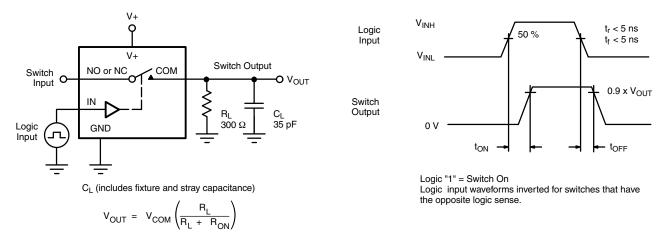


Figure 1. Switching Time

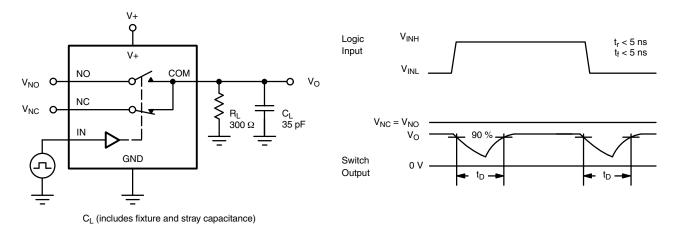


Figure 2. Break-Before-Make Interval

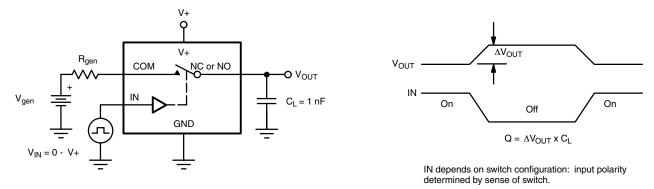


Figure 3. Charge Injection

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### **TEST CIRCUITS**

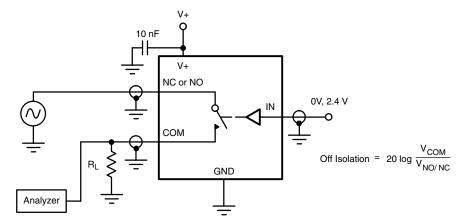


Figure 4. Off-Isolation

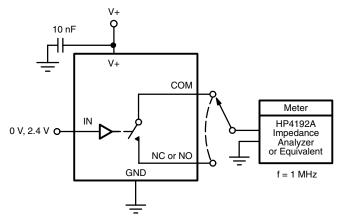


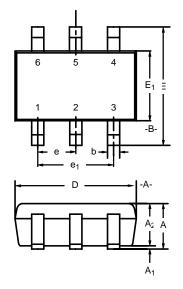
Figure 5. Channel Off/On Capacitance

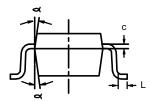
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?71448">http://www.vishay.com/ppg?71448</a>.





### **SC-70: 6-LEADS**





	MIL	LIMET	LIMETERS INCHES				
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.90	_	1.10	0.035	_	0.043	
<b>A</b> <sub>1</sub>	-	-	0.10	-	_	0.004	
A <sub>2</sub>	0.80	-	1.00	0.031	_	0.039	
b	0.15	-	0.30	0.006	_	0.012	
С	0.10	-	0.25	0.004	-	0.010	
D	1.80	2.00	2.20	0.071	0.079	0.087	
Ε	1.80	2.10	2.40	0.071	0.083	0.094	
E <sub>1</sub>	1.15	1.25	1.35	0.045	0.049	0.053	
е	0.65BSC				0.026BSC	)	
e <sub>1</sub>	1.20	1.30	1.40	0.047	0.051	0.055	
L	0.10	0.20	0.30	0.004	0.008	0.012	
٦	<b>♂</b> 7°Nom 7°Nom						
ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5550							



### **Legal Disclaimer Notice**

Vishay

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.