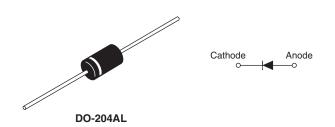


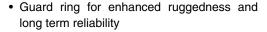
## Schottky Rectifier, 1.0 A



PRODUCT SUMMARY				
Package	DO-204AL (DO-41)			
I <sub>F(AV)</sub>	1 A			
$V_{R}$	20 V			
V <sub>F</sub> at I <sub>F</sub>	See Electrical table			
I <sub>RM</sub> max.	10 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
Diode variation	Single die			
E <sub>AS</sub>	See Electrical table			

#### **FEATURES**

- · Low profile, axial leaded outline
- · High frequency operation
- · Very low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for commercial level
- Halogen-free according to IEC 61249-2-21 definition (-M3 only)



HALOGEN

FREE

### **DESCRIPTION**

The VS-1N5817... axial leaded Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	1.0	Α		
V <sub>RRM</sub>		20	V		
I <sub>FSM</sub>	$t_p = 5 \mu s sine$	240	А		
V <sub>F</sub>	1 Apk, T <sub>J</sub> = 25 °C	0.45	V		
T <sub>J</sub>	Range	- 65 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-1N5817	VS-1N5817-M3	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	20	20	V
Maximum working peak reverse voltage	V <sub>RWM</sub>	20	20	v

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>L</sub> = 138 °C, rectangular waveform		1.0	
Maximum peak one cycle non-repetitive surge current at T <sub>J</sub> = 25 °C	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	240	Α
		10 ms sine or 6 ms rect. pulse		40	



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop	drop $V_{FM}^{(1)}$ $\frac{1 \text{ A}}{3 \text{ A}}$ $T_J = 25 \text{ °C}$	1 A	T - 25 °C	0.42	0.45	V
waximum lorward voltage drop		11 = 23 0	0.50	0.75	]	
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.012	1.0	mA
		T <sub>J</sub> = 100 °C		2.0	10	IIIA
Typical junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		110	-	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	-	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		-	10 000	V/µs

#### Note

 $^{(1)}\,$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 65 to 150	°C
Maximum thermal resistance, junction to lead	R <sub>thJL</sub>	DC operation Lead length = 1/8"	32	°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation Without cooling fin	100	C/VV
Approximate weight			0.33	g
Approximate weight			0.012	OZ.
Marking device		Case style DO-204AL (DO-41)	1N5	817

### Note

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

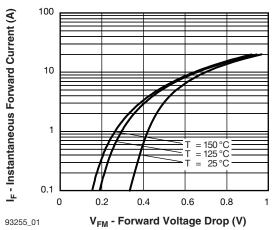


Fig. 1 - Maximum Forward Voltage Drop Characteristics

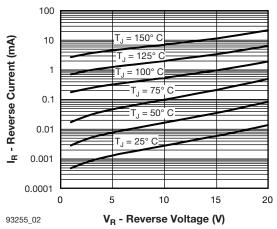


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

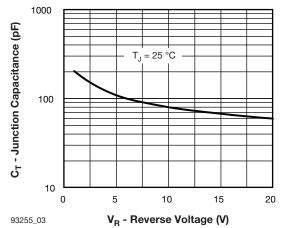
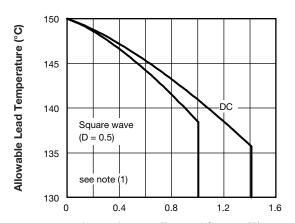


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



93255\_04 I<sub>F(AV)</sub> - Average Forward Current (A)

Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

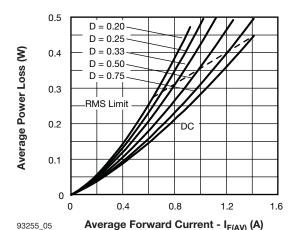
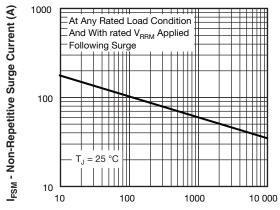


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current



93255\_06 t<sub>n</sub> - Square Wave Pulse Duration (µs)

Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

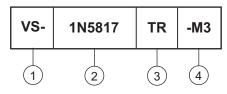
#### Note

Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$  (1 - D)



### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Part number: 1 A, 20 V

TR = Tape and reel package

None = Box package

- Environmental digit

• None = Lead (Pb)-free and RoHS compliant

• -M3 = Halogen-free, RoHS compliant, and terminations lead (Pb)-free

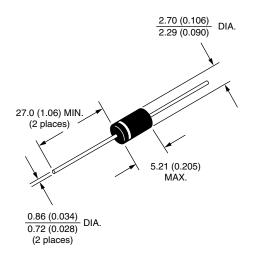
ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-1N5817	1000	1000	Bulk	
VS-1N8517TR	5000	5000	Tape and reel	
VS-1N5817-M3	1000	1000	Bulk	
VS-1N5817TR-M3	5000	5000	Tape and reel	

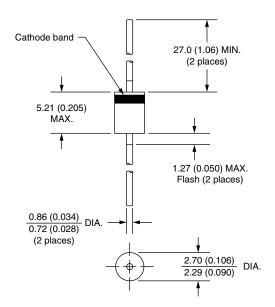
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95241			
Part marking information	www.vishay.com/doc?95304			
Packaging information	www.vishay.com/doc?95338			



# **Axial DO-204AL (DO-41)**

### **DIMENSIONS** in millimeters (inches)







### **Legal Disclaimer Notice**

Vishay

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