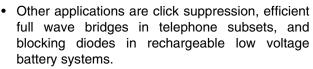


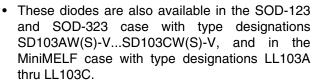
#### **Vishay Semiconductors**

# **Small Signal Schottky Diodes**

#### **Features**

- The SD103 series is a Metal-on-silicon Schottky barrier device which is protected by a PN junction guard ring.
- The low forward voltage drop and fast switching make it ideal for protection of MOS devices, steering, biasing and coupling diodes for fast switching and low logic level applications.





- · For general purpose applications
- AEC-Q101 qualified
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition





ROHS COMPLIANT HALOGEN FREE



#### **Applications**

- HF-Detector
- Protection circuit
- · Small battery charger
- AC-DC/DC-DC converters

#### **Mechanical Data**

Case: DO-35

Weight: approx. 125 mg
Cathode band color: black
Packaging codes/options:

TR/10 k per 13" reel (52 mm tape), 50 k/box TAP/10 k per Ammopack (52 mm tape), 50 k/box

#### **Parts Table**

Part	Type differentiation	Ordering code	Type Marking	Remarks
SD103A	V <sub>R</sub> = 40 V	SD103A-TR or SD103A-TAP	SD103A	Tape and Reel/Ammopack
SD103B	V <sub>R</sub> = 30 V	SD103B-TR or SD103B-TAP	SD103B	Tape and Reel/Ammopack
SD103C	V <sub>R</sub> = 20 V	SD103C-TR or SD103C-TAP	SD103C	Tape and Reel/Ammopack

#### **Absolute Maximum Ratings**

T<sub>amb</sub> = 25 °C, unless otherwise specified

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Parameter	Test condition	Part	Symbol	Value	Unit
		SD103A	V <sub>R</sub>	40	V
Peak inverse voltage		SD103B	V <sub>R</sub>	30	V
		SD103C	V <sub>R</sub>	20	V
Power dissipation (infinite heatsink)			P <sub>tot</sub>	400 <sup>1)</sup>	mW
Single cycle surge 60 Hz sine wave			I <sub>FSM</sub>	15	Α

<sup>1)</sup> Valid provided that leads at a distance of 4 mm from case are kept at ambient temperature

# SD103A, SD103B, SD103C

### **Vishay Semiconductors**



#### **Thermal Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit	
Thermal resistance junction to ambient air		$R_{thJA}$	310 <sup>1)</sup>	K/W	
Junction temperature		T <sub>j</sub>	125	°C	
Storage temperature range		T <sub>stg</sub>	- 55 to + 150	°C	

<sup>1)</sup> Valid provided that leads at a distance of 4 mm from case are kept at ambient temperature

#### **Electrical Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Min.	Тур.	Max.	Unit
Reverse Breakdown Voltage		SD103A	V <sub>(BR)</sub>	40			V
	I <sub>R</sub> = 50 μA	SD103B	V <sub>(BR)</sub>	30			V
		SD103C	V <sub>(BR)</sub>	20			V
Leakage current	V <sub>R</sub> = 30 V	SD103A	I <sub>R</sub>			5	μΑ
	V <sub>R</sub> = 20 V	SD103B	I <sub>R</sub>			5	μΑ
	V <sub>R</sub> = 10 V	SD103C	I <sub>R</sub>			5	μΑ
Forward voltage drop	I <sub>F</sub> = 20 mA		V <sub>F</sub>			370	mV
	I <sub>F</sub> = 200 mA		V <sub>F</sub>			600	mV
Diode capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		C <sub>D</sub>		50		pF
Reverse recovery time	$I_F = I_R = 50 \text{ to } 200 \text{ mA},$ recover to 0.1 $I_R$		t <sub>rr</sub>		10		ns

### **Typical Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

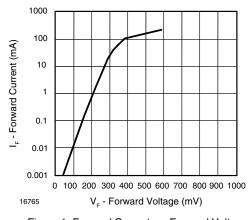


Figure 1. Forward Current vs. Forward Voltage

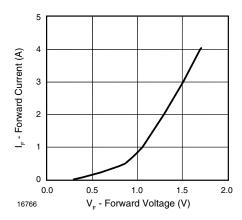


Figure 2. Forward Current vs. Forward Voltage



### **Vishay Semiconductors**

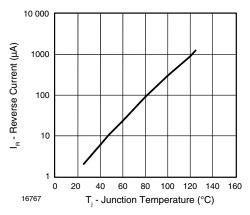


Figure 3. Reverse Current vs. Junction Temperature

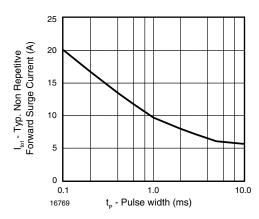


Figure 5. Typ. Non Repetitive Forward Surge Current vs.
Pulse Width

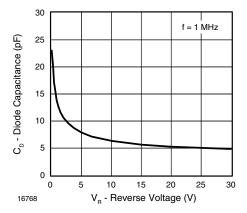
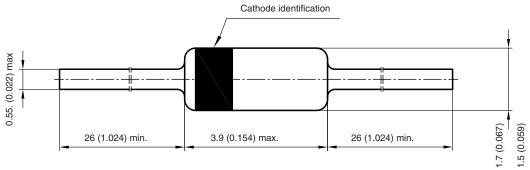


Figure 4. Diode Capacitance vs. Reverse Voltage

### Package Dimensions in millimeters (inches): DO-35



Rev. 6 - Date: 29. January 2007 Document no.: 6.560-5004.02-4

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### **Legal Disclaimer Notice**

Vishay

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