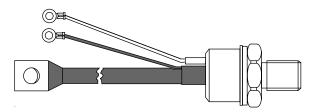




Vishay High Power Products

# Phase Control Thyristors (Stud Version), 180 A

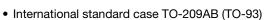


TO-209AB (TO-93)

PRODUCT SUMMARY	
I <sub>T(AV)</sub>	180 A

#### **FEATURES**







- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

#### **TYPICAL APPLICATIONS**

- DC motor controls
- Controlled DC power supplies
- AC controllers

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
1		180	А		
I <sub>T(AV)</sub>	T <sub>C</sub>	80	°C		
I <sub>T(RMS)</sub>		285			
	50 Hz	3800	A		
I <sub>TSM</sub>	60 Hz	4000			
I2t	50 Hz	72	1.42-		
1-1	60 Hz	66	kA <sup>2</sup> s		
V <sub>DRM</sub> /V <sub>RRM</sub>		400 to 1000	V		
tq	Typical	100	μѕ		
T <sub>J</sub>		- 40 to 125	°C		

#### **ELECTRICAL SPECIFICATIONS**

VOLTAGE RATINGS								
PART NUMBER	VOLTAGE CODE	V <sub>DRM</sub> /V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK VOLTAGE V	$I_{DRM}/I_{RRM}$ MAXIMUM AT $T_J = T_J$ MAXIMUM mA				
	40	400	500					
180RKI 181RKI	80	800	900	30				
	100	1000	1100					

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ABSOLUTE MAXIMUM RATINGS	3					
PARAMETER	SYMBOL		TEST CONDITIONS		VALUES	UNITS
Maximum average on-state current at case temperature	I <sub>T(AV)</sub>	180° conduc	180° conduction, half sine wave		180 80	A °C
Maximum RMS on-state current	I <sub>RMS</sub>	DC at 79 °C	case temperatu	re	285	0
	·Nivio	t = 10 ms	No voltage		3800	
Maximum peak, one-cycle		t = 8.3 ms	reapplied		4000	A kA <sup>2</sup> s
non-repetitive surge current	I <sub>TSM</sub>	t = 10 ms	100 % V <sub>RRM</sub>		3500	
		t = 8.3 ms	reapplied	Sinusoidal half wave, intial $T_J = T_J$ maximum	3660	
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	t = 10 ms	No voltage		72	
		t = 8.3 ms	reapplied		66	
		t = 10 ms	100 % V <sub>RRM</sub>		61	
		t = 8.3 ms	reapplied		56	
Maximum I <sup>2</sup> √t for fusing	I <sup>2</sup> √t	t = 0.1 ms to	10 ms, no volta	ge reapplied	720	kA²√s
Low level value of threshold voltage	V <sub>T(TO)1</sub>	(16.7 % x π x	$x \mid_{T(AV)} < l < \pi x \mid$	$T(AV)$ , $T_J = T_J$ maximum	0.83	V
High level value of threshold voltage	V <sub>T(TO)2</sub>	$(I > \pi \times I_{T(AV)}), T_J = T_J \text{ maximum}$		0.89	V	
Low level value of on-state slope resistance	r <sub>t1</sub>	(16.7 % x $\pi$ x $I_{T(AV)}$ < I < $\pi$ x $I_{T(AV)}$ ), $T_J = T_J$ maximum		0.92	<b>~</b> 0	
High level value of on-state slope resistance	r <sub>t2</sub>	$(I > \pi \times I_{T(AV)}), T_J = T_J \text{ maximum}$		0.81	mΩ	
Maximum on-state voltage	$V_{TM}$	$I_{pk} = 570 \text{ A}, T_J = T_J \text{ maximum, } t_p = 10 \text{ ms sine pulse}$		1.35	V	
Maximum holding current	lΗ	T 25 °C o	node supply 12	V registive load	600	mA
Typical latching current	ΙL	] IJ=25 0, a	T <sub>J</sub> = 25 °C, anode supply 12 V resistive load		1000	IIIA

SWITCHING					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum non-repetitive rate of rise of turned-on current	dl/dt	Gate drive 20 V, 20 $\Omega$ , $t_r \le 1~\mu s$ $T_J = T_J$ maximum, anode voltage $\le 80~\%$ $V_{DRM}$	300	A/µs	
Typical delay time	t <sub>d</sub>	Gate current 1 A, $dl_g/dt = 1 A/\mu s$ $V_d = 0.67 \% V_{DRM}$ , $T_J = 25 °C$	1.0		
Typical turn-off time	t <sub>q</sub>	$I_{TM} = 50 \text{ A}, T_J = T_J \text{ maximum, dl/dt} = 10 \text{ A/}\mu\text{s},$ $V_R = 100 \text{ V}, \text{ dV/dt} = 20 \text{ V/}\mu\text{s}$	100	μs	

BLOCKING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum critical rate of rise of off-state voltage	dV/dt	T <sub>J</sub> = T <sub>J</sub> maximum linear to 80 % rated V <sub>DRM</sub>	500	V/µs
Maximum peak reverse and off-state leakage current	I <sub>RRM,</sub> I <sub>DRM</sub>	$T_J = T_J$ maximum rated $V_{DRM}/V_{RRM}$ applied	30	mA



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TRIGGERING						
PARAMETER	SYMBOL	TEST COMPLIANS		VALUES		LINUTO
PARAMETER	STINIBUL	<b>'</b>	TEST CONDITIONS	TYP.	MAX.	UNITS
Maximum peak gate power	P <sub>GM</sub>	$T_J = T_J$ maximum, $t_p \le 5$ ms		1	0	W
Maximum average gate power	P <sub>G(AV)</sub>	$T_J = T_J$ maximum,	f = 50 Hz, d% = 50	2	.0	VV
Maximum peak positive gate current	I <sub>GM</sub>			3	.0	Α
Maximum peak positive gate voltage	+ V <sub>GM</sub>	$T_J = T_J$ maximum,	$T_J = T_J$ maximum, $t_p \le 5$ ms		.0	V
Maximum peak negative gate voltage	- V <sub>GM</sub>					V
DC gate current required to trigger I <sub>C</sub>	I <sub>GT</sub>	T <sub>J</sub> = - 40 °C	Maximum required gate trigger/ current/voltage are the lowest value which will trigger all units 12 V anode to cathode applied	130	-	mA
		T <sub>J</sub> = 25 °C		65	150	
		T <sub>J</sub> = 125 °C		35	-	
		T <sub>J</sub> = - 40 °C		2.0	-	
DC gate voltage required to trigger	$V_{GT}$	T <sub>J</sub> = 25 °C		1.2	2.5	V
		T <sub>J</sub> = 125 °C		0.9	-	
DC gate current not to trigger	I <sub>GD</sub>	T. T. man a visina visna	Maximum gate current/voltage not to trigger is the maximum value which will not trigger any unit with rated V <sub>DRM</sub> anode to cathode applied	1	0	mA
DC gate voltage not to trigger	$V_{GD}$	$T_J = T_J$ maximum		0.25		V

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum operating junction temperature range	T <sub>J</sub>		- 40 to 125	°C	
Maximum storage temperature range	T <sub>Stg</sub>		- 40 to 150		
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation	0.15	K/W	
Maximum thermal resistance, junction to ambient	R <sub>thCS</sub>	Mounting surface, smooth, flat and greased	0.04	r√ vv	
Mounting force, ± 10 %	Non-lubricated threads		31 (275)	N·m	
Mounting force, ± 10 %		Lubricated threads	24.5 (210)	(lbf · in)	
Approximate weight			280	g	
Case style		See dimensions - link at the end of datasheet	TO-209AB (	(TO-93)	

△R <sub>thJC</sub> CONDUCTION	1			
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS
180°	0.050	0.032		
120°	0.063	0.059		
90°	0.080	0.082	$T_J = T_J \text{ maximum}$	K/W
60°	0.118	0.124		
30°	0.225	0.228		

#### Note

• The table above shows the increment of thermal resistance R<sub>thJC</sub> when devices operate at different conduction angles than DC

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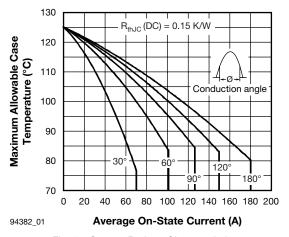


Fig. 1 - Current Ratings Characteristics

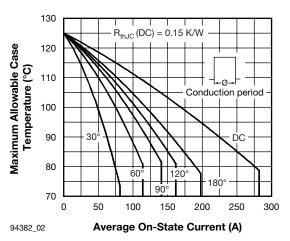
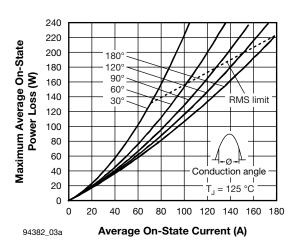
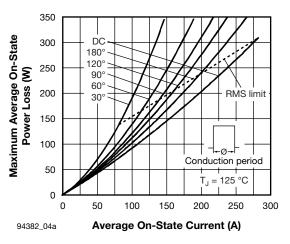


Fig. 2 - Current Ratings Characteristics



240 220 Maximum Average On-State 200 180 Power Loss (W) 160 140 120 100 80 60 40 20 0 0 25 50 75 100 125 **Maximum Allowable** 94382 03b Ambient Temperature (°C)

Fig. 3 - On-State Power Loss Characteristics



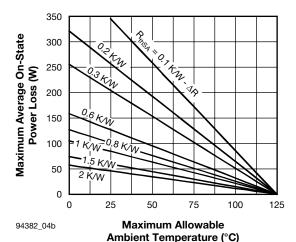


Fig. 4 - On-State Power Loss Characteristics





Phase Control Thyristors Vishay H (Stud Version), 180 A

Vishay High Power Products

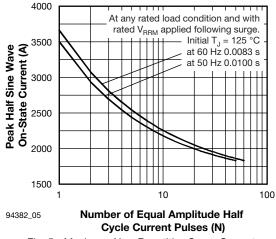


Fig. 5 - Maximum Non-Repetitive Surge Current

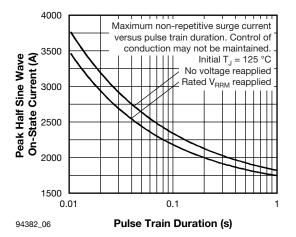


Fig. 6 - Maximum Non-Repetitive Surge Current

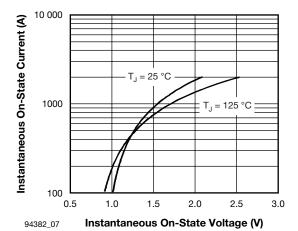


Fig. 7 - On-State Voltage Drop Characteristics

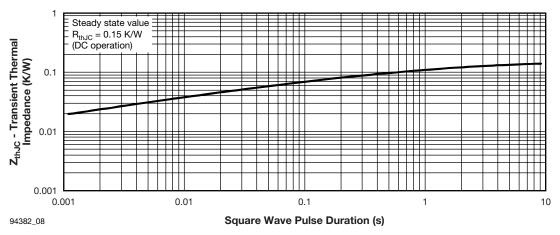


Fig. 8 - Thermal impedance  $Z_{thJC}$  Characteristics

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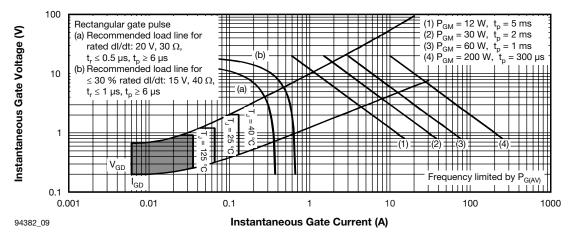
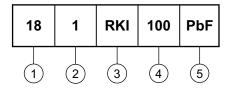


Fig. 9 - Gate Characteristics

#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 I<sub>T(AV)</sub> rated average output current (rounded/10)
  - • 0 = Eyelet terminals (gate and auxiliary cathode leads)
    - 1 = Fast-on terminals (gate and auxiliary cathode leads)
- |**3|** Thyristo
- Voltage code x 10 = V<sub>RRM</sub> (see Voltage Ratings table)
- 5 None = Standard production
  - PbF = Lead (Pb)-free

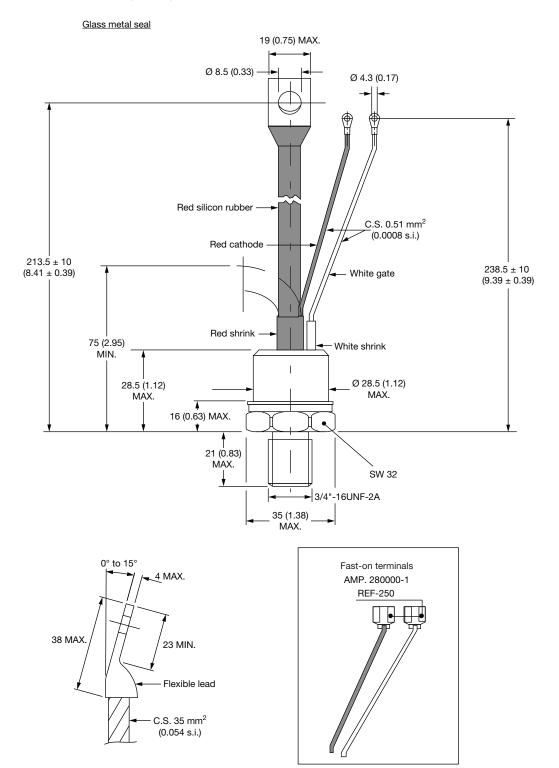
LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95077	



Vishay Semiconductors

### TO-209AB (TO-93)

#### **DIMENSIONS** in millimeters (inches)





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