



General Description

The MAX7453 is a low-cost, triple-channel video reconstruction filter for S-video and CVBS video-signal applications. This device is ideal for anti-aliasing and DAC smoothing video applications such as set-top boxes, DVD players, hard-disk recorders (HDRs), and personal video recorders. This filter operates from a single +5V supply. It is optimized for NTSC, PAL, and standard-definition TV (SDTV) video systems.

Each channel consists of a lowpass filter and an output buffer capable of driving one (YOUT, COUT) or two (CVOUT) standard 150 Ω video loads. The output buffers can drive either an AC or a DC load such that the blanking level voltage after the backmatch resistor is less than 1V. The Y and C video inputs are summed to produce the composite video output. The MAX7453 has a maximally flat frequency response.

The MAX7453 is available in an 8-pin SO package and is fully specified over the commercial (0°C to +70°C) temperature range.

Applications

Set-Top Boxes/HDRs/DVDs

Security Cameras/Digital Cameras

Game Consoles

Digital VCRs

Every Composite, S-Video Output for NTSC,

PAL, and SDTV

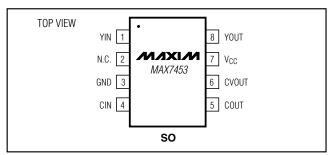
Features

- **♦ Triple-Channel Filter and Buffer for CVBS and** Y/C (S-Video) Video Signals
- ♦ Filter Response Ideal for NTSC, PAL, and **Interlaced SDTV Signals**
- 42dB (typ) Stopband Attenuation at 27MHz
- ◆ -0.3dB (typ) Passband Response
- ♦ <1V Blanking Level Allows DC-Coupled Output
 </p>
- ♦ CVOUT Drives Two 150Ω Video Loads
- ♦ YOUT and COUT Drive One 150Ω Video Load
- ♦ +5V Single-Supply Voltage
- ♦ 8-Pin SO Package

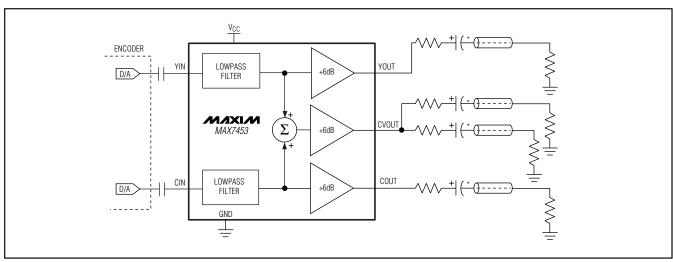
Ordering Information

PAR	Т	TEMP RANGE	PIN-PACKAGE
MAX745	3CSA	0°C to +70°C	8 SO

Pin Configuration



Functional Diagram



NIXIN

Maxim Integrated Products 1

ABSOLUTE MAXIMUM RATINGS

V _{CC} to GND+6V	Operating Temperature Range
All Other Pins to GND0.3V to (V _{CC} + 0.3V) Maximum Current into Any Pin Except V _{CC} and GND±50mA	Storage Temperature Range65°C to +150°C Junction Temperature+150°C
Continuous Power Dissipation ($T_A = +70^{\circ}C$)	Lead Temperature (soldering, 10s)+300°C
8-Pin SO 522 8mW	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

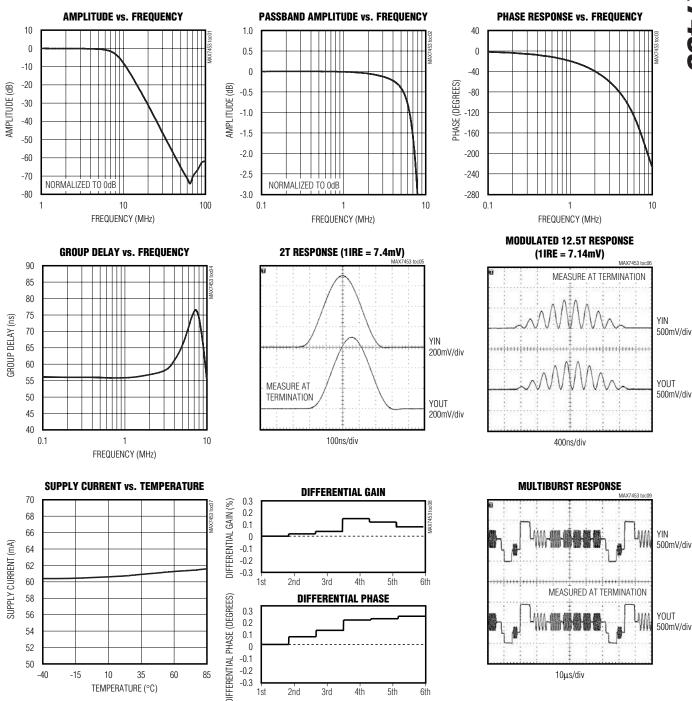
ELECTRICAL CHARACTERISTICS

 $(V_{CC} = +5V \pm 5\%, C_L = 0 \text{ to } 20\text{pF}, \text{ all loads to GND for DC-couple, all loads to } V_{CC} / 2 \text{ for AC-couple, } C_{YIN} = C_{CIN} = 0.1 \mu\text{F}, R_{YOUT} = 150\Omega, R_{COUT} = 150\Omega, R_{CVOUT} = 75\Omega, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}\text{C.})$

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Passband Response		f = 100kHz to 4.2MHz, relative to 100kHz	-0.6	-0.27	0	dB	
Stopband Attenuation	AsB	f = 27MHz	39	42		dB	
Chroma/Luma Gain		f = 3.58MHz, reference to CVOUT at 336kHz	96	98	100	%	
Differential Gain	dG	5-step modulated staircase		0.15	0.5	%	
Differential Phase	dθ	5-step modulated staircase		0.15	0.5	Degrees	
Signal-to-Noise Ratio	SNR	Peak signal (2V _{P-P}) to RMS noise, f = 100Hz to 50MHz	67	73		dB	
Line-Time Distortion	H _{DIST}	18µs, 100IRE bar			0.3	%	
Field-Time Distortion	V _{DIST}	130 lines, 18µs, 100IRE bar			0.5	%	
Clamp Settling Time		To ±1%		300		Lines	
		YOUT	0.60	0.8	0.95	95 V	
Output DC Clamp Level		CVOUT	0.60	0.8	0.95		
		COUT	1.44	1.6	1.95		
Low-Frequency Gain Accuracy	Ay	f = 100kHz, relative to 6dB	-3.0		+2.0	%	
Group-Delay Deviation	Δt_{g}	Deviation from 100kHz to 4.1MHz		8	16	ns	
Group-Delay Matching	tg(MATCH)	Low-frequency channel-to-channel matching, f = 100kHz for YOUT and COUT		2		ns	
Channel-to-Channel Crosstalk	XTALK	f = 100kHz to 3.58MHz		61		dB	
Input Leakage Current	I _{IN}				10	μΑ	
Input Dynamic Swing	Y _{INP-P}				1.2	V _{P-P}	
Imput Dynamic Swing	C _{INP-P}				0.9		
SUPPLY							
Supply Voltage Range	Vcc		4.75		5.25	V	
Supply Current	Icc	$V_{CC} = +5.25V$, no load		65	85	mA	
Power-Supply Rejection Ratio	PSRR	$V_{IN} = 100 \text{mV}_{P-P}, f = 0 \text{ to } 3.5 \text{MHz}$		33		dB	

Typical Operating Characteristics

 $(V_{CC} = +5V, T_A = +25^{\circ}C, unless otherwise noted.)$



-0.1

-0.2

-0.3

3rd

4th

5th

TEMPERATURE (°C)

50

10µs/div

Pin Description

PIN	NAME	FUNCTION	
1	YIN	Luma (Y) Input	
2	N.C.	No Connection. No internal connection.	
3	GND	Ground	
4	CIN	Chroma (C) Input	
5	COUT	Chroma (C) Output	
6	CVOUT	Composite Video Output	
7	Vcc	5V Power Supply	
8	YOUT	Luma (Y) Output	

Detailed Description

The primary function of the MAX7453 is to filter and buffer the encoder DAC outputs that contain digital video information in applications such as set-top boxes, HDRs, DVD players, and digital VCRs. This device reconstructs and cleans up analog video signals. The MAX7453 consists of a lowpass filter and a +6dB-gain output video buffer. The luma (YIN) and chroma (CIN) video inputs are summed internally to provide the composite (CVOUT) video output.

These devices operate from a single +5V supply. The filters have a nominal cutoff frequency optimized for NTSC, PAL, and SDTV.

Filter

Filter Response

The reconstruction filter consists of two 2nd-order Sallen-Key stages. The Butterworth-type response features a maximally flat passband for NTSC and PAL bandwidths. The stopband offers 42dB (typ) of attenuation at sampling frequencies of 27MHz and above (see the *Typical Operating Characteristics*).

Output Buffer

The YOUT and COUT buffers have a fixed +6dB gain capable of driving one standard 150 Ω video load with a 2VP-P signal. The CVOUT buffer has a fixed +6dB gain, capable of driving two 150 Ω video loads. The MAX7453 can drive an AC load or drive the video load directly without using the large output capacitor. The output buffer of all the channels drives a DC load with an output blanking level of less than 1V.

Output Clamp Level

When sync pulses in the luma signal (Y) are detected, the DC restore loop is activated. The function of the loop is to set the sync tip of the video signal to the desired DC level of 0.8V for YOUT and COUT, and to set the average DC voltage of COUT at 1.6V.

_Applications Information

Input Considerations

Use a $0.1\mu F$ ceramic capacitor to AC-couple the input to the MAX7453. This input capacitor stores a DC level such that the outputs are clamped to the appropriate DC voltage level.

Output Considerations

The outputs of the MAX7453 are typically connected to a 75Ω series back-match resistor followed by the video cable. Because of the inherent divide-by-two of this configuration, the blanking level of the video cable is always less than 1V, which complies with industry-standard video requirements. The video buffer can also drive an AC-coupled video load. Good video performance is achieved with an output capacitor as low as $220\mu F$.

Power-Supply Bypassing and Layout

The MAX7453 operates from a single +5V supply. Bypass VCC to GND with a $0.1\mu F$ capacitor. Place all external components as close to the device as possible. The maximum power dissipation listed in the *Absolute Maximum Ratings* section assumes that the PC board is a multilayer board with ground and power planes.

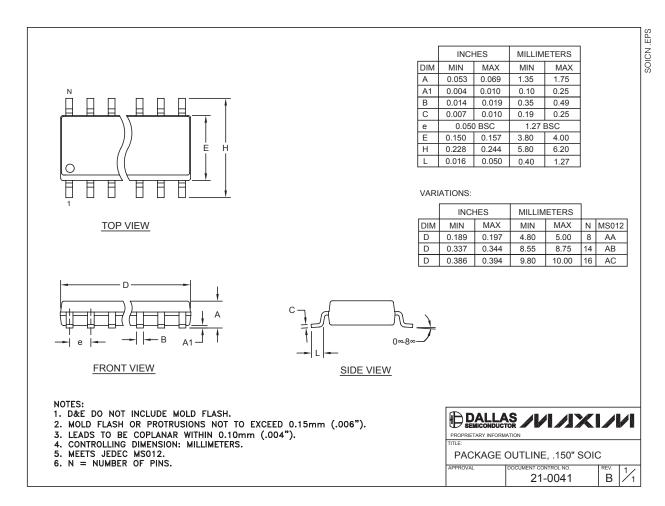
Chip Information

TRANSISTOR COUNT: 4100

PROCESS: BiCMOS

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)



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