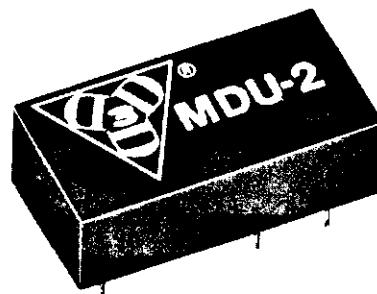


Multiple Digital Delay Units

SERIES: MDU-2
(14 pins DIP) TTL Interfaced

data delay devices, inc.



Features:

- Completely interfaced for TTL & DTL application.
- P.C. board space economy achieved.
- Fits standard 14 pins DIP socket.
- 2 equal delay units/package.

Specifications:

- Delay tolerance: $\pm 5\%$ or 2 ns whichever is greater.
- Rise-time: 4 ns typically.
- Temperature coefficient: 100 PPM/ $^{\circ}\text{C}$.
- Temperature range: 0° to $+70^{\circ}\text{C}$.
- Supply voltage: 5 Vdc $\pm 5\%$.
- Input signal requirement: TTL logic.
- Input pulse width: Min. 100% of total delay.
- Input rep. rate: Min. $3 \times$ pulse width.
- Output fan-out: TTL Schottky loads.
- Power Dissipation: 500 mw max.

Part No.	Delay Per Line (ns)
MDU-2-5	5
MDU-2-10	10
MDU-2-15	15
MDU-2-20	20
MDU-2-25	25
MDU-2-30	30
MDU-2-35	35
MDU-2-40	40
MDU-2-45	45
MDU-2-50	50
MDU-2-60	60
MDU-2-75	75
MDU-2-100	100
MDU-2-125	125
MDU-2-150	150
MDU-2-200	200
MDU-2-250	250

Other delay times available on request.

Test Conditions:

- Input Pulse Width: $>150\%$ of total delay.
- Time delay measured @ 1.5 V on rising edge.
- Rise-time is measured from .75 V to 2.4 V of leading time.
- All measurements made @ $V_{cc} = 5\text{V}$; $T_A = +25^{\circ}\text{C}$; no loads on taps.

