

Fast Logic Delayed Pulse Generator

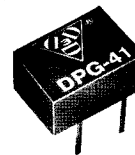
SERIES: DPG-41

**T²L Interfaced
8 pin DIP**

**data
delay
devices, inc.**

Features:

- Low Cost.
- Completely Interfaced for TTL.
- Low profile.
- Fits standard 8 pins DIP socket.



Specifications:

- Delay tolerance: see table.
- Rise-time: 2 ns typ.
- Minimum pulse width: 10 ns.
- Temperature range: 0°C to + 70°C.
(-55°C to + 125°C on request)*
- Temperature coefficient: 100 PPM/°C.
- Supply voltage: 4.75 to 5.25 Vdc.
- Input signal requirement: TTL logic.
- Output fan-out: TTL Schottky loads.
- Power dissipation: 225 mw typ.

*Add "M" after Part No. Example: DPG-41-10-25M

Part Number	Pulse Width (In ns)
DPG-41-()-5	5 ± 1
DPG-41-()-6	6 ± 1
DPG-41-()-7	7 ± 1
DPG-41-()-8	8 ± 1
DPG-41-()-9	9 ± 1
DPG-41-()-10	10 ± 1
DPG-41-()-15	15 ± 1
DPG-41-()-20	20 ± 1
DPG-41-()-25	25 ± 1
DPG-41-()-30	30 ± 1
DPG-41-()-35	35 ± 1
DPG-41-()-40	40 ± 1.5
DPG-41-()-45	45 ± 1.5
DPG-41-()-50	50 ± 1.5
DPG-41-()-60	60 ± 1.5
DPG-41-()-70	70 ± 2
DPG-41-()-75	75 ± 2
DPG-41-()-80	80 ± 2
DPG-41-()-90	90 ± 3
DPG-41-()-100	100 ± 3

Note: Insert delay time in the P/N.
Ex. DPG-41-30-35.

