

Provisional Data

Wespack Rectifier Diode

Types W3864QK100 to W3864QK180

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{RRM}	Repetitive peak reverse voltage, (note 1)	1000–1800	V
V_{RSM}	Non-repetitive peak reverse voltage, (note 1)	1100–1900	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
$I_{F(AV)M}$	Maximum average forward current, $T_{sink}=55^{\circ}C$, (note 2)	3864	A
$I_{F(AV)M}$	Maximum average forward current, $T_{sink}=100^{\circ}C$, (note 2)	2875	A
$I_{F(AV)M}$	Maximum average forward current, $T_{sink}=100^{\circ}C$, (note 3)	1599	A
$I_F(RMS)$	Nominal RMS forward current, $T_{sink}=25^{\circ}C$, (note 2)	6965	A
$I_F(d.c.)$	D.C. forward current, $T_{sink}=25^{\circ}C$, (note 4)	6013	A
I_{FSM}	Peak non-repetitive surge $t_p=10ms$, $V_{rm}=60\%V_{RRM}$, (note 5)	22.2	kA
I_{FSM2}	Peak non-repetitive surge $t_p=10ms$, $V_{rm}\leq 10V$, (note 5)	24.4	kA
I^2t	I^2t capacity for fusing $t_p=10ms$, $V_{rm}=60\%V_{RRM}$, (note 5)	2.46×10^6	A^2s
I^2t	I^2t capacity for fusing $t_p=10ms$, $V_{rm}\leq 10V$, (note 5)	2.98×10^6	A^2s
$T_{j\ op}$	Operating temperature range	-40 to +180	$^{\circ}C$
T_{stg}	Storage temperature range	-55 to +180	$^{\circ}C$

Notes:-

- 1) De-rating factor of 0.13% per $^{\circ}C$ is applicable for T_j below $25^{\circ}C$.
- 2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
- 3) Cathode side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) Double side cooled.
- 5) Half-sinewave, $180^{\circ}C$ T_j initial.

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V _{FM}	Maximum peak forward voltage	-	-	1.20	I _{FM} =3000A	V
V _{FM}	Maximum peak forward voltage	-	-	1.81	I _{FM} =9000A	V
V _{T0}	Threshold voltage	-	-	0.861		V
r _T	Slope resistance	-	-	0.109		mΩ
I _{RRM}	Peak reverse current	-	-	50	Rated V _{RRM}	mA
Q _{rr}	Recovered charge	-	2200	2400		μC
Q _{ra}	Recovered charge, 50% chord	-	1650	-	I _{TM} =1000A, t _p =1000μs, di/dt=10A/μs, V _r =50V	μC
I _{rm}	Reverse recovery current	-	150	-		A
t _{rr}	Reverse recovery time, 50% chord	-	20	-		μs
R _{thJK}	Thermal resistance, junction to heatsink	-	-	0.0170	Double side cooled	K/W
		-	-	0.0303	Anode side cooled	K/W
		-	-	0.0387	Cathode side cooled	K/W
F	Mounting force	16	-	20	Note 2	kN
W _t	Weight	-	200	-		g

Notes:-

- 1) Unless otherwise indicated T_j=180°C.
- 2) For other clamp forces, please consult factory.

Notes on Ratings and Characteristics**1.0 Voltage Grade Table**

Voltage Grade	V_{RRM} V	V_{RSM} V	V_R DC V
10	1000	1100	700
14	1400	1500	930
18	1800	1900	1150

2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_j below 25°C.

4.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

5.0 Computer Modelling Parameters**5.1 Device Dissipation Calculations**

$$I_{AV} = \frac{-V_{T0} + \sqrt{V_{T0}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T} \quad \text{and:} \quad W_{AV} = \frac{\Delta T}{R_{th}}$$

$$\Delta T = T_{j\max} - T_K$$

Where $V_{T0}=0.861V$, $r_T=0.109m\Omega$,

R_{th} = Supplementary thermal impedance, see table below and

ff = Form factor, see table below.

Supplementary Thermal Impedance				
Conduction Angle	6 phase (60°)	3 phase (120°)	½ wave (180°)	d.c.
Square wave Double Side Cooled	0.0231	0.0207	0.0192	0.0170
Square wave Cathode Side Cooled	0.0417	0.0408	0.0398	0.0387
Sine wave Double Side Cooled	0.0208	0.0181	0.0170	
Sine wave Cathode Side Cooled	0.0404	0.0396	0.0387	

Form Factors				
Conduction Angle	6 phase (60°)	3 phase (120°)	½ wave (180°)	d.c.
Square wave	2.449	1.732	1.414	1
Sine wave	2.778	1.879	1.57	

5.2 Calculating V_F using ABCD Coefficients

The on-state characteristic I_F vs. V_F , on page 6 is represented in two ways;

- (i) the well established V_{T0} and r_F tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for V_F in terms of I_F given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for V_F agree with the true device characteristic over a current range, which is limited to that plotted.

25°C Coefficients		180°C Coefficients	
A	1.619194	A	1.310311
B	-0.190117	B	-0.179583
C	-5.461467×10^{-5}	C	-4.416532×10^{-5}
D	0.023874	D	0.026656

5.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}} \right)$$

Where $p = 1$ to n , n is the number of terms in the series and:

t = Duration of heating pulse in seconds.

r_t = Thermal resistance at time t .

r_p = Amplitude of p_{th} term.

τ_p = Time Constant of r_{th} term.

The coefficients for this device are shown in the tables below.

D.C. Double Side Cooled				
Term	1	2	3	4
r_p	0.01054152	4.166135×10^{-3}	9.048202×10^{-4}	1.404721×10^{-3}
τ_p	0.2322298	0.05315938	0.0151575	2.630485×10^{-3}

D.C. Cathode Side Cooled			
Term	1	2	3
r_p	0.02947555	7.046786×10^{-3}	2.102936×10^{-3}
τ_p	1.276137	0.0795146	3.881676×10^{-3}

6.0 Reverse recovery ratings

(i) Q_{ra} is based on 50% I_{RM} chord as shown in Fig. 1

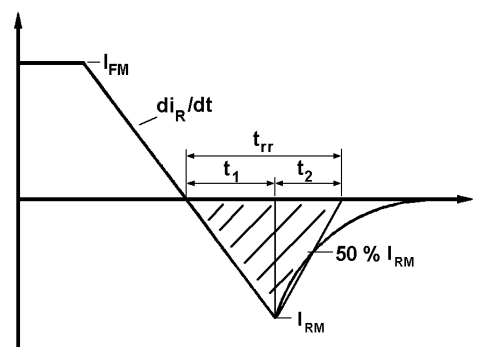


Fig. 1

(ii) Q_{rr} is based on a 150 μ s integration time i.e.

$$Q_{rr} = \int_0^{150\mu s} i_{rr} \cdot dt$$

(iii)

$$K \text{ Factor} = \frac{t_1}{t_2}$$

Curves

Figure 1 – Forward characteristics of Limit device

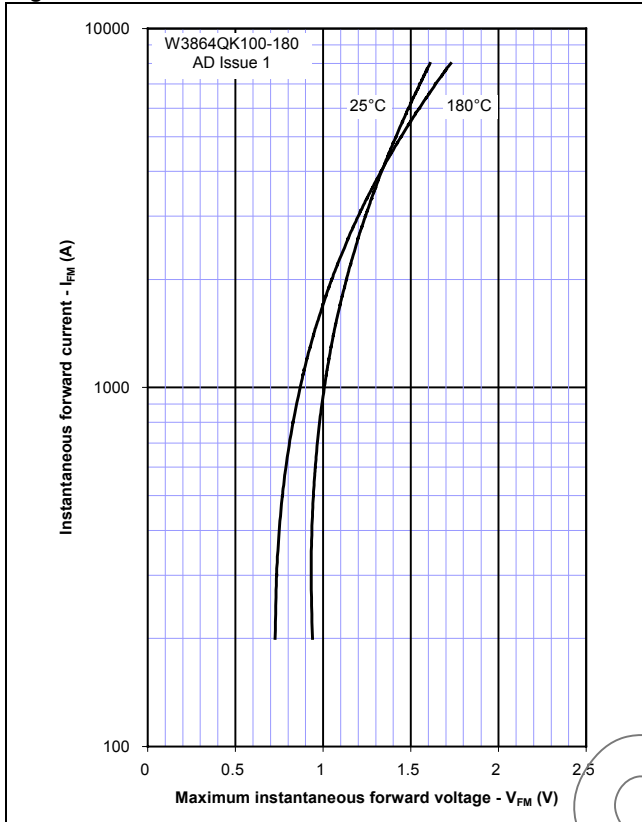


Figure 2 – Transient thermal impedance

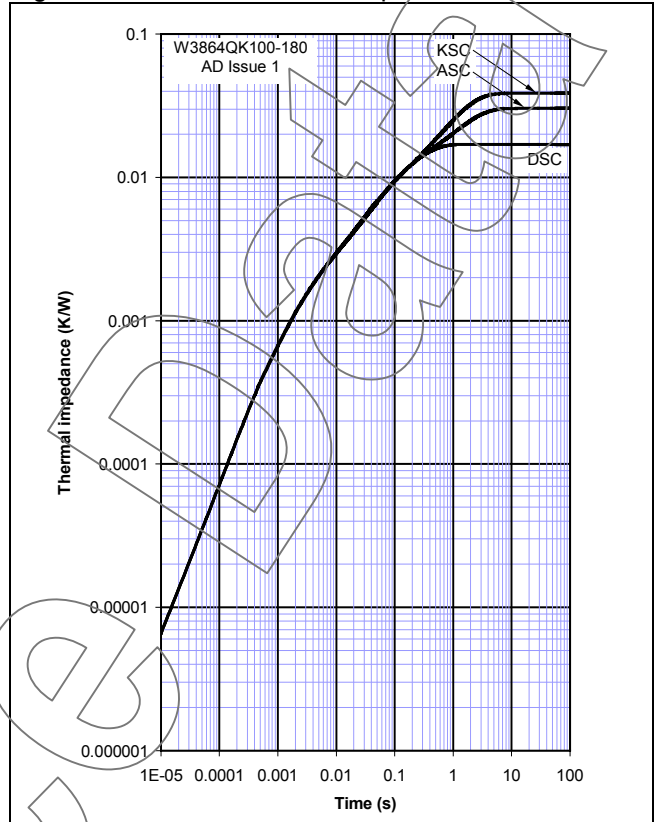


Figure 3 – Maximum Surge Rating

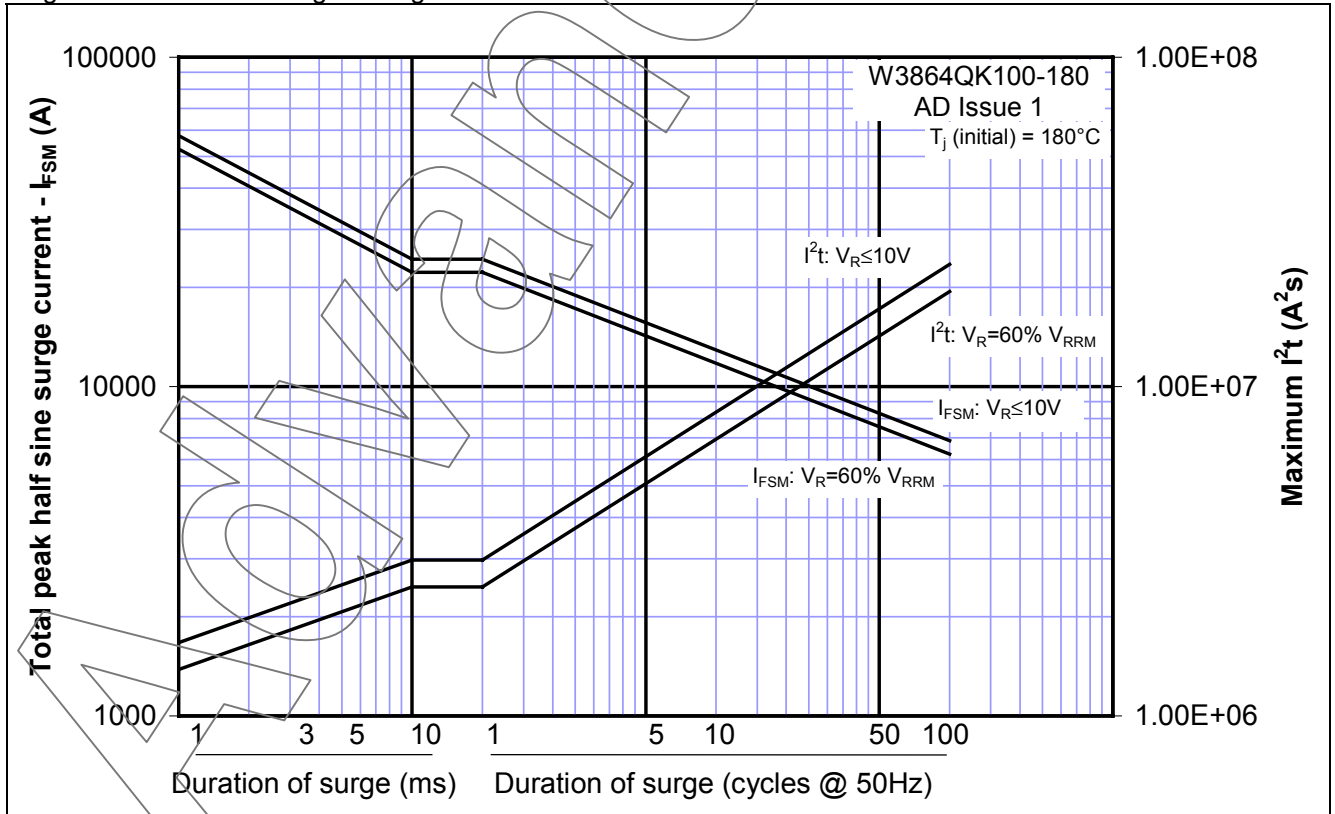


Figure 4 – Total recovered charge, Q_{rr}

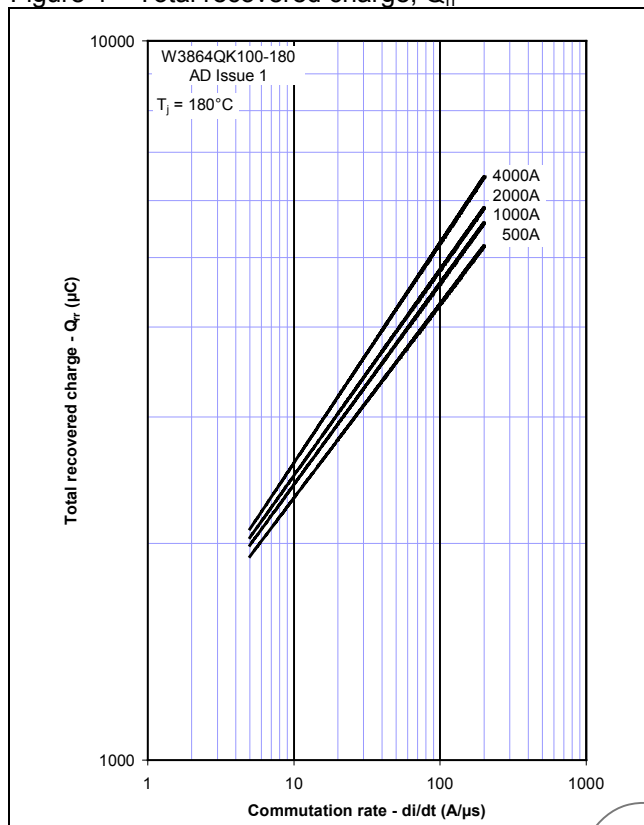


Figure 5 – Recovered charge, Q_{ra} (50% chord)

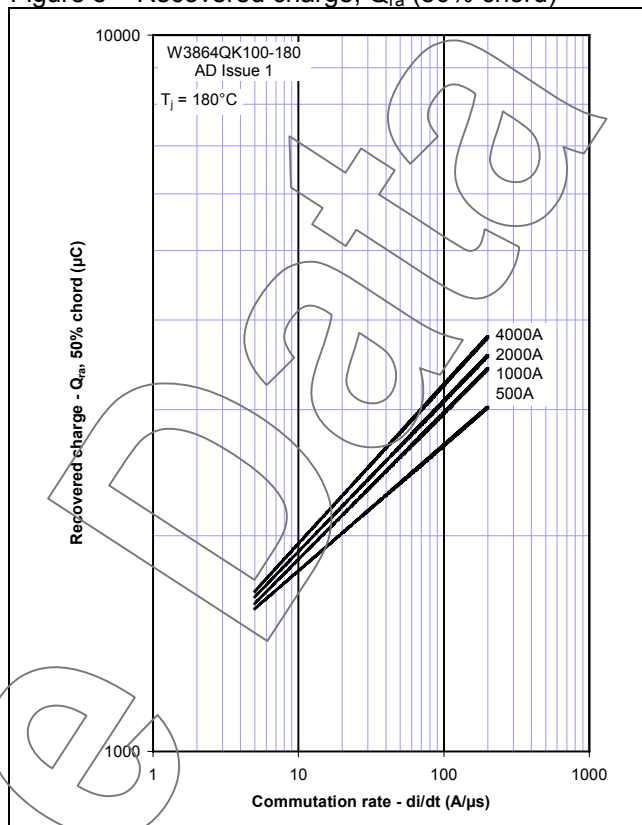


Figure 6 – Peak reverse recovery current, I_{rm}

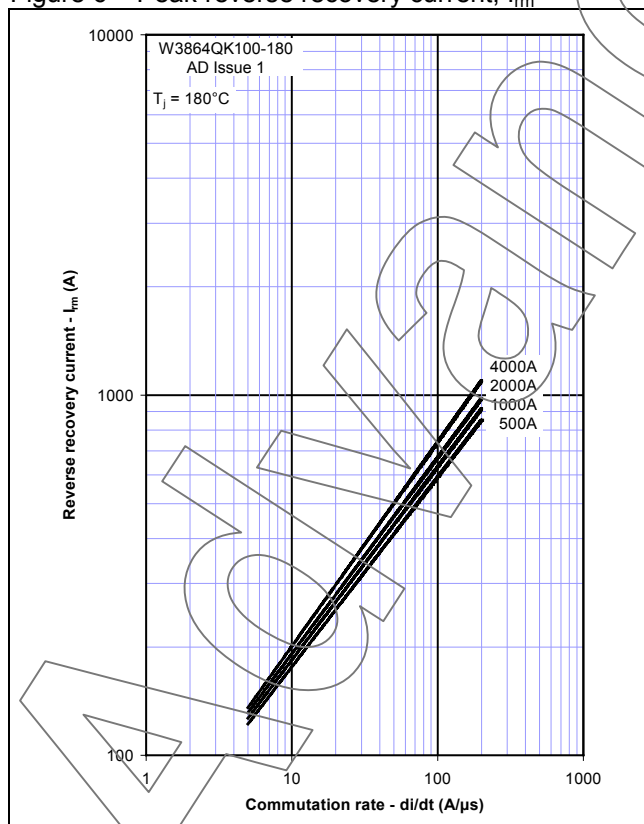


Figure 7 – Maximum recovery time, t_{rr} (50% chord)

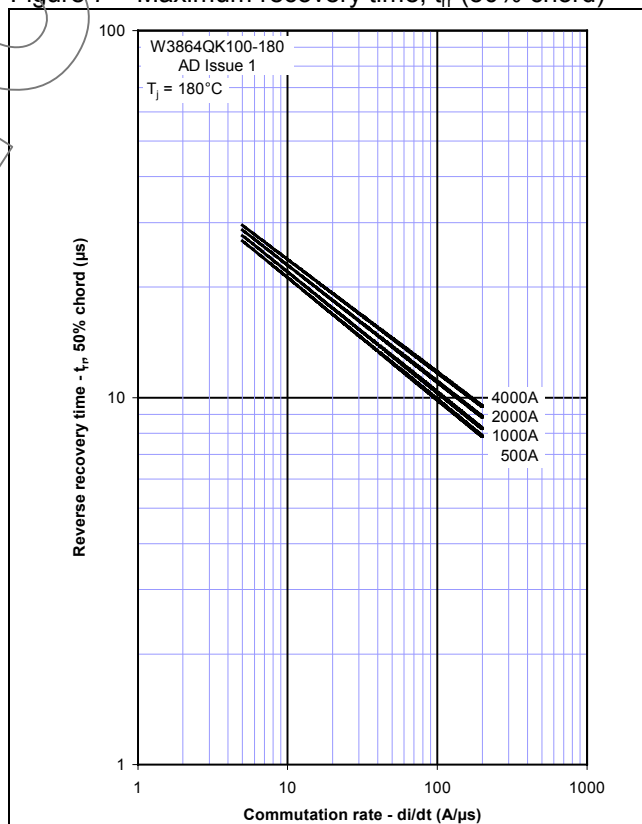


Figure 8 – Forward current vs. Power dissipation – Double Side Cooled

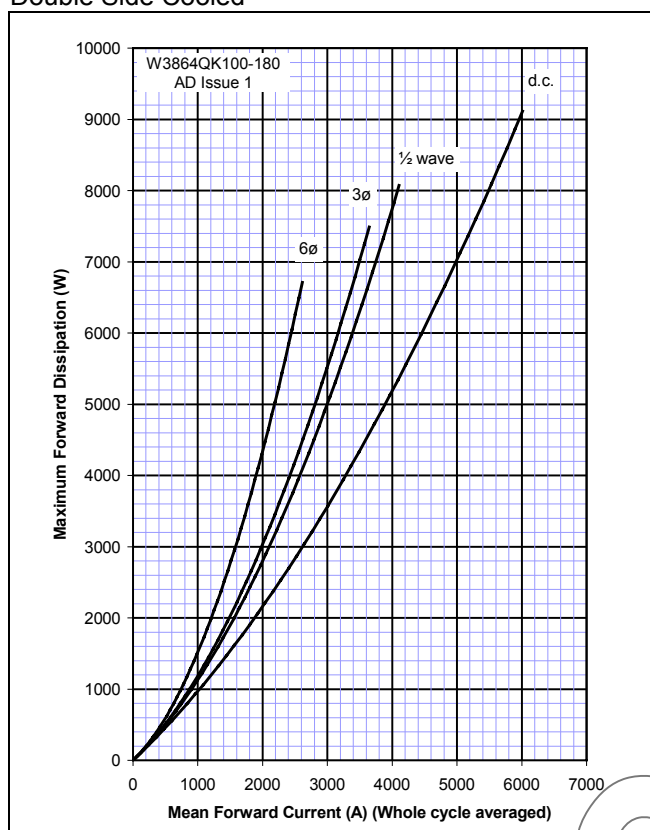


Figure 9 – Forward current vs. Heatsink temperature – Double Side Cooled

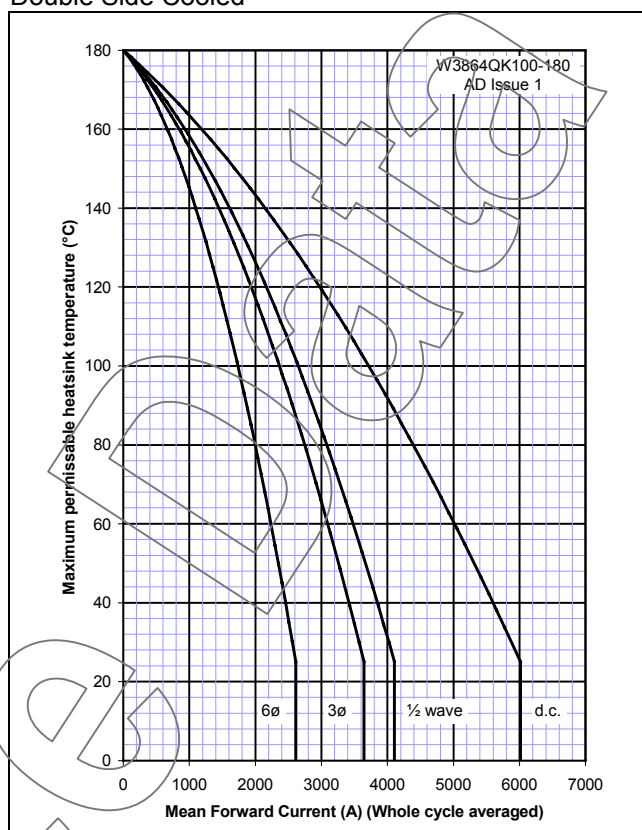


Figure 10 – Forward current vs. Power dissipation – Cathode Side Cooled

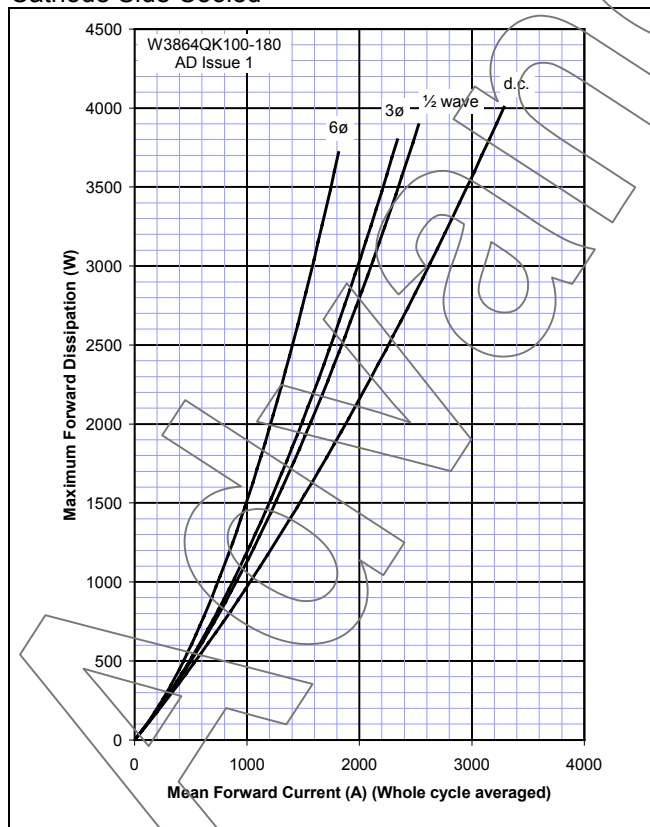
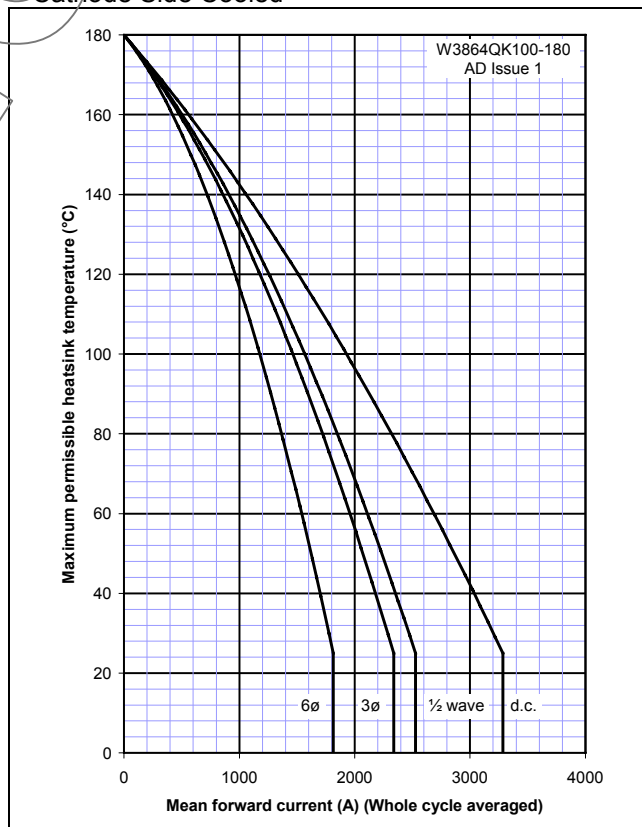


Figure 11 – Forward current vs. Heatsink temperature – Cathode Side Cooled



Outline Drawing & Ordering Information

