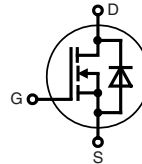


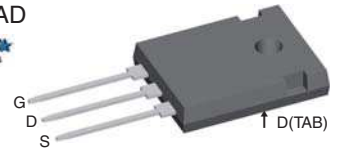
COOLMOS® * Power MOSFET

N-Channel Enhancement Mode
Low $R_{DS(on)}$, High V_{DSS} MOSFET
Ultra low gate charge

$$\begin{aligned} I_{D25} &= 35 \text{ A} \\ V_{DSS} &= 600 \text{ V} \\ R_{DS(on) \text{ max}} &= 0.1 \Omega \end{aligned}$$



TO-247 AD

COOLMOS®
Power Semiconductors


MOSFET			
Symbol	Conditions	Maximum Ratings	
V_{DSS}	$T_{VJ} = 25^\circ\text{C}$	600	V
V_{GS}		± 20	V
I_{D25}	$T_C = 25^\circ\text{C}$	35	A
I_{D90}	$T_C = 90^\circ\text{C}$	25	A
E_{AS} E_{AR}	single pulse repetitive } $I_D = 11 \text{ A}; T_C = 25^\circ\text{C}$	800 1.2	mJ mJ
dV/dt	MOSFET dV/dt ruggedness $V_{DS} = 0 \dots 480 \text{ V}$	50	V/ns

Features

- fast COOLMOS® * power MOSFET 4th generation
- High blocking capability
- Lowest resistance
- Avalanche rated for unclamped inductive switching (UIS)
- Low thermal resistance due to reduced chip thickness
- Enhanced total power density

Applications

- Switched mode power supplies (SMPS)
- Uninterruptible power supplies (UPS)
- Power factor correction (PFC)
- Welding
- Inductive heating
- PDP and LCD adapter

*COOLMOS® is a trademark of Infineon Technologies AG.

Symbol	Conditions	Characteristic Values			
		(T _{VJ} = 25°C, unless otherwise specified)			
		min.	typ.	max.	
$R_{DS(on)}$	$V_{GS} = 10 \text{ V}; I_D = 18 \text{ A}$		90	100	mΩ
$V_{GS(th)}$	$V_{DS} = V_{GS}; I_D = 1.2 \text{ mA}$	2.5	3	3.5	V
I_{DSS}	$V_{DS} = 600 \text{ V}; V_{GS} = 0 \text{ V}$			5	μA
	$T_{VJ} = 25^\circ\text{C}$ $T_{VJ} = 125^\circ\text{C}$		50		μA
I_{GSS}	$V_{GS} = \pm 20 \text{ V}; V_{DS} = 0 \text{ V}$			100	nA
C_{iss} C_{oss}	$V_{GS} = 0 \text{ V}; V_{DS} = 100 \text{ V}$ $f = 1 \text{ MHz}$		2800 130		pF pF
Q_g Q_{gs} Q_{gd}	$V_{GS} = 0 \text{ to } 10 \text{ V}; V_{DS} = 400 \text{ V}; I_D = 18 \text{ A}$		60 14 20	80	nC nC nC
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	$V_{GS} = 10 \text{ V}; V_{DS} = 400 \text{ V}$ $I_D = 18 \text{ A}; R_G = 3.3 \Omega$		10 5 60 5		ns ns ns ns
R_{thJC}				0.35	K/W

Source-Drain Diode

Symbol	Conditions	Characteristic Values			
		(T _{VJ} = 25°C, unless otherwise specified)			
		min.	typ.	max.	
I _S	V _{GS} = 0 V			18	A
V _{SD}	I _F = 18 A; V _{GS} = 0 V		0.9	1.2	V
t _{rr}	I _F = 18 A; -di _F /dt = 100 A/μs; V _R = 400 V		450		ns
Q _{RM}			12		μC
I _{RM}			70		A

Component

Symbol	Conditions	Maximum Ratings	
T _{VJ}	operating	-55...+150	°C
T _{stg}		-55...+150	°C
M _d	mounting torque	0.8 ... 1.2	Nm

Symbol	Conditions	Characteristic Values			
		min.	typ.	max.	
R _{thCH}	with heatsink compound		0.25		K/W
Weight			6		g

TO-247 AD Outline

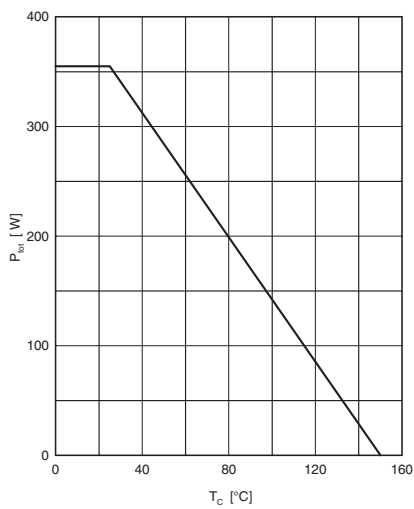
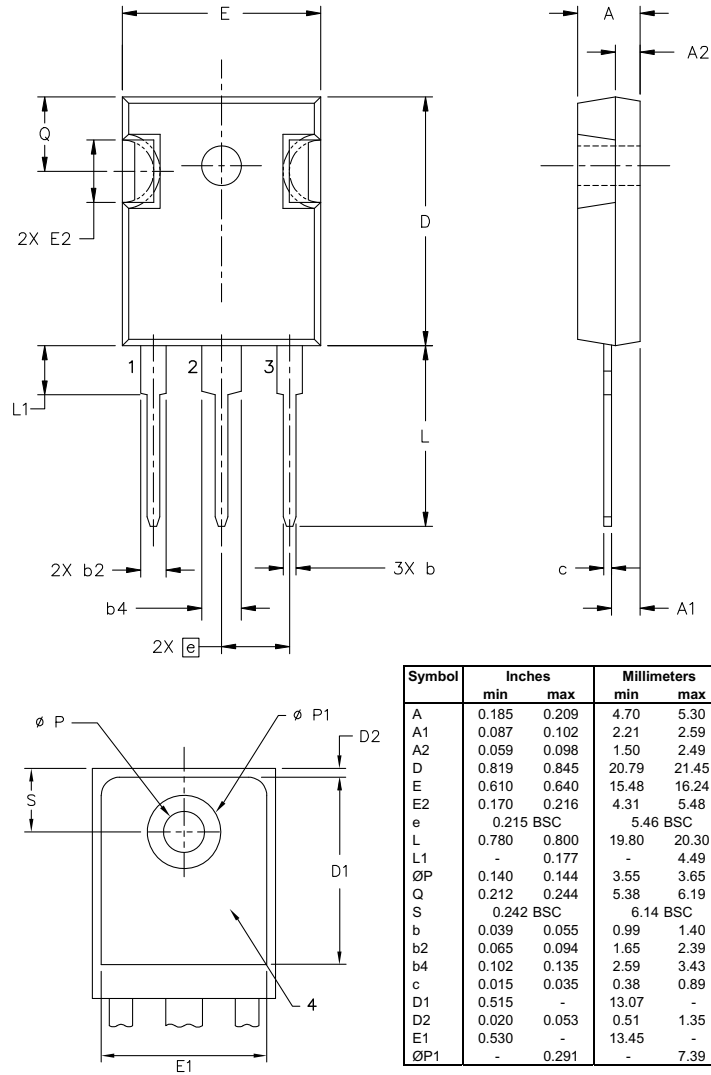


Fig. 1 Power dissipation

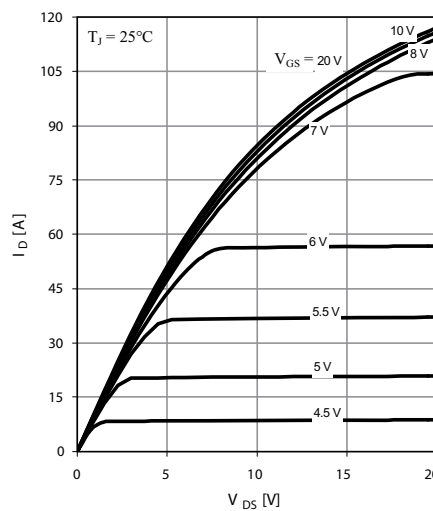


Fig. 2 Typ. output characteristics

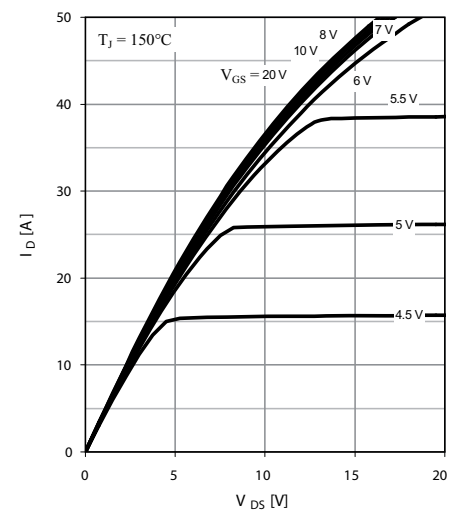


Fig. 3 Typ. output characteristics

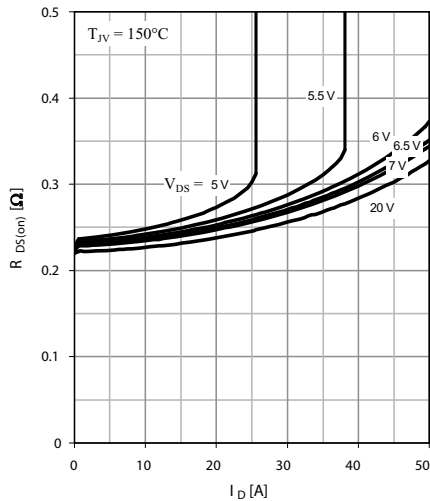


Fig. 4 Typ. drain-source on-state resistance characteristics of IGBT

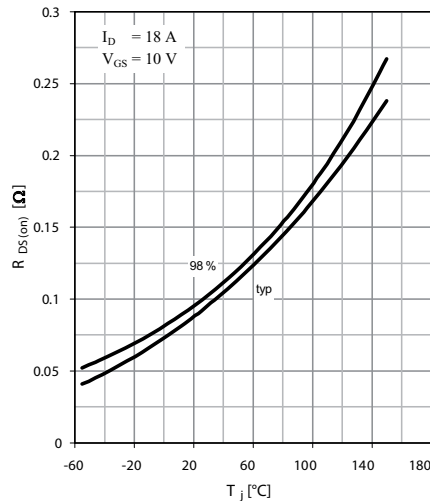


Fig. 5 Drain-source on-state resistance

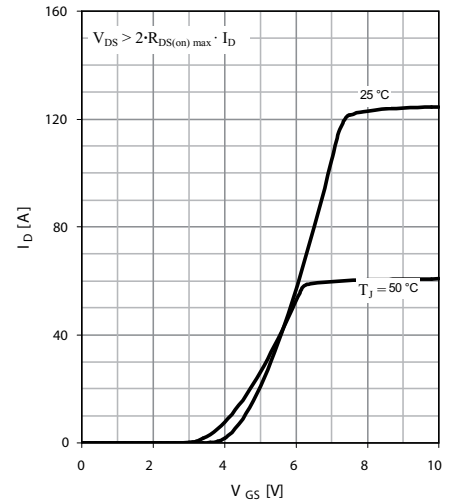


Fig. 6 Typ. transfer characteristics

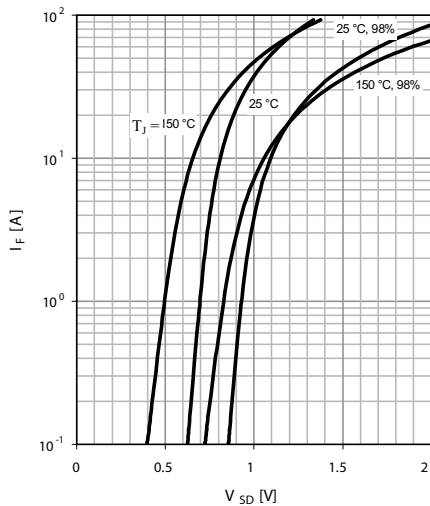


Fig. 7 Forward characteristic of reverse diode

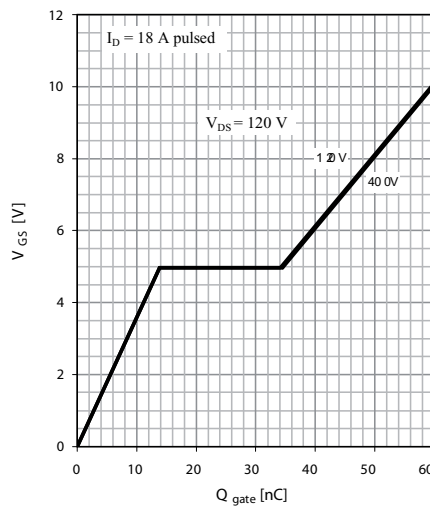


Fig. 8 Typ. gate charge

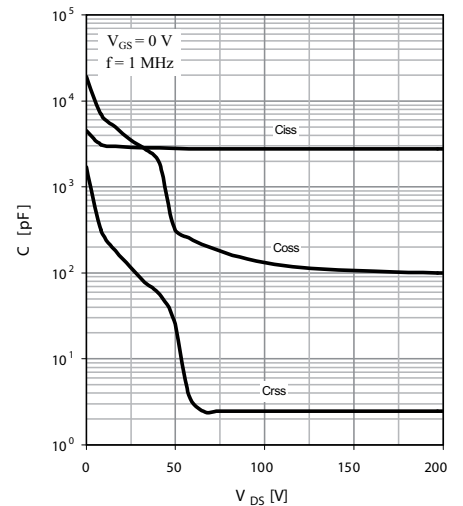


Fig. 9 Typ. capacitances

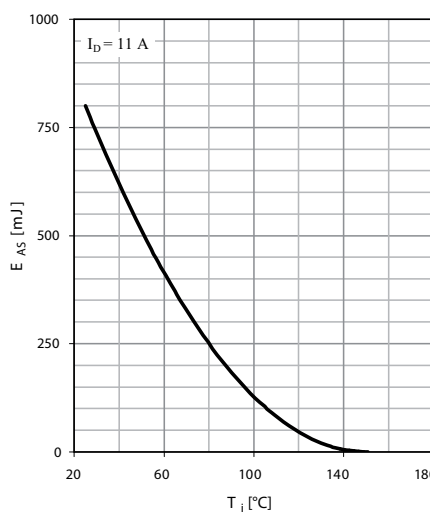


Fig. 10 Avalanche energy

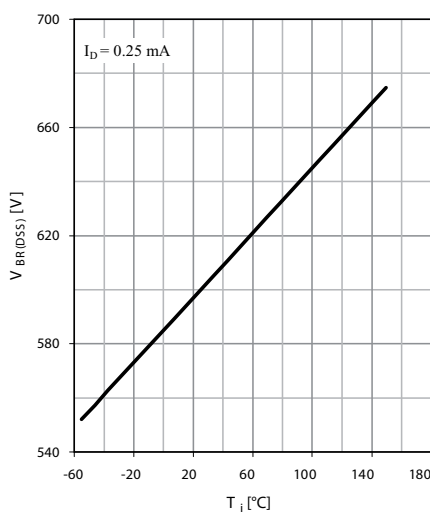


Fig. 11 Drain-source breakdown voltage

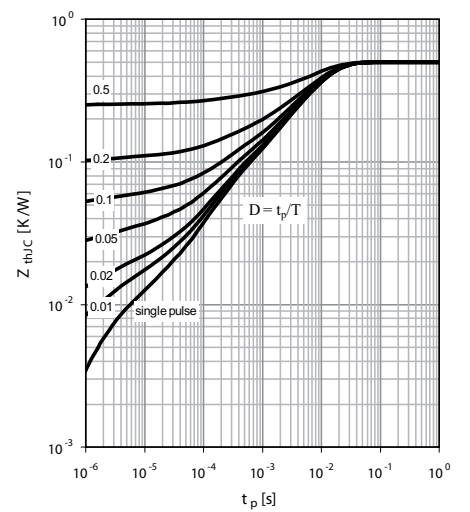


Fig. 12 Max. transient thermal impedance

IXYS reserves the right to change limits, test conditions and dimensions.

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