WESTCODE

Provisional Data

Distributed Gate Thyristor
Types R3047TC24x to R3047TC28x

Absolute Maximum Ratings

	VOLTAGE RATINGS (Note 1)	MAXIMUM LIMITS	UNITS
V_{DRM}	Repetitive peak off-state voltage	2400-2800	V
V_{DSM}	Non-repetitive peak off-state voltage	2400-2800	V
V_{RRM}	Repetitive peak reverse voltage	2400-2800	V
V_{RSM}	Non-repetitive peak reverse voltage	2500-2900	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
$I_{T(AV)}$	Mean on-state current, T_{sink}=55°C (note 2)	3047	A
$I_{T(AV)}$	Mean on-state current. T_{sink}=85°C (note 2)	2043	A
$I_{T(AV)}$	Mean on-state current. T_{sink}=85°C (note 3)	1196	A
$I_{T(RMS)}$	Nominal RMS on-state current, T_{sink}=25°C (note 2)	6094	A
$I_{T(d.c.)}$	D.C. on-state current, T_{sink}=25°C (note 4)	5097	A
I_{TSM}	Peak non-repetitive surge t_p=10ms, V_{RM}=0.6V_{RRM} (note 5)	50	kA
I_{TSM2}	Peak non-repetitive surge t_p=10ms, V_{RM}≤10V (note 5)	55	kA
I^2t	I^2t capacity for fusing t_p=10ms, V_{RM}=0.6V_{RRM} (note 5)	12.5×10⁶	A²s
I^2t	I^2t capacity for fusing t_p=10ms, V_{RM}≤10V (note 5)	15.1×10⁶	A²s
di_T/dt	Maximum rate of rise of on-state current (repetitive) (Note 6)	500	A/μs
	Maximum rate of rise of on-state current (non-repetitive) (Note 6)	1000	A/μs
V_{RGM}	Peak reverse gate voltage	5	V
$P_{G(AV)}$	Mean forward gate power	4	W
P_{GM}	Peak forward gate power	50	W
V_{GD}	Non-trigger gate voltage (Note 7)	0.25	V
T_{HS}	Operating temperature range	-40 to +125	°C
T_{stg}	Storage temperature range	-40 to +150	°C

Notes:-
1) De-rating factor of 0.13% per °C is applicable for T_j below 25°C.
2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
4) Double side cooled.
5) Half-sinewave, 125°C T_j initial.
6) V_D=67% V_{DRM}, I_{FG}=2A, t_r≤0.5μs, T_{case}=125°C.
7) Rated V_{DRM}.

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V_{TM}	Maximum peak on-state voltage	-	-	2.45	I_{TM}=5000A	V
V_0	Threshold voltage	-	-	1.581		V
r_S	Slope resistance	-	-	0.171		mΩ
dv/dt	Critical rate of rise of off-state voltage	200	-	-	V_D=80% V_{DRM}, linear ramp	V/µs
I_{DRM}	Peak off-state current	-	-	200	Rated V_{DRM}	mA
I_{RRM}	Peak reverse current	-	-	200	Rated V_{RRM}	mA
V_{GT}	Gate trigger voltage	-	-	3.0	T_j=25°C V_D=10V, I_T=3A	V
I_{GT}	Gate trigger current	-	-	600		mA
I_H	Holding current	-	-	1000	T_j=25°C	mA
t_{gd}	Gate-controlled turn-on delay time	-	0.7	1.5	V_D=67% V_{DRM}, I_T=2000A, di/dt=60A/µs, I_{FG}=2A, t_r=0.5µs, T_j=25°C	µs
t_{gt}	Turn-on time	-	1.5	3.0		
Q_{rr}	Recovered charge	-	1900	-	I_{TM}=4000A, t_p=2000µs, di/dt=60A/µs, V_r=100V	µC
Q_{ra}	Recovered charge, 50% Chord	-	950	1180		µC
I_{rm}	Reverse recovery current	-	260	-		A
t_{rr}	Reverse recovery time, 50% chord	-	7.3	-		µs
t_q	Turn-off time	-	-	80	I_{TM}=4000A, t_p=2000µs, di/dt=60A/µs, V_r=100V, V_{dr}=67%V_{DRM}, dV_{dr}/dt=20V/µs	µs
		60	85	100	I_{TM}=4000A, t_p=2000µs, di/dt=60A/µs, V_r=100V, V_{dr}=67%V_{DRM}, dV_{dr}/dt=200V/µs	
$R_{th(j-hs)}$	Thermal resistance, junction to heatsink	-	-	0.008	Double side cooled	K/W
		-	-	0.016	Single side cooled	K/W
F	Mounting force	63	-	77		kN
W_t	Weight	-	1.23	-		kg

Notes:-
1) Unless otherwise indicated T_j=125°C.
2) The required t_q (specified with dV_{dr}/dt=200V/µs) is represented by an 'x' in the device part number. See ordering information for details of t_q codes.

Notes on Ratings and Characteristics

1.0 Voltage Grade Table

Voltage Grade	V_{DRM} V_{DSM} V_{RRM} V	V_{RSM} V	V_D V_R DC V
2400	2400	2500	1450
2600	2600	2700	1550
2800	2800	2900	1650

2.0 Extension of Voltage Grades

This report is applicable to other and higher voltage grades when supply has been agreed by Sales/Production.

3.0 Extension of Turn-off Time

This Report is applicable to other t_q/re-applied dv/dt combinations when supply has been agreed by Sales/Production.

4.0 Repetitive dv/dt

Higher dv/dt selections are available up to 1000V/μs on request.

5.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_j below 25°C.

6.0 Rate of rise of on-state current

The maximum un-primed rate of rise of on-state current must not exceed 1000A/μs at any time during turn-on on a non-repetitive basis. For repetitive performance, the on-state rate of rise of current must not exceed 500A/μs at any time during turn-on. Note that these values of rate of rise of current apply to the total device current including that from any local snubber network.

7.0 Square wave ratings

These ratings are given for load component rate of rise of forward current of 100 and 500 A/μs.

8.0 Duty cycle lines

The 100% duty cycle is represented on all the ratings by a straight line. Other duties can be included as parallel to the first.

9.0 Maximum Operating Frequency

The maximum operating frequency is set by the on-state duty, the time required for the thyristor to turn off (t_q) and for the off-state voltage to reach full value (t_v), i.e.

$$f_{max} = \frac{1}{t_{pulse} + t_q + t_v}$$

10.0 On-State Energy per Pulse Characteristics

These curves enable rapid estimation of device dissipation to be obtained for conditions not covered by the frequency ratings.
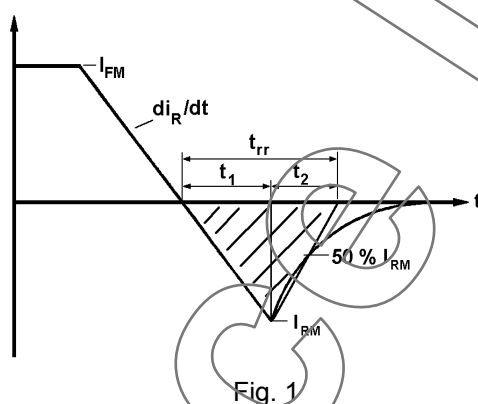
> Let E_p be the Energy per pulse for a given current and pulse width, in joules
> Let $R_{th(J\text{-}Hs)}$ be the steady-state d.c. thermal resistance (junction to sink)
> and T_{SINK} be the heat sink temperature.

Then the average dissipation will be:

$$W_{AV} = E_P \cdot f \; and \; T_{SINK(\max.)} = 125 - \left(W_{AV} \cdot R_{th(J-Hs)}\right)$$

11.0 Reverse recovery ratings

> (i) Q_{ra} is based on 50% I_{rm} chord as shown in Fig. 1 below.

Fig. 1

> (ii) Q_{rr} is based on a 150µs integration time.

i.e.

$$Q_{rr} = \int_{0}^{150\,\mu s} i_{rr}.dt$$

(iii)

$$K\;Factor = \frac{t1}{t2}$$

12.0 Reverse Recovery Loss

> 12.1 Determination by Measurement

From waveforms of recovery current obtained from a high frequency shunt (see Note 1, Page 5) and reverse voltage present during recovery, an instantaneous reverse recovery loss waveform must be constructed. Let the area under this waveform be E joules per pulse. A new heat sink temperature can then be evaluated from:

$$T_{SINK(new)} = T_{SINK(original)} - E \cdot \left(k + f \cdot R_{th(J-Hs)}\right)$$

where k = 0.227 (°C/W)/s

E = Area under reverse loss waveform per pulse in joules (W.s.)
f = rated frequency Hz at the original heat sink temperature.
$R_{th(J\text{-}Hs)}$ = d.c. thermal resistance (°C/W).

The total dissipation is now given by:

$$W_{(TOT)} = W_{(original)} + E \cdot f$$

12.2 Determination without Measurement

In circumstances where it is not possible to measure voltage and current conditions, or for design purposes, the additional losses E in joules may be estimated as follows.

Let E be the value of energy per reverse cycle in joules (curves in Figure 9).
Let f be the operating frequency in Hz

$$T_{SINK(new)} = T_{SINK(original)} - (E \cdot R_{th} \cdot f)$$

Where $T_{SINK\,(new)}$ is the required maximum heat sink temperature and $T_{SINK\,(original)}$ is the heat sink temperature given with the frequency ratings.

A suitable R-C snubber network is connected across the thyristor to restrict the transient reverse voltage to a peak value (V_{rm}) of 67% of the maximum grade. If a different grade is being used or V_{rm} is other than 67% of Grade, the reverse loss may be approximated by a pro rata adjustment of the maximum value obtained from the curves.

NOTE 1- Reverse Recovery Loss by Measurement

This thyristor has a low reverse recovered charge and peak reverse recovery current. When measuring the charge care must be taken to ensure that:

(a) a.c. coupled devices such as current transformers are not affected by prior passage of high amplitude forward current.
(b) A suitable, polarised, clipping circuit must be connected to the input of the measuring oscilloscope to avoid overloading the internal amplifiers by the relatively high amplitude forward current signal
(c) Measurement of reverse recovery waveform should be carried out with an appropriate critically damped snubber, connected across diode anode to cathode. The formula used for the calculation of this snubber is shown below:

$$R^2 = 4 \cdot \frac{V_r}{C_S \cdot \frac{di}{dt}}$$

Where: V_r = Commutating source voltage
C_S = Snubber capacitance
R   = Snubber resistance

13.0 Gate Drive

The recommended pulse gate drive is 30V, 15Ω with a short-circuit current rise time of not more than 0.5µs. This gate drive must be applied when using the full di/dt capability of the device.

The duration of pulse may need to be configured with respect to the application but should be no shorter than 20µs, otherwise an increase in pulse current could be needed to supply the resulting increase in charge to trigger.

14.0 Computer Modelling Parameters

14.1 Calculating V_T using ABCD Coefficients

The on-state characteristic I_T vs V_T, on page 7 is represented in two ways;
(i)   the well established V_0 and r_s tangent used for rating purposes and
(ii)   a set of constants A, B, C, D, forming the coefficients of the representative equation for V_T in terms of I_T given below:

$$V_T = A + B \cdot \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

The constants, derived by curve fitting software, are given in this report for hot and cold characteristics where possible. The resulting values for V_T agree with the true device characteristic over a current range, which is limited to that plotted.

	25°C Coefficients		125°C Coefficients
A	1.262553	A	2.08186705
B	0.140687	B	-0.1296326
C	1.41627×10^{-4}	C	1.055787×10^{-4}
D	-9.591208×10^{-3}	D	0.01335506

14.2 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}} \right)$$

Where *p = 1* to *n, n* is the number of terms in the series.

t   = Duration of heating pulse in seconds.
r_t = Thermal resistance at time t.
r_p = Amplitude of p_{th} term.
τ_p = Time Constant of r_{th} term.

D.C. Double Side Cooled			
Term	1	2	3
r_p	5.228149×10^{-3}	3.076205×10^{-3}	1.977511×10^{-3}
τ_p	0.9862513	0.2593041	0.03447094

D.C. Single Side Cooled				
Term	1	2	3	4
r_p	0.01186497	3.872272×10^{-3}	3.457033×10^{-3}	1.694157×10^{-3}
τ_p	7.361938	1.651253	0.2019036	0.02934724

Curves

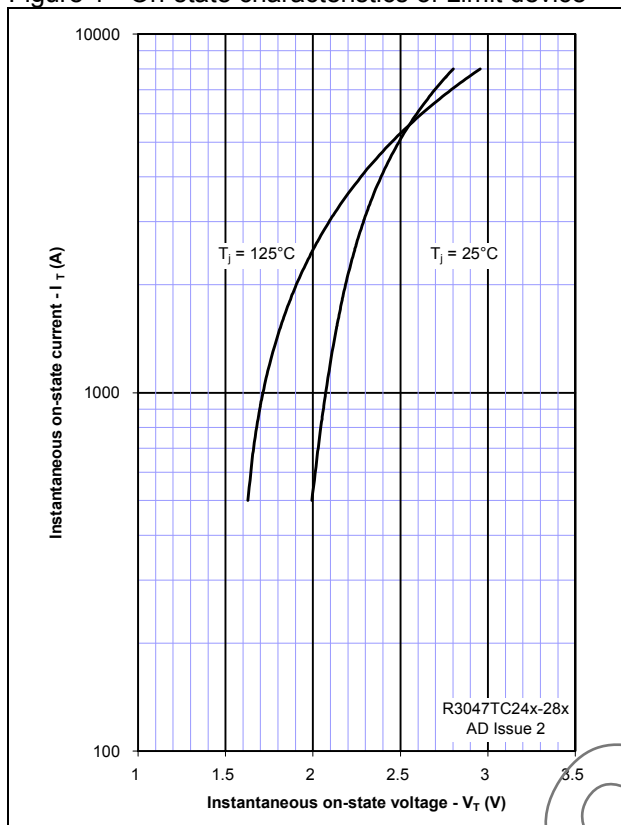### Figure 1 - On-state characteristics of Limit device
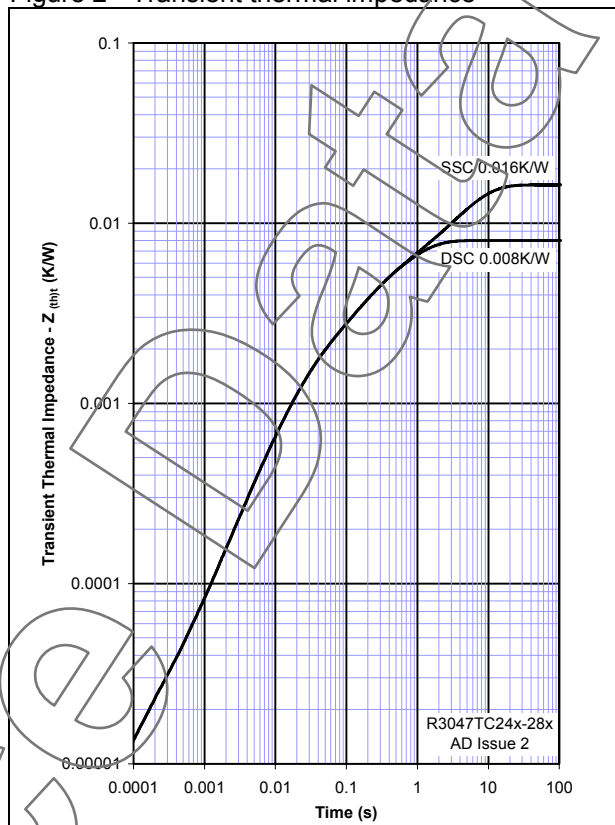

Figure 2 - Transient thermal impedance

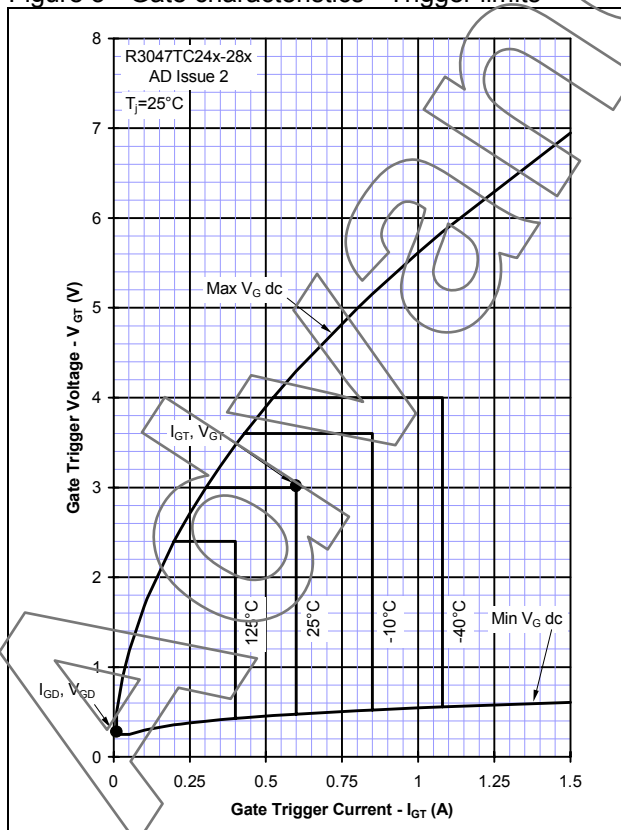### Figure 3 - Gate characteristics - Trigger limits

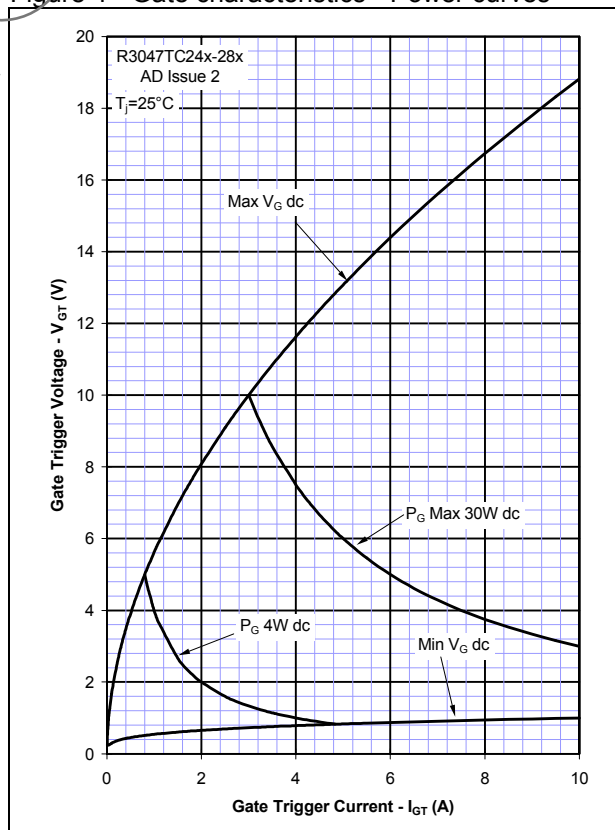### Figure 4 - Gate characteristics - Power curves
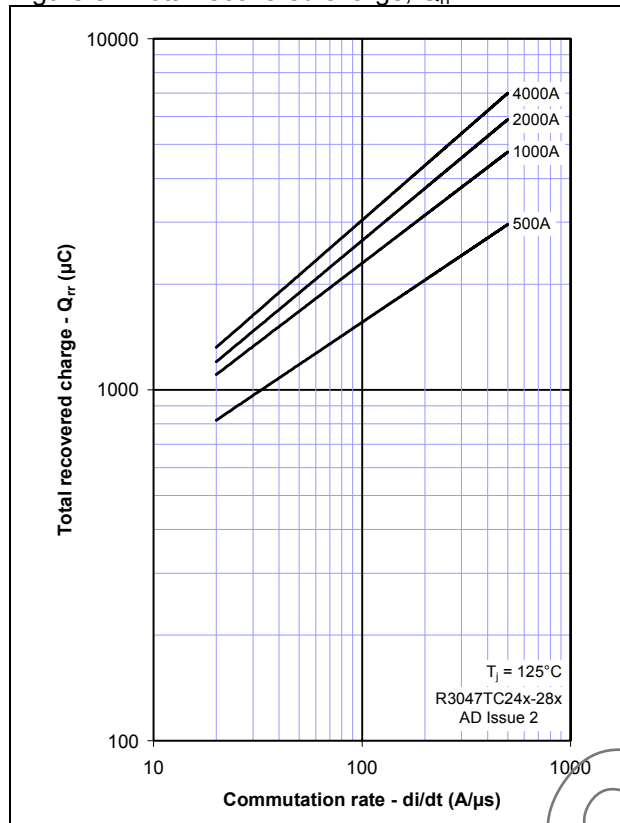
Figure 5 - Total recovered charge, Q_{rr}
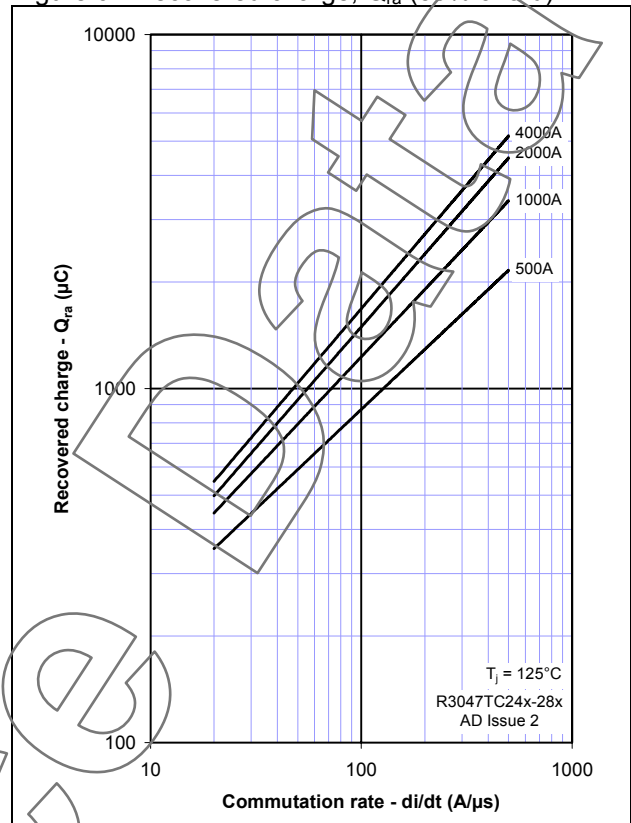
Figure 6 - Recovered charge, Q_{ra} (50% chord)
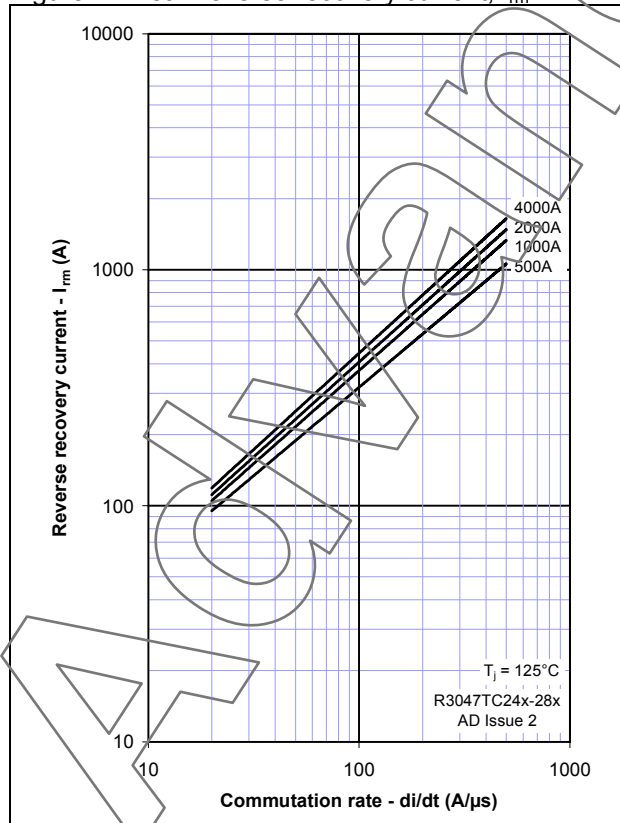
Figure 7 - Peak reverse recovery current, I_{rm}
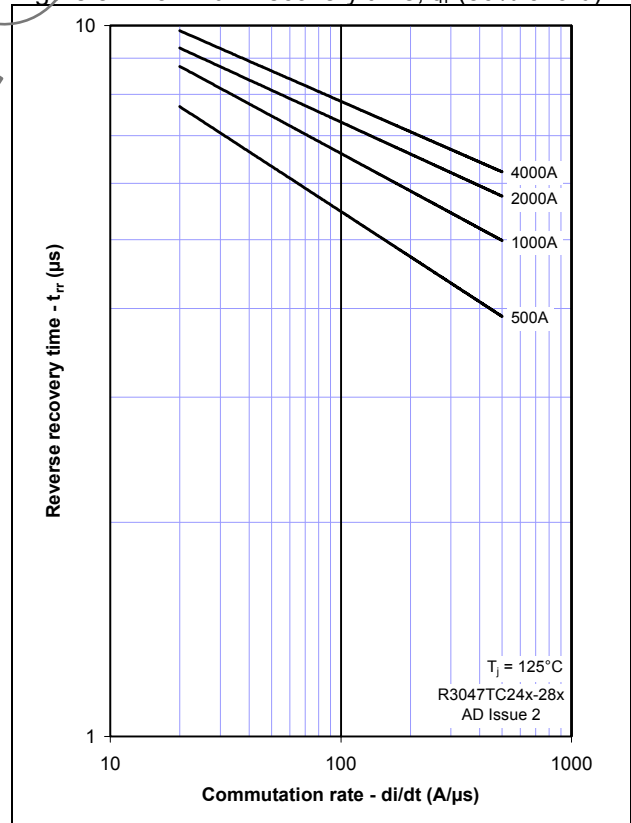
Figure 8 - Maximum recovery time, t_{rr} (50% chord)
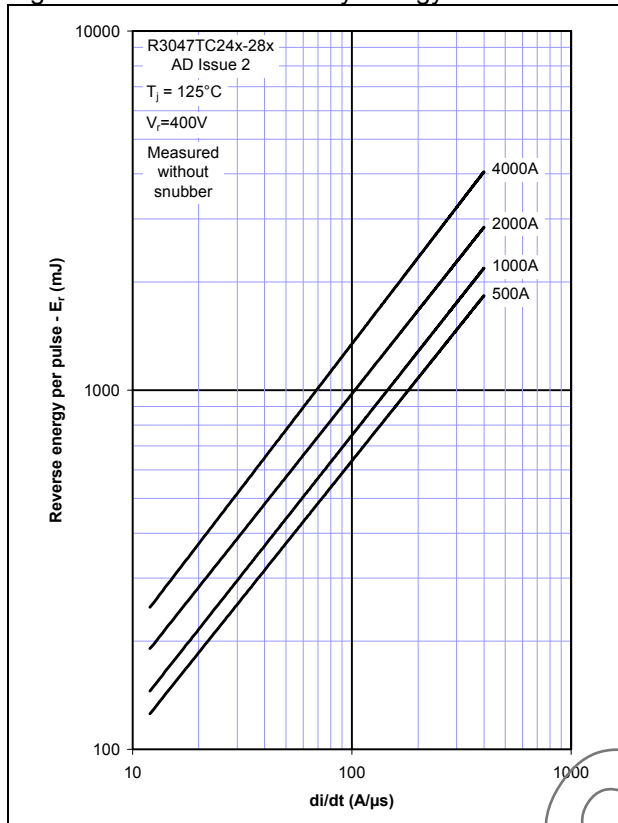
Figure 9 – Reverse Recovery Energy

R3047TC24x-28x
AD Issue 2

$T_j = 125°C$

$V_r = 400V$

Measured without snubber

4000A
2000A
1000A
500A

Reverse energy per pulse - E_r (mJ)

di/dt (A/µs)

Figure 10 - Sine wave energy per pulse

R3047TC24x-28x
AD Issue 2

$T_j = 125°C$

8000A
6000A
4000A
2000A
1000A
500A

Energy per pulse (J)

Pulse width (s)
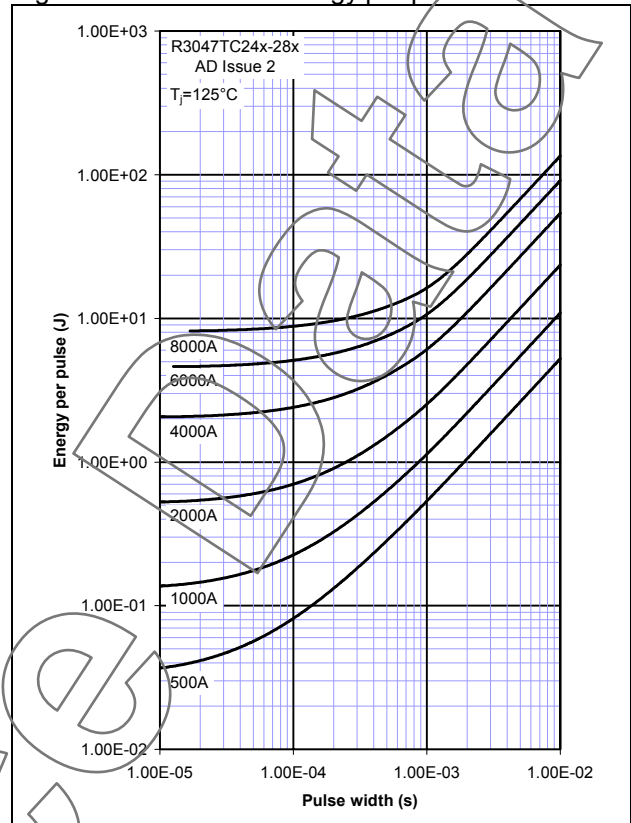
Figure 11 - Sine wave frequency ratings

R3047TC24x-28x
AD Issue 2

$T_{Hs} = 55°C$

1000A
100% Duty Cycle
2000A
4000A
6000A
8000A

Frequency (Hz)

Pulse Width (s)
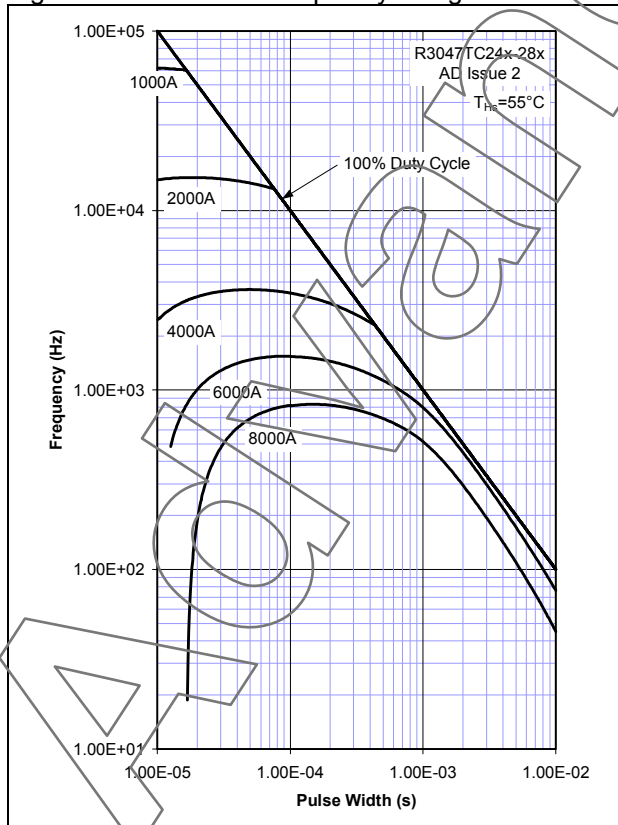
Figure 12 - Sine wave frequency ratings

R3047TC24x-28x
AD Issue 2

$T_{Hs} = 85°C$

1000A
100% Duty Cycle
2000A
4000A
6000A
8000A

Frequency (Hz)

Pulse width (s)
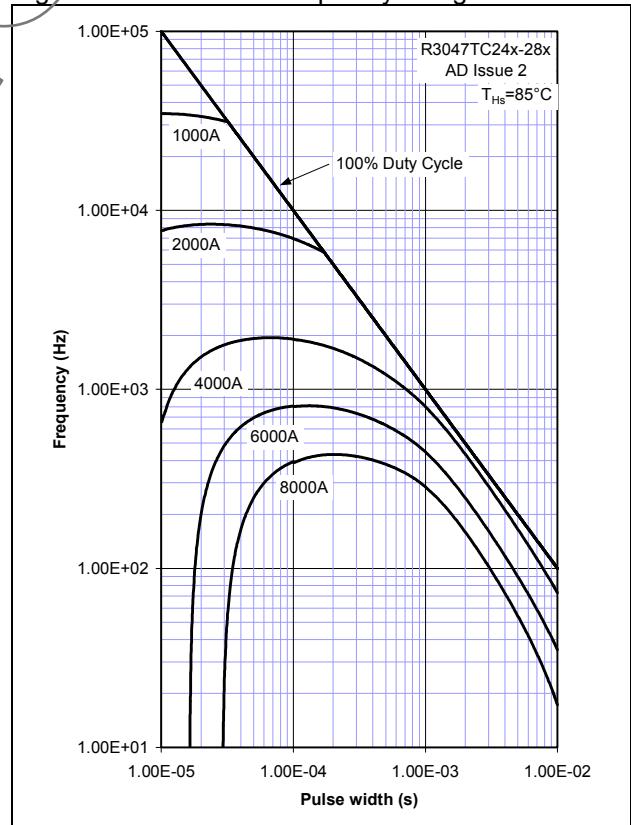
Figure 13 - Square wave frequency ratings

100% Duty Cycle

2000A
4000A
6000A
8000A

T_{Hs}=55°C
di/dt=100A/μs
R3047TC24x-28x
AD Issue 2
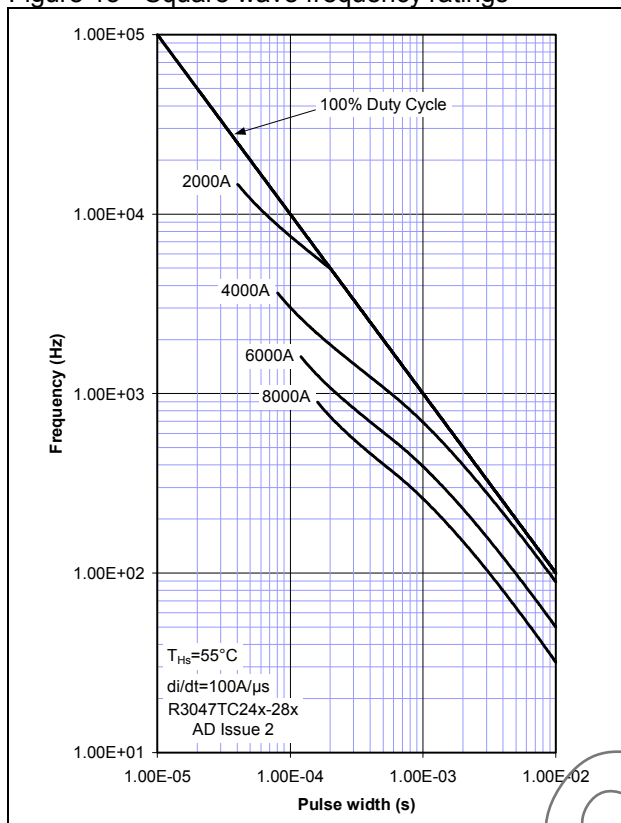
Figure 14 - Square wave frequency ratings

R3047TC24x-28x
AD Issue 2
di/dt=500A/μs
T_{Hs}=55°C

1000A
100% Duty Cycle
2000A
4000A
6000A
8000A
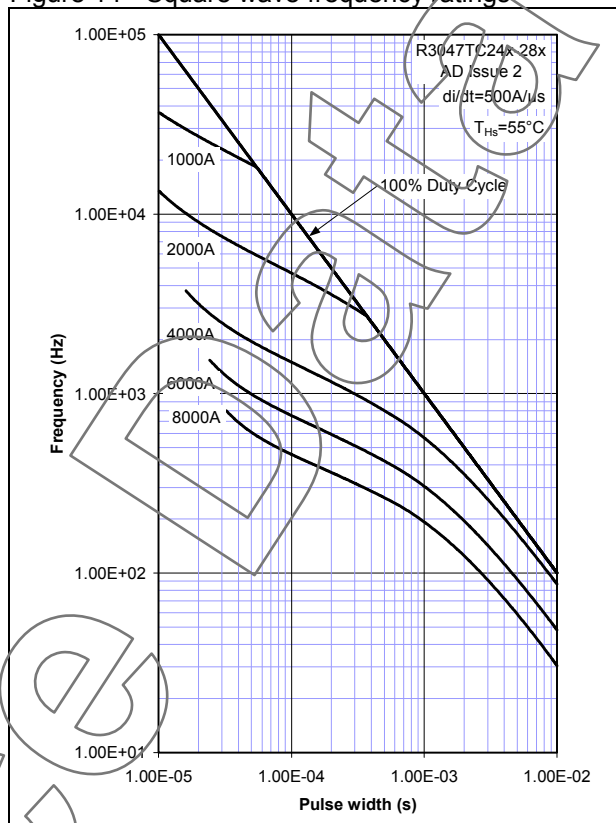
Figure 15 - Square wave frequency ratings

1000A
100% Duty Cycle
2000A
4000A
6000A
8000A

T_{Hs}=85°C
di/dt=100A/μs
R3047TC24x-28x
AD Issue 2
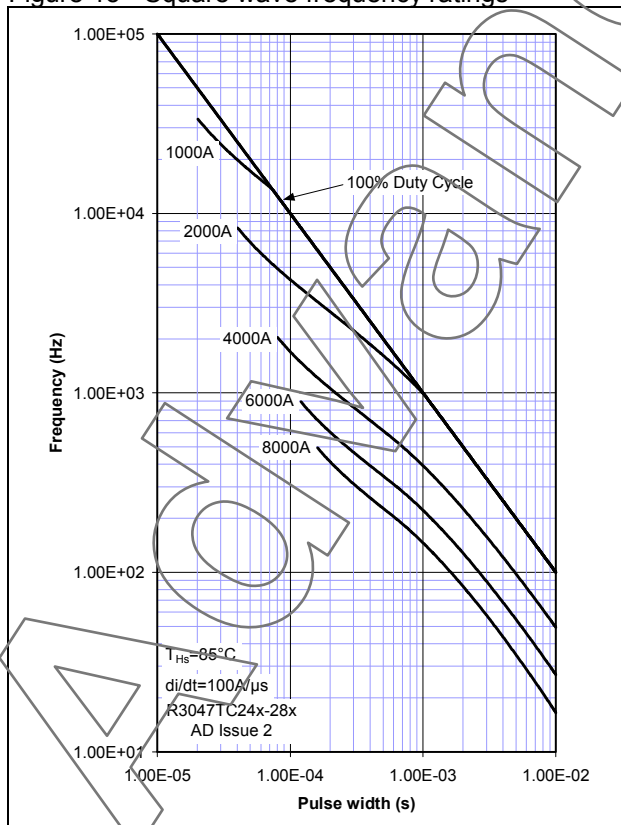
Figure 16 - Square wave frequency ratings

500A
100% Duty Cycle
1000A
2000A
4000A
6000A
8000A

T_{Hs}=85°C
di/dt=500A/μs
R3047TC24x-28x
AD Issue 2
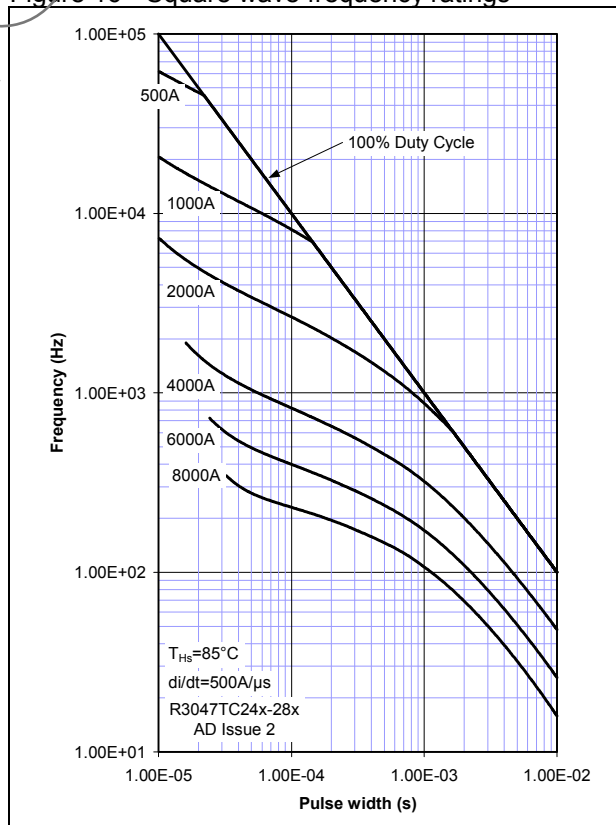
Figure 17 - Square wave energy per pulse

R3047TC24-28x
AD Issue 2
di/dt=100A/μs
T_j=125°C

8000A
6000A
4000A
2000A
1000A
500A

Pulse width (s)
Energy per pulse (J)
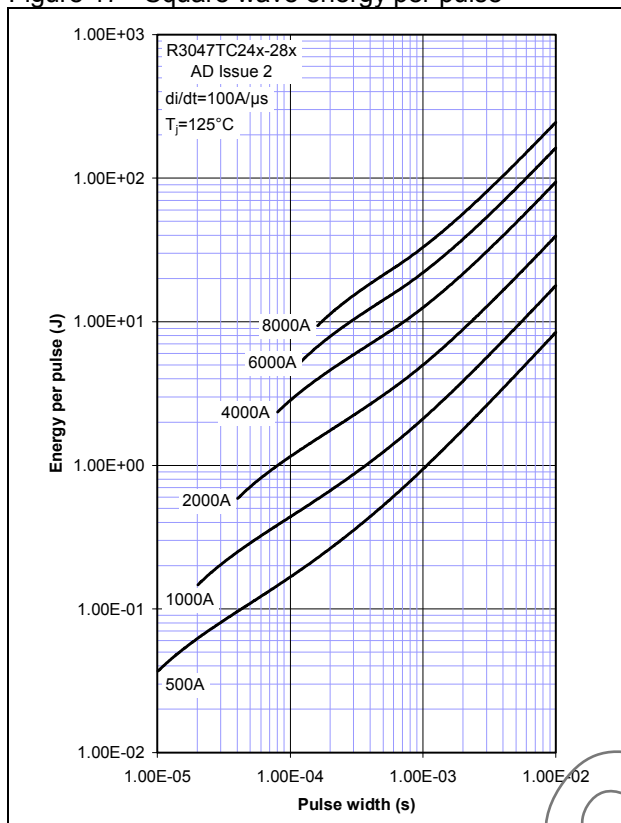
Figure 18 - Square wave energy per pulse

R3047TC24x-28x
AD Issue 2
di/dt=500A/μs
T_j=125°C

8000A
6000A
4000A
2000A
1000A
500A

Pulse width (s)
Energy per pulse (J)
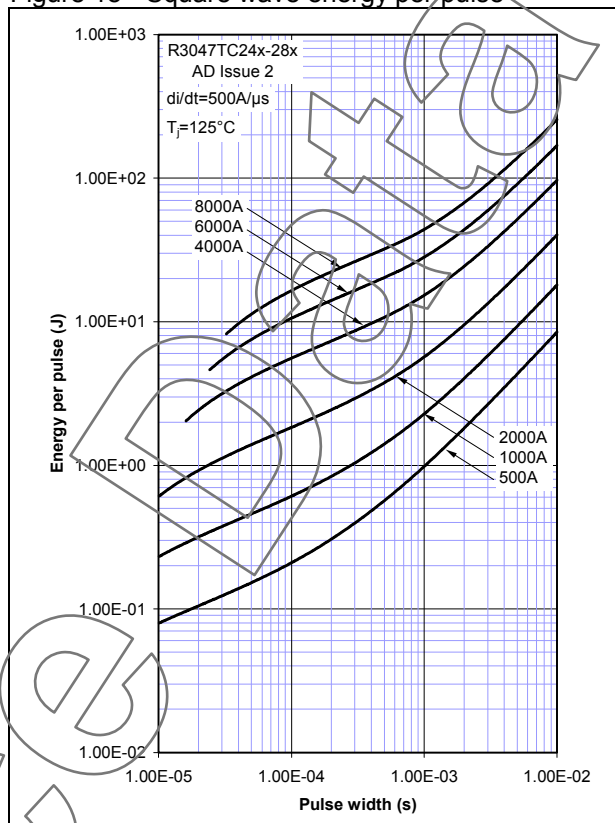
Figure 19 - Maximum surge and I^2t Ratings

Gate may temporarily lose control of conduction angle

R3047TC24x-28x
AD Issue 2
T_j (initial) = 125°C

I^2t: $V_{RRM} \leq 10V$
I^2t: 60% V_{RRM}
I_{TSM}: $V_{RRM} \leq 10V$
I_{TSM}: 60% V_{RRM}

Total peak half sine surge current - I_{TSM} (A)
Maximum I^2t (A^2s)

Duration of surge (ms)
Duration of surge (cycles @ 50Hz)
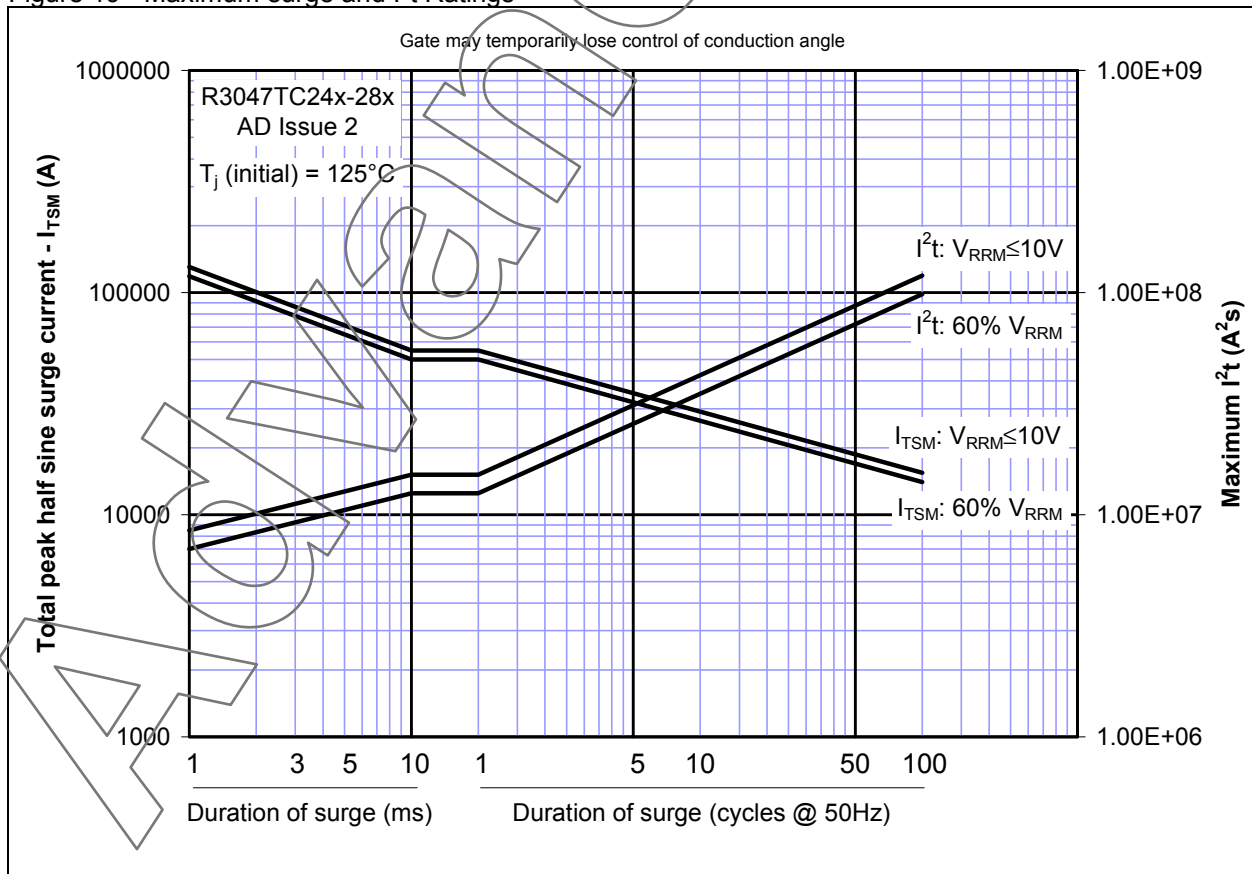
Outline Drawing & Ordering Information

ø3.6/3.5x2 DEEP
HOLE IN CATHODE
AND IN ANODE.

ø112 MAX

20°±5°

4.75 TAB

ø75 ±0.1

26.1 27.1

GATE PIN USE
AMP 60598-1

33.5 MIN.
CREEP PATH.

ø75 ±0.1

101A325
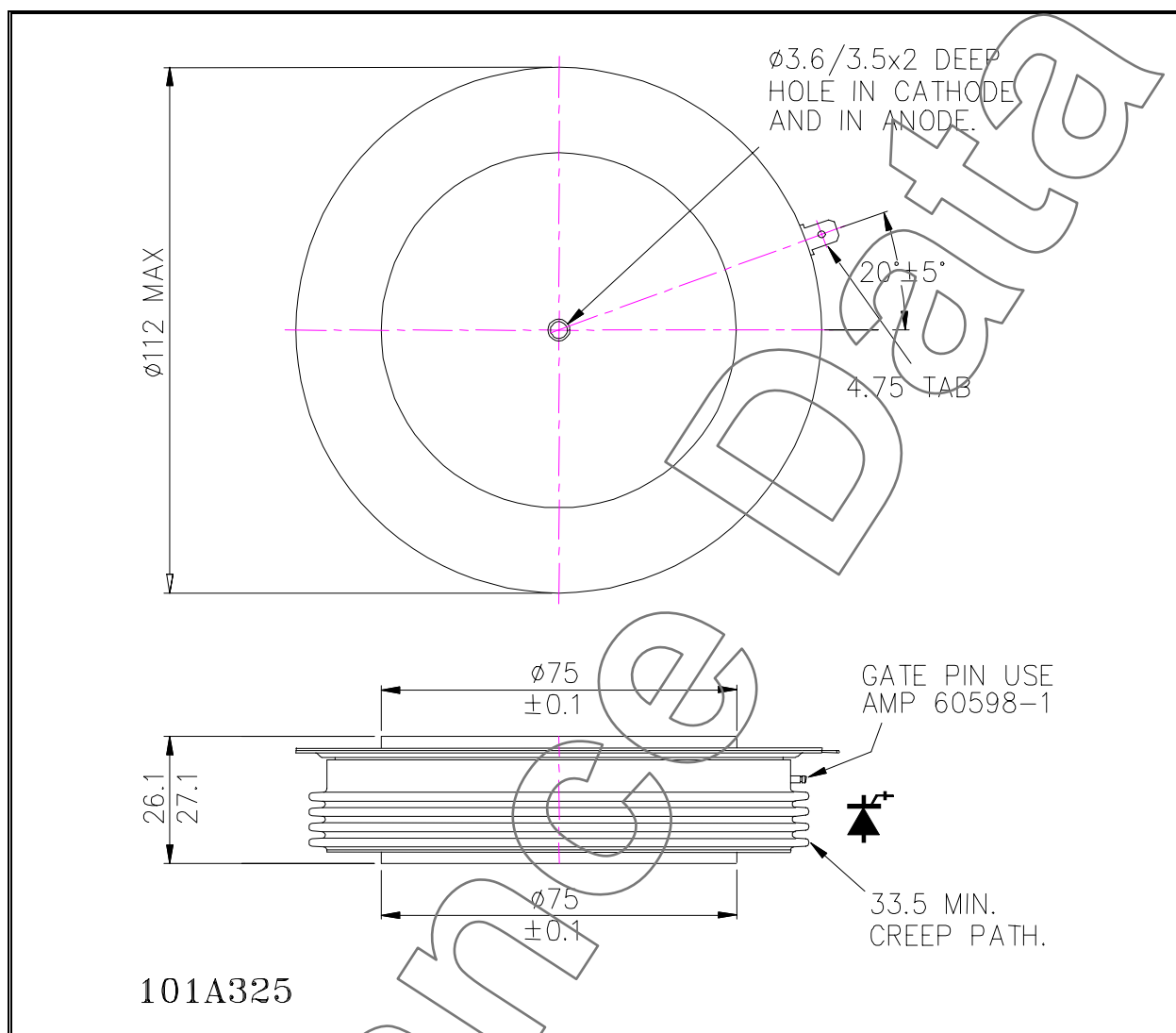
ORDERING INFORMATION		(Please quote 10 digit code as below)	
R3047	**TC**	◆ ◆	◆
Fixed Type Code	Fixed Outline Code	Off-state Voltage Code $V_{DRM}/100$ 40-45	t_q Code K=60µs, L=65µs, M=70µs, N=100µs

Typical order code: R3047TC26 – 2600V V_{DRM}, V_{RRM}, 300µs t_q, 27.1mm clamp height capsule.

WESTCODE

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