

Wide band directional coupler with ISO port

Features

- 50Ω nominal input / output impedance
- Wide operating frequency range (824 MHz to 2170 MHz)
- Low Insertion Loss (< 0.2 dB)
- 34 dB typical coupling factor
- High directivity (typical 25 dB)
- High ESD robustness (IEC 61000-4-2 Level 4)
- Flip-Chip package
- Small footprint: 1300 x 1000 µm

Benefits

- Very low profile (< 690 µm)</p>
- Lead-free package
- High RF performance
- RF module size reduction

Applications

- Quad band power amplifier module
- Quad band front end module
- GSM / WCDMA mobile phone

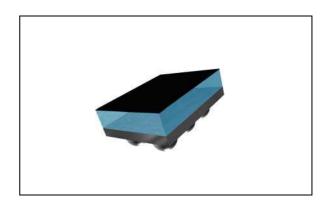
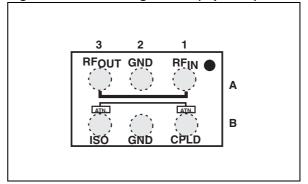


Figure 1. Pin configuration (top view)



Description

The CPL-WB-00D3 is a wide band directional coupler designed to measure RF antenna output power in GSM / WCDMA / TD-SCDMA applications. This CPL has been customized for wide band operating frequencies (EGSM and CELL, PCS, DCS, TD-SCDMA, WCDMA Band I) with less than 0.2 dB insertion losses in the transmit bandwidth (824 MHz to 2170 MHz).

The CPL-WB-00D3 has been designed using STMicroelectronics IPD (integrated passive device) technology on non conductive glass substrate to optimize RF performance. The device is delivered 100% tested in tape and reel.

Characteristics CPL-WB-00D3

1 Characteristics

Table 1. Absolute maximum rating (limiting values)

Symbol	Parameter	Value			Unit
	Farameter		Тур.	Max.	Ollit
P _{IN}	Input Power RF _{IN}			35	dBm
V _{ESD (IEC)}	ESD ratings IEC 61000-4-2 (C = 150 pF, R = 330 Ω , 10 shots with both polarities and each condition, cumulative method) ISO and CPLD pins connected to ground: RF _{IN} , RF _{OUT} , air discharge RF _{IN} , RF _{OUT} , contact discharge	±15 ±8			kV kV
V _{ESD (HBM)}	Human body model, JESD22-A114F, all I/O	2			kV
V _{ESD (MM)}	Machine model, JESD22-A115-A, all I/O	100			V
V _{ESD (CDM)}	Charge device model, JESD22-C101-C, all I/O	500			V
T _{OP}	Operating temperature	-30		+85	ºC

Table 2. Electrical characteristics ($T_{amb} = 25 \, ^{\circ}C$) - impedances

Symbol	Parameter	Value			Unit
Symbol		Min.	Тур.	Max.	Offic
Z _{OUT}	Nominal output impedance		50		Ω
Z _{IN}	Nominal input impedance		50		Ω
Z _{CPLD}	Nominal coupling impedance		50		Ω
Z _{OUT}	Nominal ISO impedance		50		Ω

Table 3. Electrical characteristics ($T_{amb} = 25 \, ^{\circ}C$) - RF performance

Symbol	Parameter	Test condition	Value			Unit
		rest condition	Min.	Тур.	Max.	J.III
f	Frequency range (bandwidth)		824		2170	MHz
IL	Insertion loss in bandwidth	From 824 MHz to 2170 MHz		0.1	0.2	dB
R _L	Return loss in bandwidth	From 824 MHz to 2170 MHz	15			dB
CPLD	Coupling factor (including	From 824 MHz to 915 MHz	35		39	dB
OI LD	attenuator)	From 1710 MHz to 2025 MHz	28		33	dB
Ripple	Coupling ripple in individual band	(824 to 849 MHz) (880 to 915 MHz) (1710 to 1785 MHz) (1850 to 1910 MHz) (1880 to 2025 MHz) (1920 to 1980 MHz)			0.5	dB
DIR	Coupler directivity	From 824 MHz to 2025 MHz	20	25		dB

CPL-WB-00D3 Characteristics

1.1 RF measurement (on reference evaluation board)

Measurements done on reference evaluation board under 50 $\Omega\!\!\!/$ de-embedding at CPL-WB-00D3 bumps.

Figure 2. Insertion loss

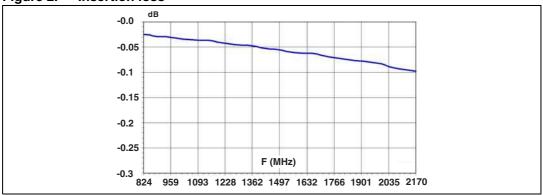


Figure 3. Coupling and isolation

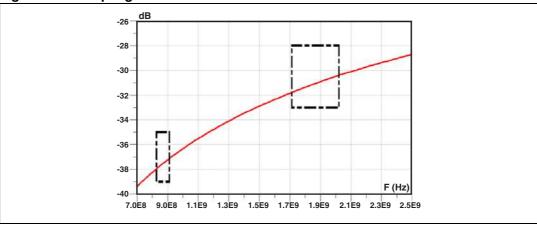
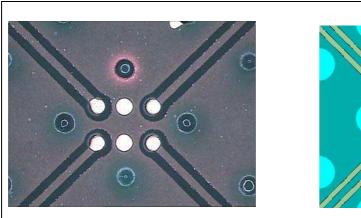


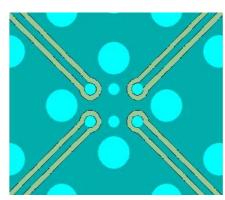
Figure 4. Directivity



2 Reference evaluation board

Figure 5. CPW lines (W = 127 μ m with gap to gnd = 100 μ m) on top layer + GND on layer 2





- Material: 4 layers FR4 with solder mask on top and bottom layer
- Substrate thickness: 0.8 mm
- Line lengths: 14.3 mm
- TRL cal kit available

CPL-WB-00D3 Package information

3 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Figure 6. Package dimensions (bump side)

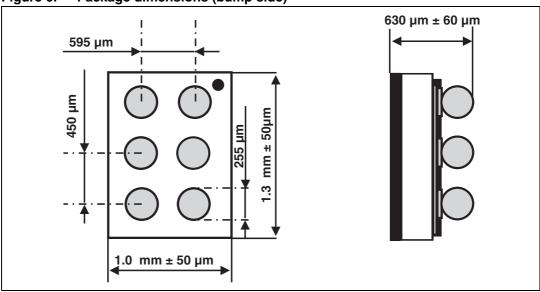


Figure 7. Footprint

Figure 8. Marking

Copper pad Diameter:
220 µm recommended,
260 µm max

Solder stencil opening: 220 µm

Solder mask opening:
320 µm recommendation

Solder mask opening:
320 µm recommendation

Package information CPL-WB-00D3

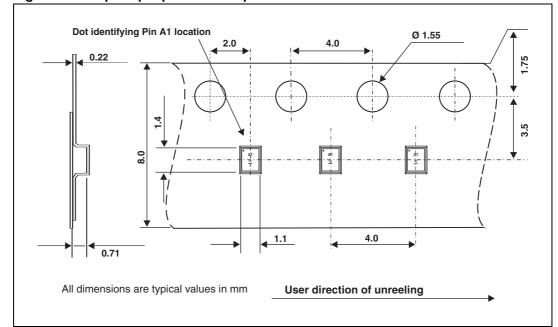


Figure 9. Flip Chip tape and reel specifications

Note: More information is available in the application note:

AN1235: "Flip Chip: package description and recommendations for use"

4 Ordering information

Table 4. Ordering information

Order code	Marking	Base qty	Delivery mode
CPL-WB-00D3	RX	5000	Tape and reel

5 Revision history

Table 5. Document revision history

Date	Revision	Changes
21-Apr-2010	1	Initial release.

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