

T1620T-8I, T1635T-8I

Snubberless[™] 16 A Triac

Datasheet - production data

Features

- High static and dynamic commutation
- Three quadrants
- Snubberless device
- Package is RoHS (2002/95/EC) compliant
- Tab insulated, voltage = 2500 V rms
- UL certified (ref. file E81734)

Applications

- General purpose AC line load switching
- Home appliances:
 - Fan
 - Pump
 - Solenoid
- Lighting
- Heaters
- Inrush current limiting circuits
- Overvoltage crowbar protection circuits

Description

Available in TO220AB-Ins. (ceramic insulated), the T1620T-8I, and T1635T-8I Triacs can be used as on/off or phase angle function controllers in general purpose AC switching.

These devices can be used without snubber (R + C networks) if the datasheet limits are respected.

Provides insulation rated at 2500 V rms (TO-220AB insulated package).

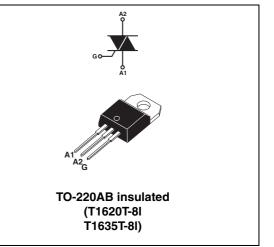


Table 1.Device summary

Order code	Quadrants	Value I _{GT} (mA)
T1620T-8I	- -	20
T1635T-8I	1 - 11 - 111	35

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Doc ID 022134 Rev 2

This is information on a product in full production.

1 Characteristics

Symbol	Parameter		Value	Unit	
1	On state rms surrent (full sins ways)		T _c = 108 °C	16	٨
I _{T(RMS)}	On-state rms current (full sine wave)		T _c = 119 °C	12	A
I	Non repetitive surge peak on-state current (full		t _p = 20 ms	120	А
I _{TSM}	cycle, T _j initial = 25 °C)	cycle, T_j initial = 25 °C) $F = 60 Hz$		126	A
l ² t	I ² t Value for fusing		t _p = 10 ms	95	A ² s
V _{DRM} /	$T_j = 150 \text{ °C}$				V
V _{RRM}	Repetitive peak off-state voltage, gate open	T _j = 125 °C	800	v	
V _{DSM} , V _{RSM}	Non repetitive surge peak off-state voltage	on repetitive surge peak off-state voltage $T_{j} = 10 \text{ ms}$ $T_{j} = 10 \text{ ms}$		900	V
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$		F = 100 Hz	100	A/µs
I _{GM}	Peak gate current	t _p = 20 μs	T _j = 150 °C	4	А
P _{G(AV)}	Average gate power dissipation		T _j = 150 °C	1	W
T _{stg}	Storage junction temperature range			-40 to +150	0°
Тj	Operating junction temperature range	-40 to +150	Ũ		
ΤL	Lead temperature for soldering during 10 s (at 4 mm from case for TO220AB-ins.)	260	°C		
V _{ins} (rms)	Insulation rms voltage, 1 minute, TO220AB ceramic	2500	V		

Table 2. Absolute maximum rating ($T_j = 25$ °C, unless otherwise specified)

Symbol	Toot conditions		Quadrant		Va	lue	Unit
Symbol	Test conditions		Quadrant		T1620T	T1635T	Onit
L (1)	$I_{GT}^{(1)}$ $V_{D} = 12 V, R_{L} = 30 \Omega$		- -	MIN.	1	1.75	mA
'GT ` ′				- -	MAX.	20	35
V _{GT}	V _D = 12 V, RL = 30 Ω		All	MAX.	1.	.3	V
V _{GD}	V_D = 800 V, R_L = 3.3 k Ω , T_j = 125 °C	All	MIN.	0	.2	V	
I _H ⁽¹⁾	I _T = 500 mA		MAX.	25	45	mA	
			-	MAX.	35	55	mA
١L	$I_{\rm G} = 1.2 I_{\rm GT}$		II	IVIAA.	40	65	mA
dV/dt ⁽¹⁾	V _D = 67% x 800 V gate open	T _j = 125 °C		MIN.	1000	2000	V/µs
	$V_{D} = 67\% \text{ x } 600 \text{ V gate open}$ $T_{j} = 150 \text{ °C}$			IVIIIN.	500	1000	v/µ5
(dl/dt)c ⁽¹⁾	$T_j = 125$ (dV/dt)c = snubberless (> 20 V/µs)			MIN.	6	16	A/ms
(ui/ut)c · ·	$(\alpha v/\alpha i) c = shubbeliess (> 20 v/\mu s)$	T _j = 150 °C		IVIIIN.	4.5	12	AVIIIS
t _{GT}	gate controlled turn on time $I_{TM} = 13$ A, V $I_G = 100$ mA, $dI_G/dt = 100$ mA/µs, $R_L = 30$	- -	TYP.	2	2	μs	

Table 3.	Electrical characteristics (T	j = 25 °C,	unless	otherwise	specifi	ed)

1. For both polarities of A2 referenced to A1

Table 4.Static characteristics

Symbol	Test conditions		Value	Unit	
V _{TM} ⁽¹⁾	I _{TM} = 22.6 A, t _p = 380 μs	T _j = 25 °C	MAX.	1.55	V
V_{to} ⁽¹⁾	Threshold voltage	T _j = 150 °C	MAX.	0.85	V
R _d ⁽¹⁾	Dynamic resistance	T _j = 150 °C	MAX.	30	mΩ
	V – V – 200 V	T _j = 25 °C		5	μA
	$V_{DRM} = V_{RRM} = 800 V$	T _j = 125 °C	MAX.	1	m۸
IRRM	$V_{DRM} = V_{RRM} = 600 V$	T _j = 150 °C		3.6	mA

1. for both polarities of A2 referenced to A1

Table 5. Thermal resistance

Table 5.	mermanesistance		
Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case (AC)	2.1	°C/W
R _{th(j-a)}	Junction to ambient	60	°C/W



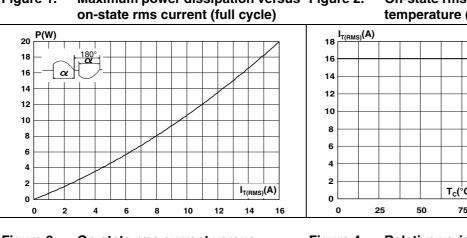
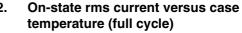
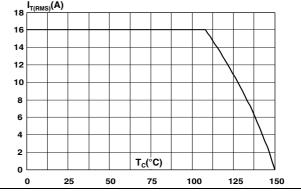


Figure 1. Maximum power dissipation versus Figure 2.





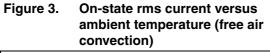


Figure 4. **Relative variation of thermal** impedance versus pulse duration

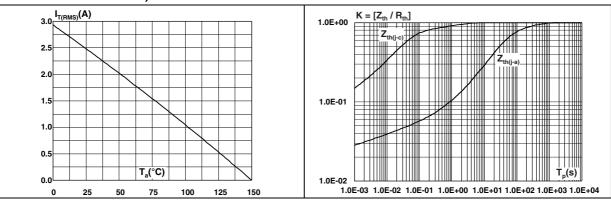
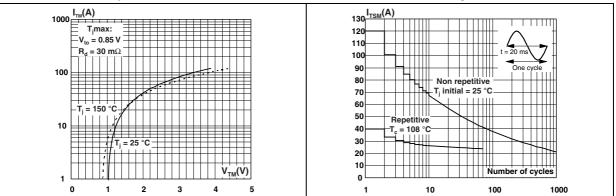


Figure 5. On-state characteristics (maximum Figure 6. values)

Surge peak on-state current versus number of cycles





Non repetitive surge peak on-state Figure 7. current and corresponding values of I²t

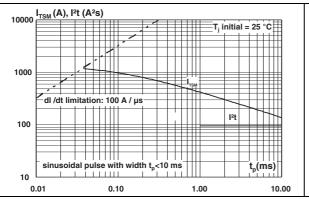


Figure 9. Relative variation of gate trigger voltage versus junction temperature

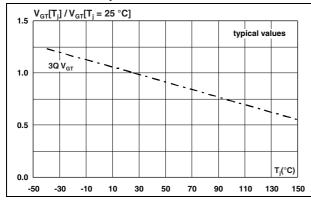
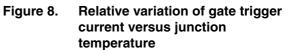


Figure 11. decrease of main current (di/dt)c versus reapplied (dV/dt)c



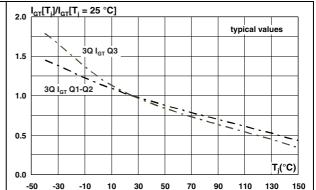
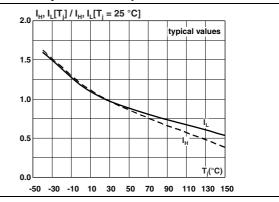
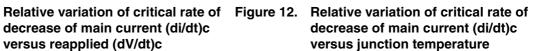
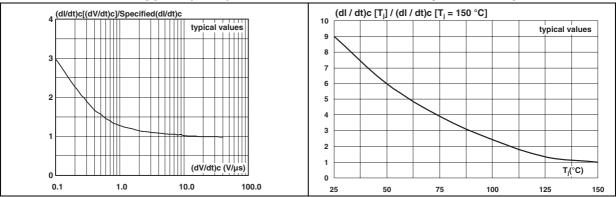


Figure 10. **Relative variation of holding** current and latching current versus junction temperature







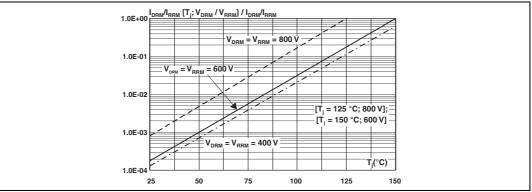


Figure 13. Relative variation of leakage current versus junction temperature for different values of blocking voltage



2 Package information

- Epoxy meets UL94, V0
- Recommended torque value: 0.4 to 0.6 N·m

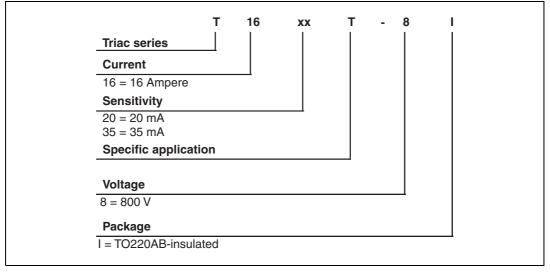
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Table 6. TO-220AB insulated dimensions

					Dimer	nsions		
		Ref.	Mi	illimete	rs		Inches	
			Min.	Тур.	Max.	Min.	Тур.	Max.
		А	15.20		15.90	0.598		0.625
		a1		3.75			0.147	
	C C	a2	13.00		14.00	0.511		0.551
	b2,	В	10.00		10.40	0.393		0.409
	C2 €2 €2 €1	b1	0.61		0.88	0.024		0.034
A		b2	1.23		1.32	0.048		0.051
14 I3 ··		С	4.40		4.60	0.173		0.181
		c1	0.49		0.70	0.019		0.027
+ 12		c2	2.40		2.72	0.094		0.107
a2		е	2.40		2.70	0.094		0.106
		F	6.20		6.60	0.244		0.259
← b1	←→ c1	ØI	3.75		3.85	0.147		0.151
		14	15.80	16.40	16.80	0.622	0.646	0.661
		L	2.65		2.95	0.104		0.116
		12	1.14		1.70	0.044		0.066
		13	1.14		1.70	0.044		0.066
		М		2.60			0.102	

3 Ordering information scheme

Figure 14.	Ordering ir	nformation	scheme
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4 Ordering information

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T1620T-8I	T1620T-8I	TO-220AB insulated	2.3	50	Tube
T1635T-8I	T1635T-8I	TO-220AB insulated	2.3	50	Tube

5 Revision history

Table 8.Document revision history

Date	Revision	Changes
20-Jan-2012	1	First issue.
25-Apr-2012	2	Updated UL certification.



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