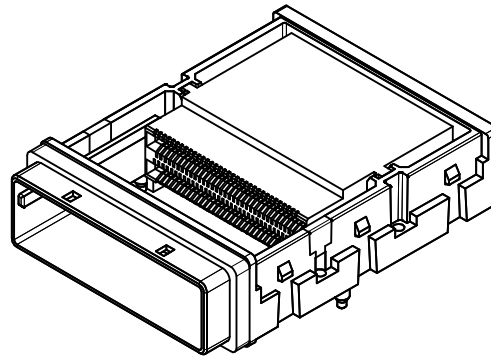
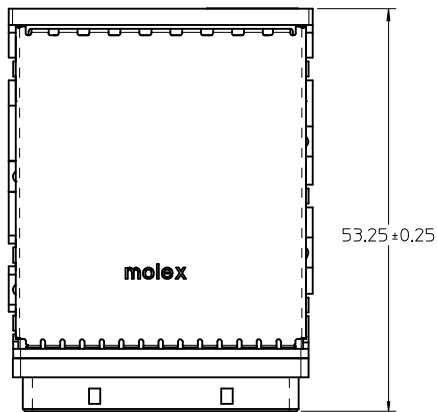
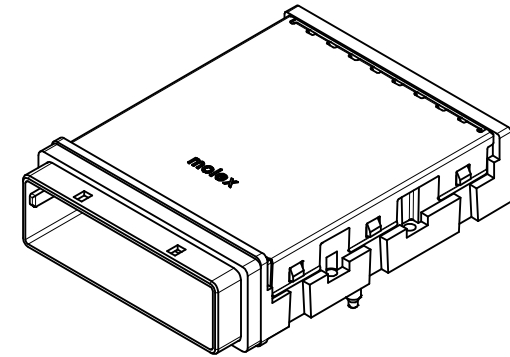
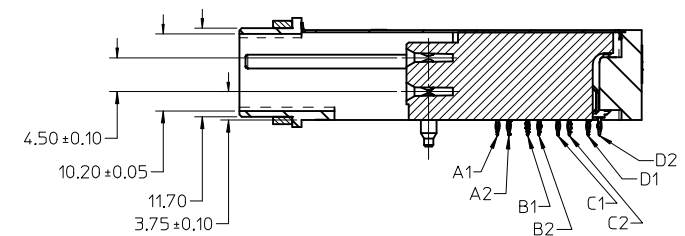
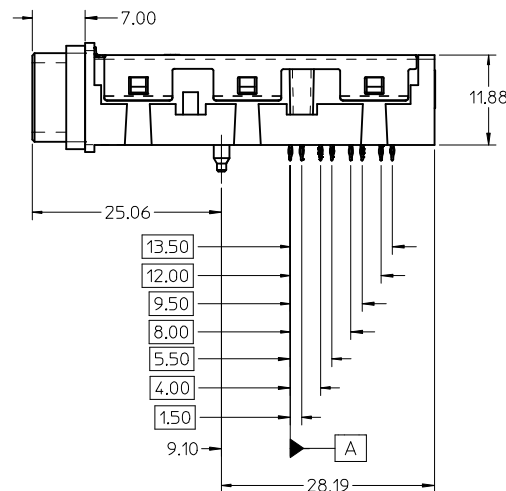
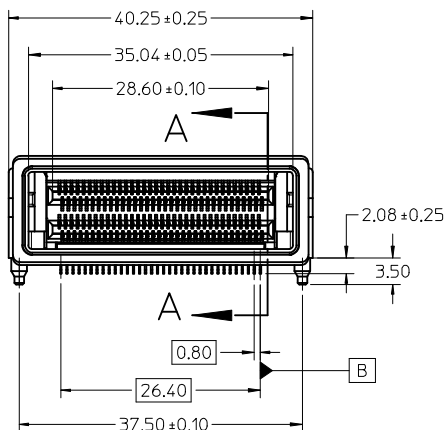




NO COVER OPTION



COVER OPTION



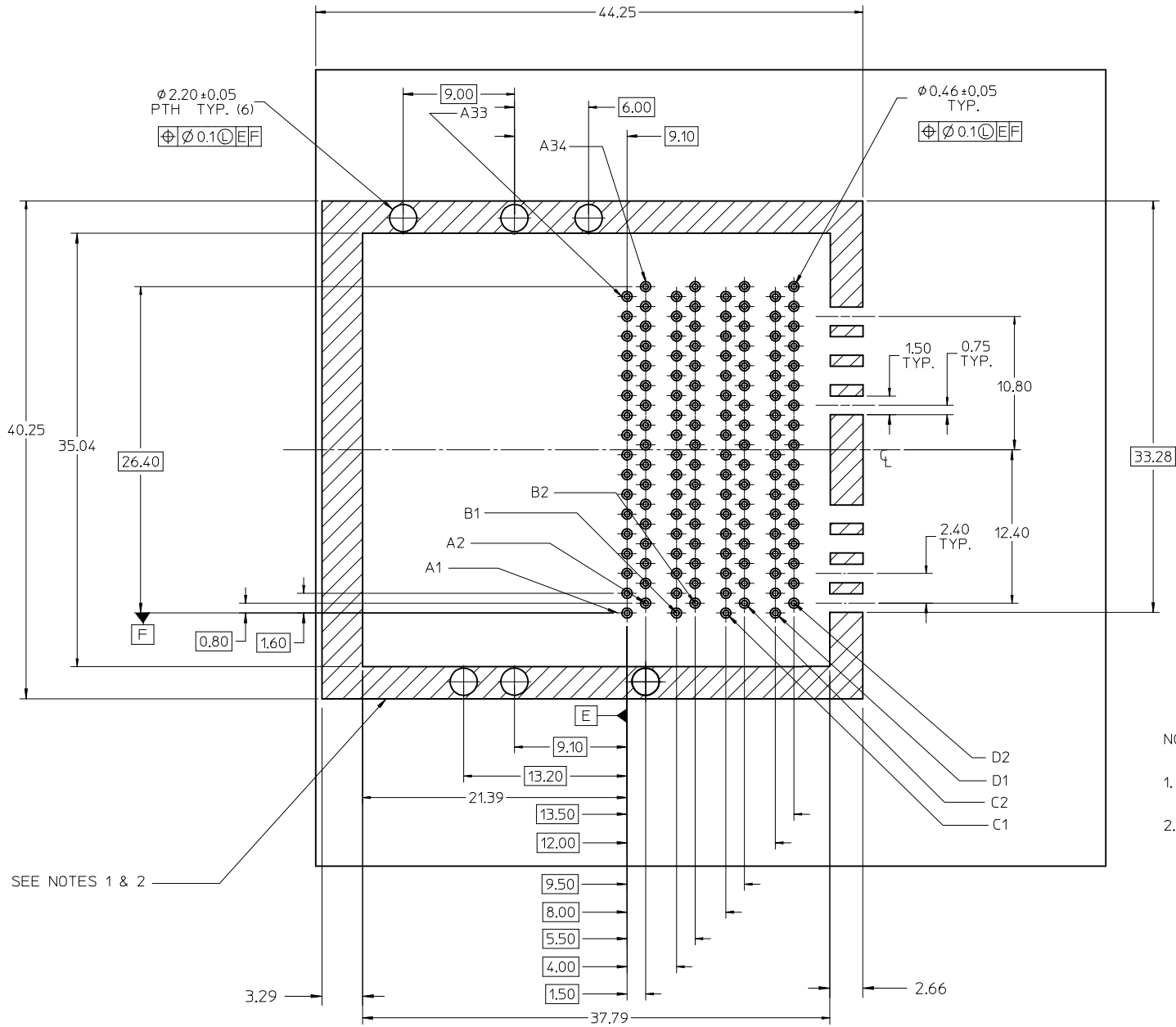
SECTION A-A

NOTES :

- MATERIALS:
CONNECTOR HOUSING - HIGH TEMP THERMOPLASTIC,
GLASS FILLED, UL94V-0
EMI HOUSING - NICKEL PLATED DIE CAST ALLOY
COVER - STAINLESS STEEL ALLOY
EMI GASKET - SEE CHART
- PLATING OPTIONS:
(A) CONTACT AREA - 0.38 μ m MIN GOLD OVER 2.54 μ m MIN NICKEL
COMPLIANT TAIL AREA - 0.76 - 1.52 μ m TIN
OVER 1.27 μ m MIN NICKEL.
(B) CONTACT AREA - 0.76 μ m MIN GOLD OVER 2.54 μ m MIN NICKEL
COMPLIANT TAIL AREA - 0.76 - 1.52 μ m TIN
OVER 1.27 μ m MIN NICKEL.
- PRODUCT SPECIFICATION : PS-75586-001
- THIS CONNECTOR IS DESIGNED TO MATE WITH MOLEX CABLE SERIES 74546.
- PACKAGING SPECIFICATION : TRAY PACK PER PK-75581-001
- MOUNTING HARDWARE REQ'D : M2x8mm CAP SCREWS (4)

PART NUMBER	PLATING OPTION	GASKET TYPE	COVER OPTION
75581-0008	A	CONDUCTIVE POLYMER	YES
75581-0009	B	CONDUCTIVE POLYMER	YES
75581-0018	A	UL 94V-0	YES
75581-0019	B	UL 94V-0	YES
75581-0020	A	UL 94V-0	NO
75581-0021	B	UL 94V-0	NO

ADD P/N UL94V0 EC NO: UCP2014-5033 DRWN:TWCLLELL 2014/05/30 CHKD:TWCLLELLAND 2014/06/03 APPR:MBANAKIS 2014/06/05 H3	DESCRIPTION REV	QUALITY SYMBOLS F_W=0 F_E=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE 2:1	DESIGN UNITS METRIC	 THIRD ANGLE PROJECTION	TITLE 16X PCIE I-PASS CONNECTOR FAMILY 0.80 MM PITCH I/O	
				mm	INCH	DRAWN BY KREGNIER	DATE 10/21/04	MOLEX MOLEX INCORPORATED			
			4 PLACES	± ---	± ---	CHECKED BY	DATE				
			3 PLACES	± ---	± ---	APPROVED BY RNELSON	DATE 10/28/04				
			2 PLACES	± 0.13	± ---	MATERIAL NO.	DOCUMENT NO.				
			1 PLACE	± 0.25	± ---	ANGULAR ±1/2°			SHEET NO. 1 OF 3		
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS			SEE CHART		SD-75581-001			
			SIZE			THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					



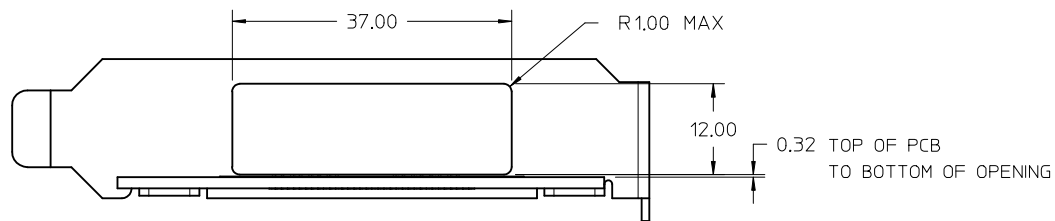
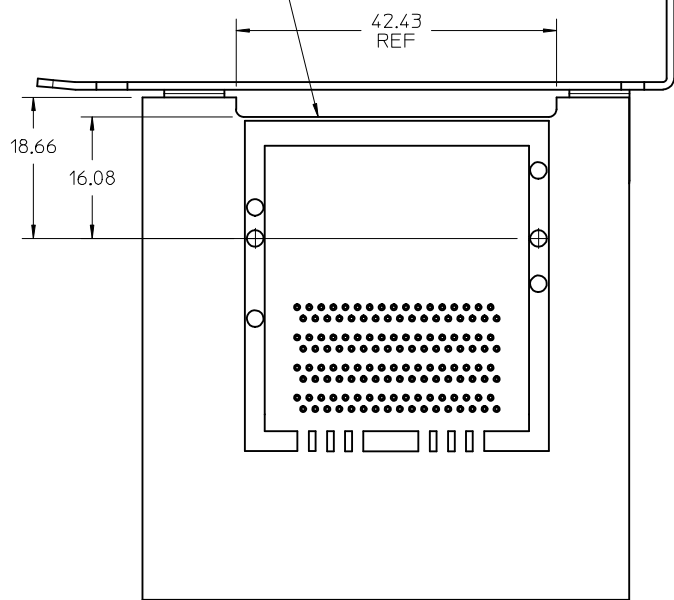
- NOTES :
- 1. CROSS HATCHED AREA TO BE CONDUCTIVE ON THE PCB.
 - 2. CROSS HATCHED AREA MUST BE INCORPORATED IN THE FLAT ROCK PRESS-FIT TOOLING.

RECOMMENDED PCB LAYOUT : COMPONENT SIDE SHOWN

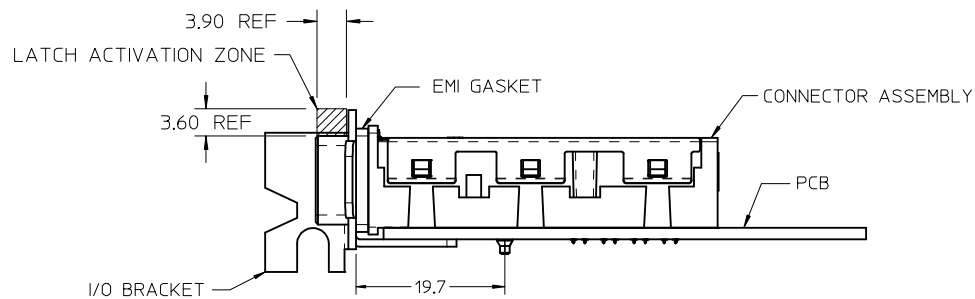
PCB THICKNESS : 1.35mm MIN.

SEE SHEET 1 EC NO: UCP2014-5033 DRWN:TMCCLELL 2014/05/30 CHKD:TMCCLELLAND 2014/06/03 APPR:MBANAKIS 2014/06/05	QUALITY SYMBOLS F=0 E=0	DESCRIPTION REV	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE 4:1	DESIGN UNITS METRIC	 THIRD ANGLE PROJECTION			
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	mm	INCH	DRAWN BY KREGNIER	DATE 10/21/04	TITLE 16X PCIE I-PASS CONNECTOR FAMILY 0.80 MM PITCH I/O  MOLEX INCORPORATED				
				4 PLACES	± ---	± ---	CHECKED BY				DATE	
				3 PLACES	± ---	± ---						
				2 PLACES	± 0.13	± ---						
				1 PLACE	± 0.25	± ---						
				ANGULAR ±1/2°		APPROVED BY RNELSON	DATE 10/28/04	DOCUMENT NO. SD-75581-001				
						MATERIAL NO.						
						SEE CHART		SHEET NO. 2 OF 3				
						SIZE  C						
THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION												

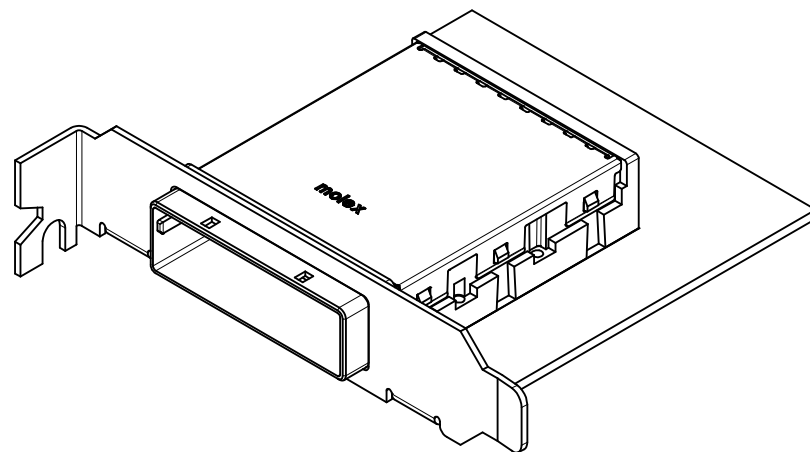
PCIe PCB APPLICATION
PCB EDGE CUT OUT



I/O BRACKET CUT-OUT
(NOT SUPPLIED)



ASSEMBLED VIEW



SEE SHEET 1 EC NO: UCP2014-5033 DRWN:TMCCLELL 2014/05/30 CHKD:TMCCLELLAND 2014/06/03 APPR:MBANAKIS 2014/06/05 H3	QUALITY SYMBOLS		GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE 2:1	DESIGN UNITS METRIC	 THIRD ANGLE PROJECTION
	▽=0 ▽=0	DESCRIPTION	mm	INCH	DRAWN BY KREGNIER	DATE 10/21/04	TITLE 16X PCIE I-PASS CONNECTOR FAMILY 0.80 MM PITCH I/O		
			4 PLACES ± ---	± ---	CHECKED BY	DATE	MOLEX MOLEX INCORPORATED		
			3 PLACES ± ---	± ---	APPROVED BY RNELSON 10/28/04				
			2 PLACES ± 0.13	± ---					
			1 PLACE ± 0.25	± ---					
			ANGULAR ±1/2°		MATERIAL NO.		DOCUMENT NO.		SHEET NO. 3 OF 3
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SEE CHART		SD-75581-001		
			SIZE C	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					