International Rectifier

REPETITIVE AVALANCHE AND dv/dt RATED
HEXFET®TRANSISTORS THRU-HOLE (TO-204AA/AE)

IRF9140
100V, P-CHANNEL

Product Summary

Part Number	BVDSS	$R_{DS(on)}$	I_D
IRF9140	-100V	0.2Ω	-18A


TO-3

The HEXFET®technology is the key to International Rectifier's advanced line of power MOSFET transistors. The efficient geometry and unique processing of this latest "State of the Art" design achieves: very low on-state resistance combined with high transconductance; superior reverse energy and diode recovery dv/dt capability.

The HEXFET transistors also feature all of the well established advantages of MOSFETs such as voltage control, very fast switching, ease of parelleling and temperature stability of the electrical parameters.

They are well suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers and high energy pulse circuits.

Features:
- Repetitive Avalanche Ratings
- Dynamic dv/dt Rating
- Hermetically Sealed
- Simple Drive Requirements
- Ease of Paralleling

Absolute Maximum Ratings

	Parameter		Units
I_D @ V_{GS} = 0V, T_C = 25°C	Continuous Drain Current	-18	A
I_D @ V_{GS} = 0V, T_C = 100°C	Continuous Drain Current	-11	
I_{DM}	Pulsed Drain Current ①	-72	
P_D @ T_C = 25°C	Max. Power Dissipation	125	W
	Linear Derating Factor	1.0	W/°C
V_{GS}	Gate-to-Source Voltage	±20	V
E_{AS}	Single Pulse Avalanche Energy ②	500	mJ
I_{AR}	Avalanche Current ①	-18	A
E_{AR}	Repetitive Avalanche Energy ①	12.5	mJ
dv/dt	Peak Diode Recovery dv/dt ③	-5.5	V/ns
T_J T_{STG}	Operating Junction Storage Temperature Range	-55 to 150	°C
	Lead Temperature	300 (0.063 in. (1.6mm) from case for 10s)	
	Weight	11.5(typical)	g

For footnotes refer to the last page

Electrical Characteristics @ Tj = 25°C (Unless Otherwise Specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	-100	—	—	V	V_{GS} = 0V, I_D = -1.0mA
$\Delta BV_{DSS}/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	-0.087	—	V/°C	Reference to 25°C, I_D = -1.0mA
$R_{DS(on)}$	Static Drain-to-Source On-State Resistance	—	—	0.2	Ω	V_{GS} = -10V, I_D = -11A ④
		—	—	0.23		V_{GS} =-10V, I_D = -18A ④
$V_{GS(th)}$	Gate Threshold Voltage	-2.0	—	-4.0	V	V_{DS} = V_{GS}, I_D = -250μA
g_{fs}	Forward Transconductance	6.2	—	—	S (Ʊ)	V_{DS} > -15V, I_{DS} = -11A ④
I_{DSS}	Zero Gate Voltage Drain Current	—	—	-25	μA	V_{DS}= -80V, V_{GS}=0V
		—	—	-250		V_{DS} = -80V V_{GS} = 0V, T_J = 125°C
I_{GSS}	Gate-to-Source Leakage Forward	—	—	-100	nA	V_{GS} = -20V
I_{GSS}	Gate-to-Source Leakage Reverse	—	—	100		V_{GS} = 20V
Q_g	Total Gate Charge	31	—	60	nC	V_{GS} =-10V, ID = -18A
Q_{gs}	Gate-to-Source Charge	3.7	—	13		V_{DS}= -50V
Q_{gd}	Gate-to-Drain ('Miller') Charge	7.0	—	35.2		
$t_{d(on)}$	Turn-On Delay Time	—	—	35	ns	V_{DD} = -50V, I_D = -18A,
t_r	Rise Time	—	—	85		V_{GS} =-10V, R_G = 9.1Ω
$t_{d(off)}$	Turn-Off Delay Time	—	—	85		
t_f	Fall Time	—	—	65		
$L_S + L_D$	Total Inductance	—	6.1	—	nH	Measured from drain lead (6mm/ 0.25in. from package) to source lead (6mm/0.25in. from package)
C_{iss}	Input Capacitance	—	1400		pF	V_{GS} = 0V, V_{DS} = -25V
C_{oss}	Output Capacitance	—	600	—		f = 1.0MHz
C_{rss}	Reverse Transfer Capacitance	—	200	—		

Source-Drain Diode Ratings and Characteristics

	Parameter	Min	Typ	Max	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)	—	—	-18	A	
I_{SM}	Pulse Source Current (Body Diode) ①	—	—	-72		
V_{SD}	Diode Forward Voltage	—	—	-5.0	V	T_j = 25°C, I_S =-18A, V_{GS} = 0V ④
t_{rr}	Reverse Recovery Time	—	170	280	nS	T_j = 25°C, I_F = -18A, di/dt ≤ -100A/μs
Q_{RR}	Reverse Recovery Charge	—	—	3.6	μC	V_{DD} ≤ -50V ④
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $L_S + L_D$.				

Thermal Resistance

	Parameter	Min	Typ	Max	Units	Test Conditions
R_{thJC}	Junction-to-Case	—	—	1.0	°C/W	
R_{thJA}	Junction-to-Ambient	—	—	30		Soldered to a 2" square copper-clad board

Note: Corresponding Spice and Saber models are available on International Rectifier Website.
For footnotes refer to the last page

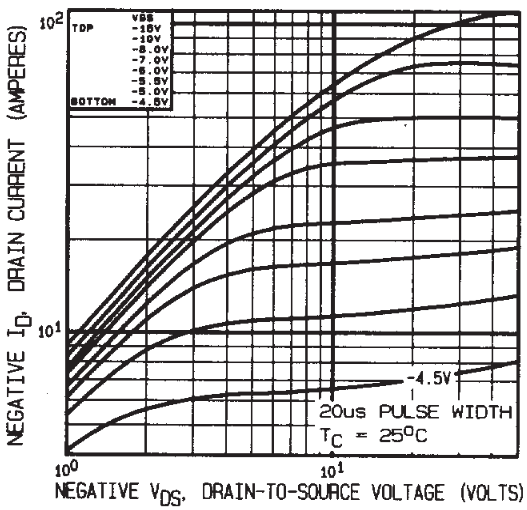


Fig 1. Typical Output Characteristics

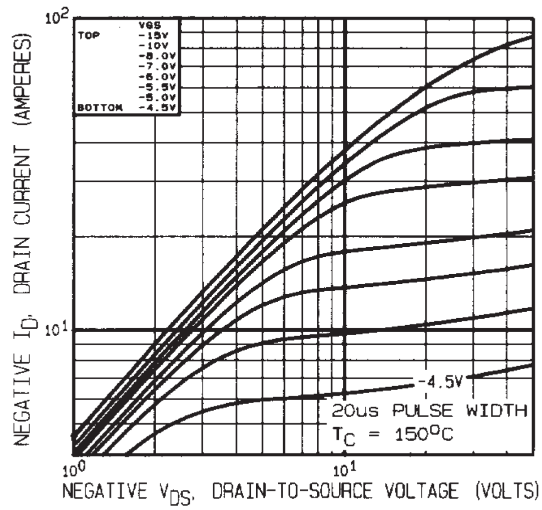


Fig 2. Typical Output Characteristics

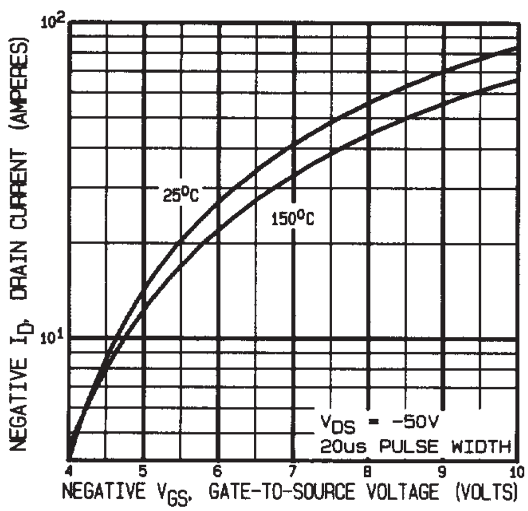


Fig 3. Typical Transfer Characteristics

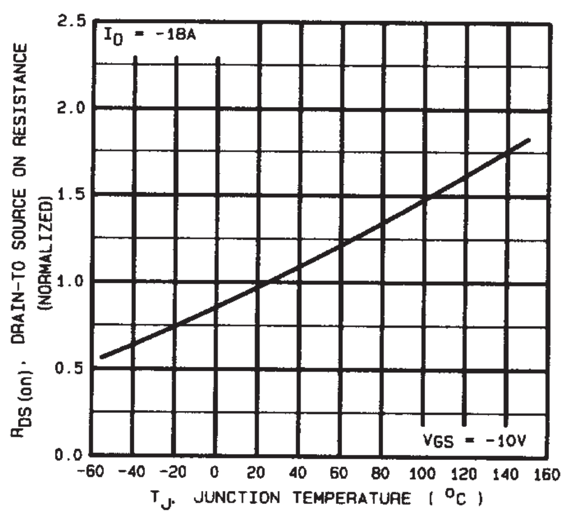


Fig 4. Normalized On-Resistance
Vs. Temperature

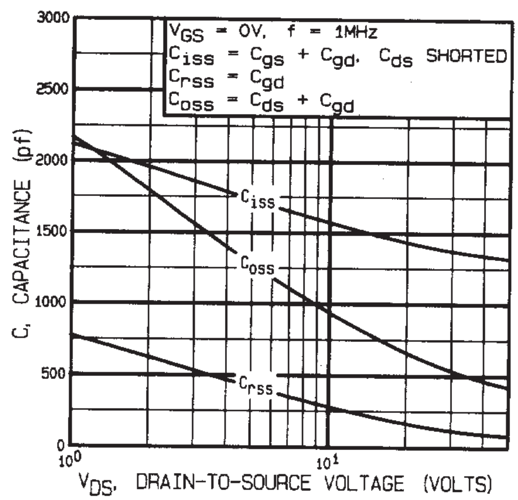


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

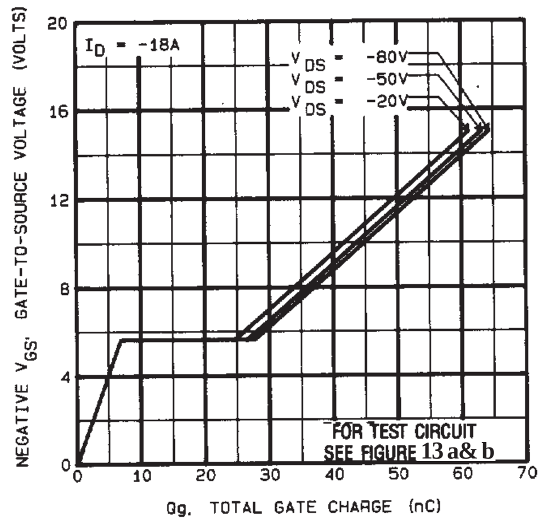


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

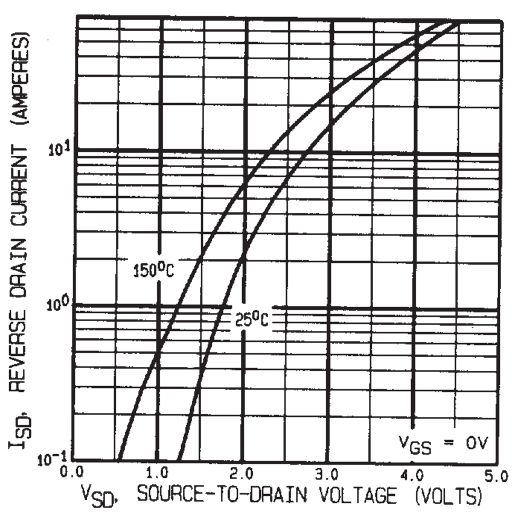


Fig 7. Typical Source-Drain Diode Forward Voltage

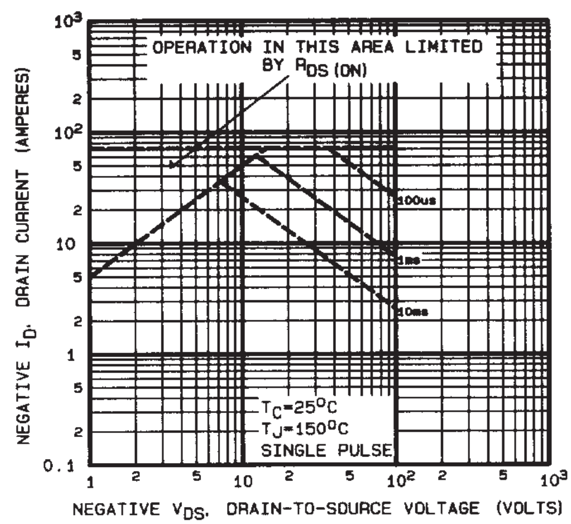


Fig 8. Maximum Safe Operating Area

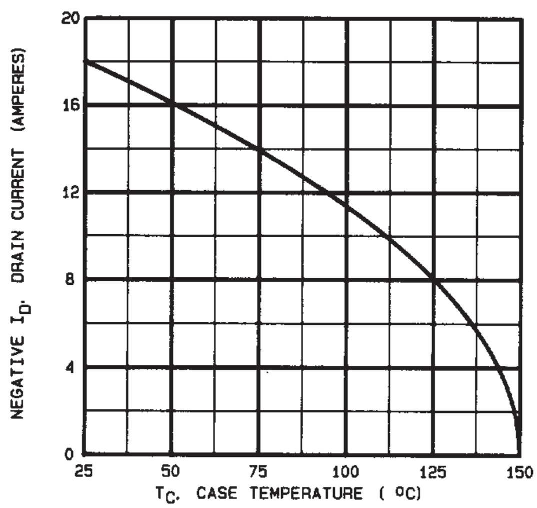


Fig 9. Maximum Drain Current Vs. Case Temperature

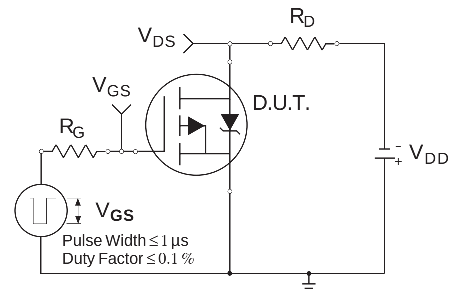


Fig 10a. Switching Time Test Circuit

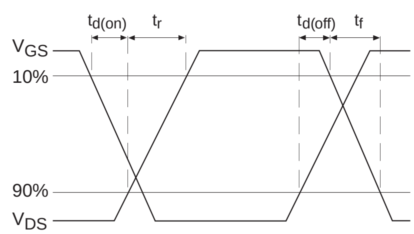


Fig 10b. Switching Time Waveforms

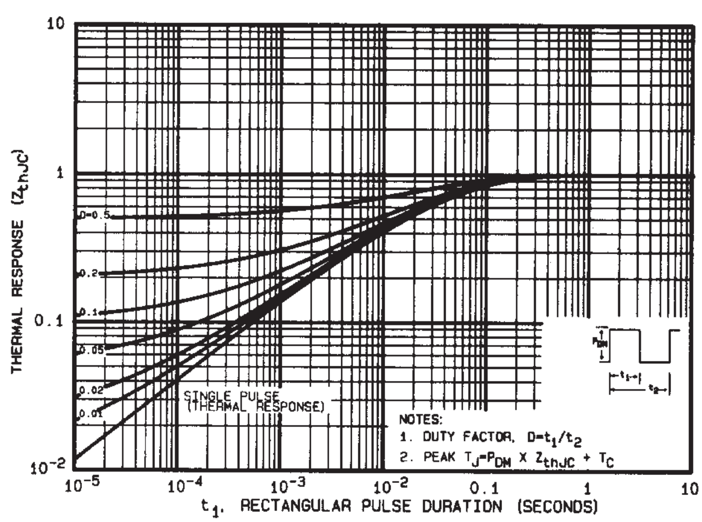


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

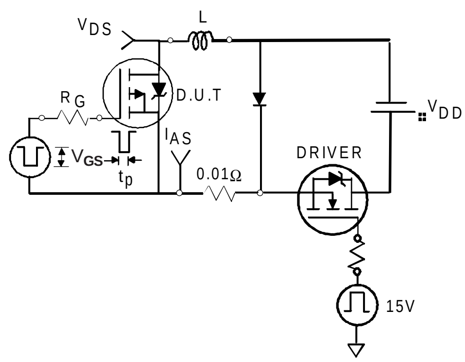


Fig 12a. Unclamped Inductive Test Circuit

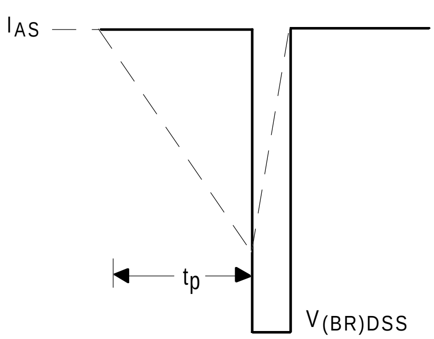


Fig 12b. Unclamped Inductive Waveforms

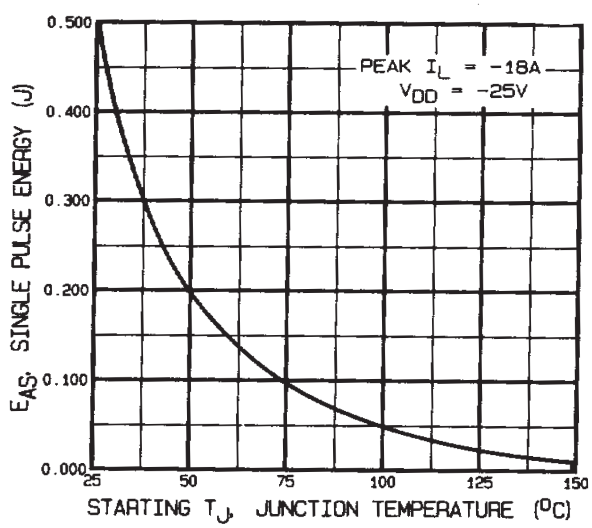


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

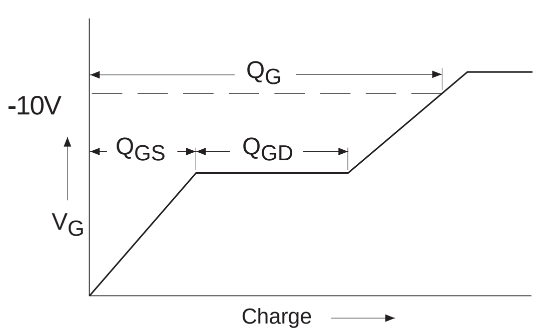


Fig 13a. Basic Gate Charge Waveform

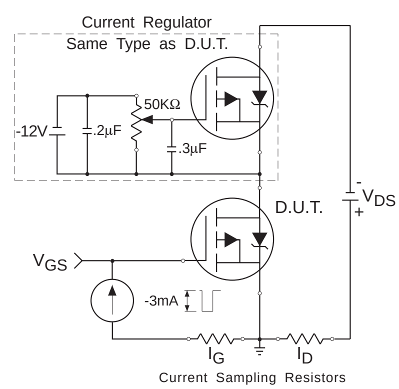


Fig 13b. Gate Charge Test Circuit

Foot Notes:

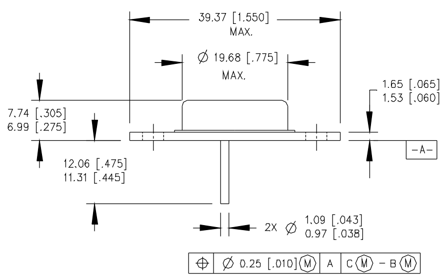
① Repetitive Rating; Pulse width limited by maximum junction temperature.

② V_{DD} = -25V, starting T_J = 25°C, Peak I_L = -18A, V_{GS} = -10V

③ I_{SD} ≤ -18A, di/dt ≤ -100A/μs, V_{DD} ≤ -100V, T_J ≤ 150°C

④ Pulse width ≤ 300 μs; Duty Cycle ≤ 2%

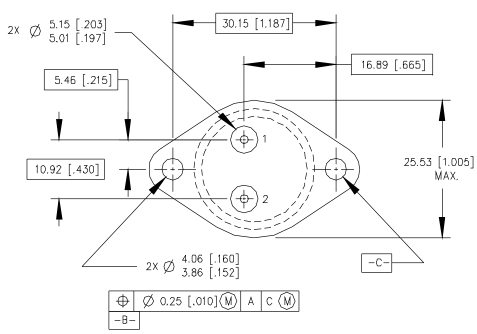
Case Outline and Dimensions —TO-204AA (Modified TO-3)



PIN ASSIGNMENTS

HEXFET

1 - SOURCE
2 - GATE
3 - DRAIN (CASE)



NOTES:

1. DIMENSIONING & TOLERANCING PER ANSI Y14.5M-1982.
2. CONTROLLING DIMENSION : INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
4. OUTLINE CONFORMS TO JEDEC OUTLINE TO-204-AA.

International
IOR Rectifier
IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105
IR LEOMINSTER : 205 Crawford St., Leominster, Massachusetts 01453, USA Tel: (978) 534-5776
TAC Fax: (310) 252-7903
Visit us at www.irf.com for sales contact information.
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