

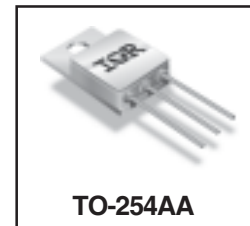
International
IR Rectifier
**RADIATION HARDENED
 POWER MOSFET
 THRU-HOLE (T0-254AA)**

PD-91564G

**IRHM7064
 JANSR2N7431
 60V, N-CHANNEL
 REF:MIL-PRF-19500/663
 RAD-Hard™ HEXFET® TECHNOLOGY**

Product Summary

Part Number	Radiation Level	R _{DS(on)}	I _D	QPL Part Number
IRHM7064	100K Rads (Si)	0.021Ω	35A*	JANSR2N7431
IRHM3064	300K Rads (Si)	0.021Ω	35A*	JANSF2N7431
IRHM4064	500K Rads (Si)	0.021Ω	35A*	JANSG2N7431
IRHM8064	1000K Rads (Si)	0.030Ω	35A*	JANSH2N7431



International Rectifier's RAD-Hard™ HEXFET® technology provides high performance power MOSFETs for space applications. This technology has over a decade of proven performance and reliability in satellite applications. These devices have been characterized for both Total Dose and Single Event Effects (SEE). The combination of low R_{DS(on)} and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well established advantages of MOSFETs such as voltage control, fast switching, ease of paralleling and temperature stability of electrical parameters.

Features:

- Single Event Effect (SEE) Hardened
- Low R_{DS(on)}
- Low Total Gate Charge
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Ceramic Package
- Light Weight

Absolute Maximum Ratings

Pre-Irradiation

	Parameter		Units
I _D @ V _{GS} = 12V, T _C = 25°C	Continuous Drain Current	35*	A
I _D @ V _{GS} = 12V, T _C = 100°C	Continuous Drain Current	35*	
I _{DM}	Pulsed Drain Current ①	140	
P _D @ T _C = 25°C	Max. Power Dissipation	250	W
	Linear Derating Factor	2.0	W/°C
V _{GS}	Gate-to-Source Voltage	±20	V
EAS	Single Pulse Avalanche Energy ②	500	mJ
I _{AR}	Avalanche Current ①	35	A
E _{AR}	Repetitive Avalanche Energy ①	25	mJ
dv/dt	Peak Diode Recovery dv/dt ③	2.5	V/ns
T _J	Operating Junction	-55 to 150	°C
T _{STG}	Storage Temperature Range		
	Lead Temperature	300 (0.063 in.(1.6mm) from case for 10s)	
	Weight	9.3 (Typical)	g

*Current is limited by package
 For footnotes refer to the last page

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Electrical Characteristics @ T_j = 25°C (Unless Otherwise Specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
B _V D _{SS}	Drain-to-Source Breakdown Voltage	60	—	—	V	V _{GS} = 0V, I _D = 1.0mA
ΔB _V D _{SS} /ΔT _J	Temperature Coefficient of Breakdown Voltage	—	0.056	—	V/°C	Reference to 25°C, I _D = 1.0mA
R _{DS(on)}	Static Drain-to-Source On-State Resistance	—	—	0.021	Ω	V _{GS} = 12V, I _D = 35A ④
V _{GS(th)}	Gate Threshold Voltage	2.0	—	4.0	V	V _{DS} = V _{GS} , I _D = 1.0mA
g _{fs}	Forward Transconductance	18	—	—	S	V _{DS} ≥ 15V, I _{DS} = 35A ④
I _{DSS}	Zero Gate Voltage Drain Current	—	—	25	μA	V _{DS} = 48V, V _{GS} = 0V
		—	—	250		V _{DS} = 48V, V _{GS} = 0V, T _J = 125°C
I _{GSS}	Gate-to-Source Leakage Forward	—	—	100	nA	V _{GS} = 20V
I _{GSS}	Gate-to-Source Leakage Reverse	—	—	-100		V _{GS} = -20V
Q _g	Total Gate Charge	—	—	270	nC	V _{GS} = 12V, I _D = 35A
Q _{gs}	Gate-to-Source Charge	—	—	60		V _{DS} = 30V
Q _{gd}	Gate-to-Drain ('Miller') Charge	—	—	110		
t _{d(on)}	Turn-On Delay Time	—	—	27	ns	V _{DD} = 30V, I _D = 35A
t _r	Rise Time	—	—	120		V _{GS} = 12V, R _G = 2.35Ω
t _{d(off)}	Turn-Off Delay Time	—	—	120		
t _f	Fall Time	—	—	100		
L _S + L _D	Total Inductance	—	6.8	—	nH	Measured from Drain lead (6mm /0.25in from package) to Source lead (6mm /0.25in from package) with Source wires internally bonded from Source Pin to Drain Pad
C _{iss}	Input Capacitance	—	4900	—	pF	V _{GS} = 0V, V _{DS} = 25V
C _{oss}	Output Capacitance	—	2800	—		f = 1.0MHz
C _{rss}	Reverse Transfer Capacitance	—	860	—		

Source-Drain Diode Ratings and Characteristics

	Parameter	Min	Typ	Max	Units	Test Conditions
I _S	Continuous Source Current (Body Diode)	—	—	35*	A	
I _{SM}	Pulse Source Current (Body Diode) ①	—	—	140		
V _{SD}	Diode Forward Voltage	—	—	1.5	V	T _j = 25°C, I _S = 35A, V _{GS} = 0V ④
t _{rr}	Reverse Recovery Time	—	—	360	ns	T _j = 25°C, I _F = 35A, di/dt ≤ 100A/μs
Q _{RR}	Reverse Recovery Charge	—	—	3.1	μC	V _{DD} ≤ 50V ④
t _{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by L _S + L _D .				

*Current is limited by package

Thermal Resistance

	Parameter	Min	Typ	Max	Units	Test Conditions
R _{thJC}	Junction-to-Case	—	—	0.50	°C/W	
R _{thJA}	Junction-to-Ambient	—	—	48		Typical socket mount
R _{thCS}	Case-to-Sink	—	0.21	—		

Note: Corresponding Spice and Saber models are available on International Rectifier Website.

For footnotes refer to the last page

Radiation Characteristics

IRHM7064, JANSR2N7431

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

Table 1. Electrical Characteristics @ Tj = 25°C, Post Total Dose Irradiation ⑤⑥

	Parameter	Up to 500K Rads(Si) ¹		1000K Rads (Si) ²		Units	Test Conditions
		Min	Max	Min	Max		
BV _{DSS}	Drain-to-Source Breakdown Voltage	60	—	60	—	V	V _{GS} = 0V, I _D = 1.0mA
V _{GS(th)}	Gate Threshold Voltage	2.0	4.0	1.25	4.5		V _{GS} = V _{DS} , I _D = 1.0mA
I _{GSS}	Gate-to-Source Leakage Forward	—	100	—	100	nA	V _{GS} = 20V
I _{GSS}	Gate-to-Source Leakage Reverse	—	-100	—	-100		V _{GS} = -20 V
I _{DSS}	Zero Gate Voltage Drain Current	—	25	—	50	μA	V _{DS} = 48V, V _{GS} = 0V
R _{DS(on)}	Static Drain-to-Source ④ On-State Resistance (TO-3)	—	0.021	—	0.030	Ω	V _{GS} = 12V, I _D = 35A
V _{SD}	Diode Forward Voltage ④	—	1.5	—	1.5	V	V _{GS} = 0V, I _S = 35A

1. Part numbers IRHM7064 (JANSR2N7431), IRHM3064 (JANSF2N7431) and IRHM4064 (JANSR2N7431)
2. Part number IRHM8064 (JANSH2N7431U)

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

Table 2. Single Event Effect Safe Operating Area

Ion	LET (MeV/(mg/cm ²))	Energy (MeV)	Range (μm)	V _{DS} (V)				
				@V _{GS} =0V	@V _{GS} =-5V	@V _{GS} =-10V	@V _{GS} =-15V	@V _{GS} =-20V
Br	36.8	305	39	60	60	45	40	30
I	59.9	345	32.8	40	35	30	25	20

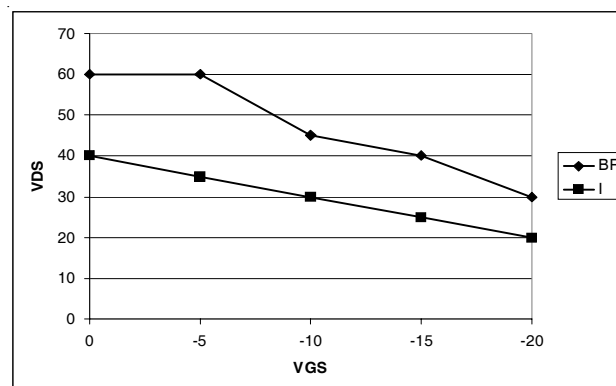


Fig a. Single Event Effect, Safe Operating Area

For footnotes refer to the last page

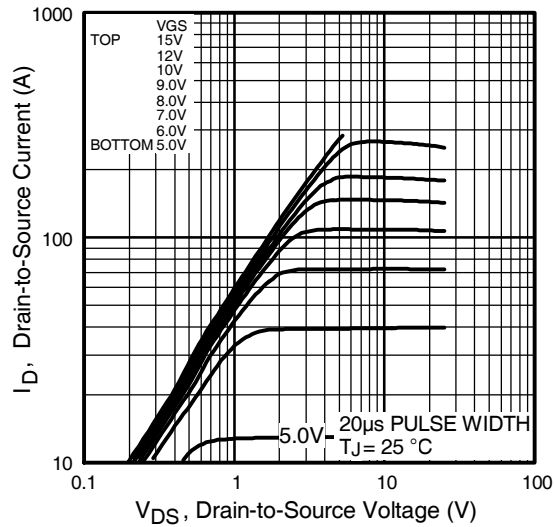


Fig 1. Typical Output Characteristics

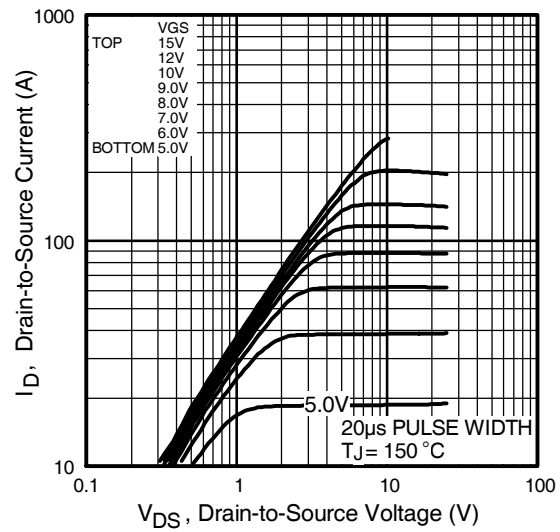


Fig 2. Typical Output Characteristics

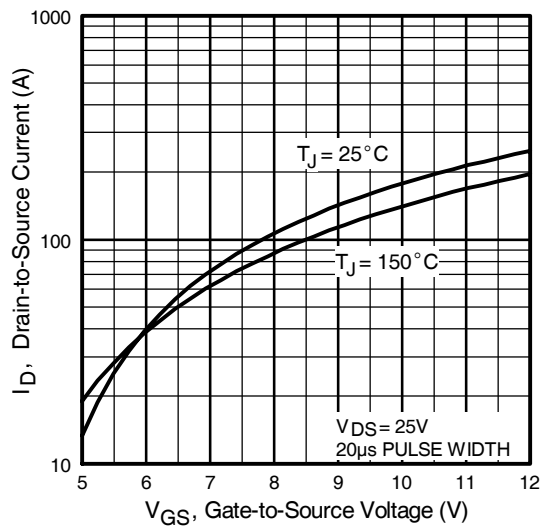


Fig 3. Typical Transfer Characteristics

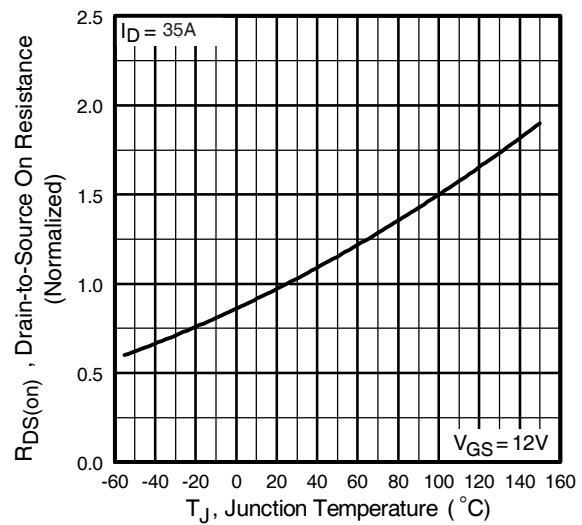


Fig 4. Normalized On-Resistance Vs. Temperature

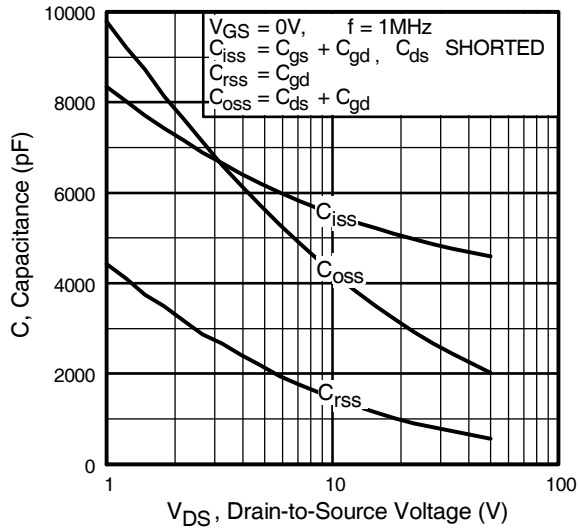


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

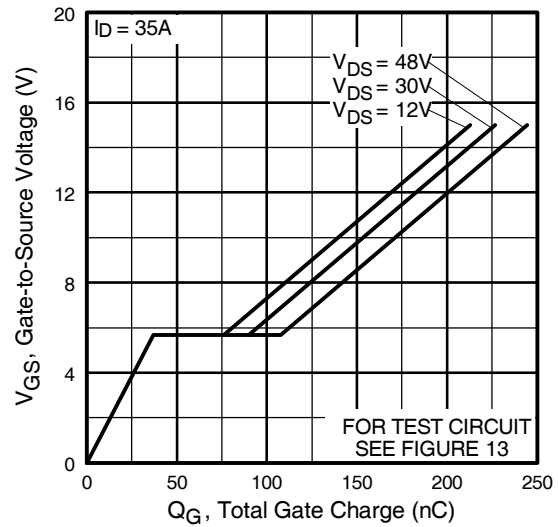


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

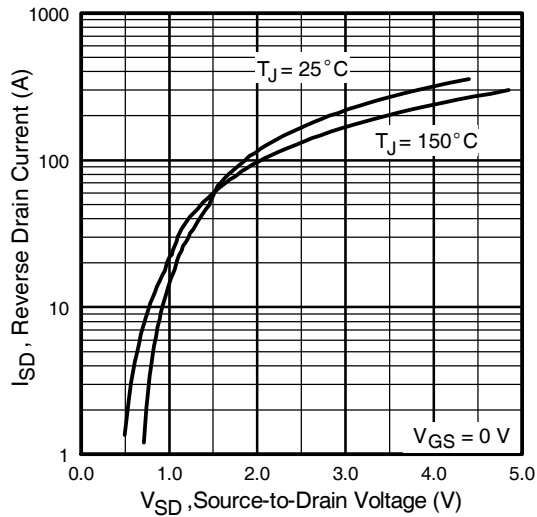


Fig 7. Typical Source-Drain Diode Forward Voltage

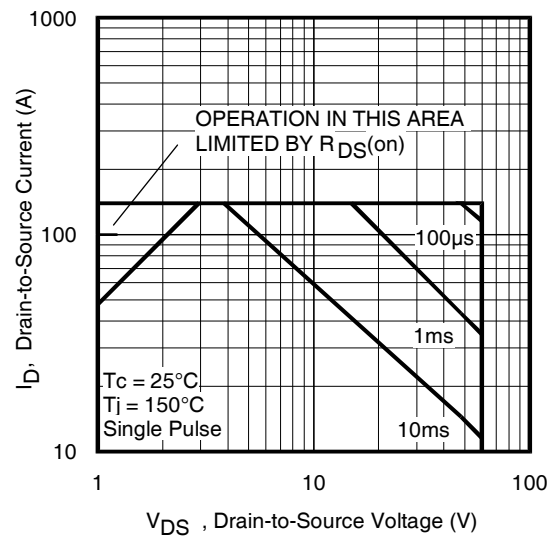


Fig 8. Maximum Safe Operating Area

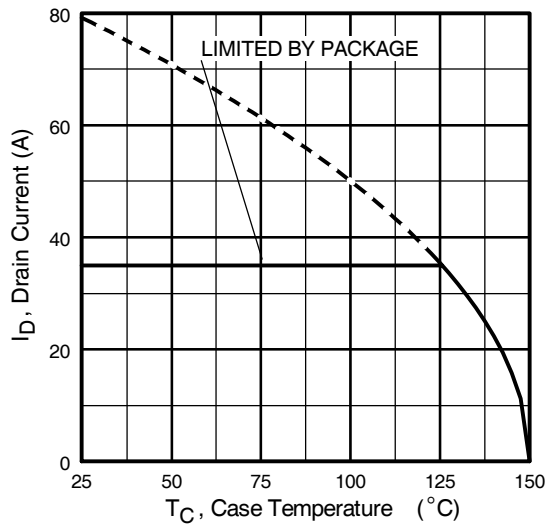


Fig 9. Maximum Drain Current Vs. Case Temperature

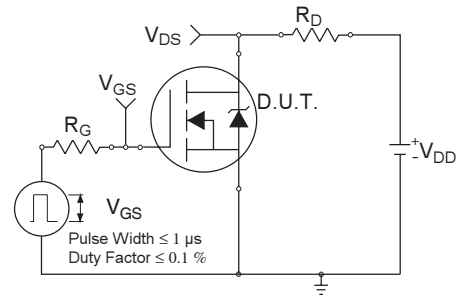


Fig 10a. Switching Time Test Circuit

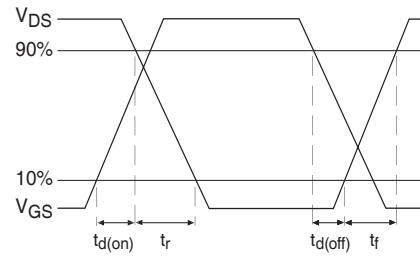


Fig 10b. Switching Time Waveforms

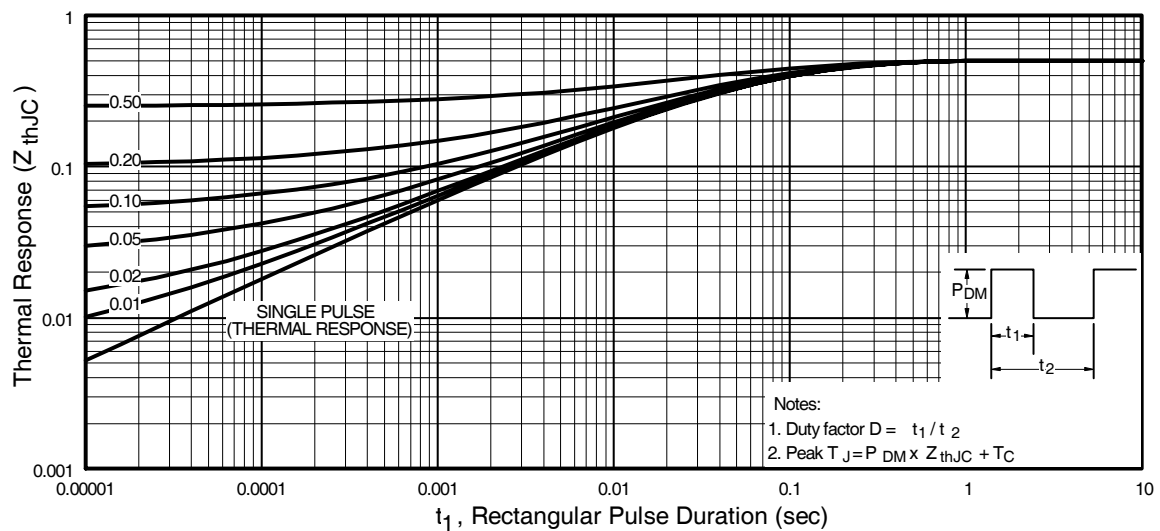


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

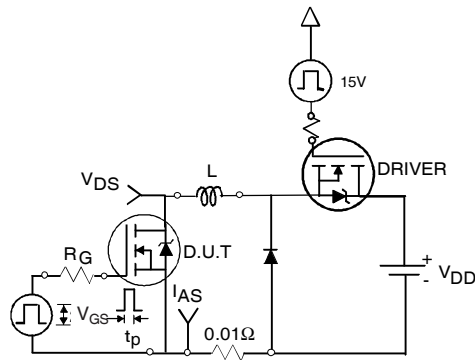


Fig 12a. Unclamped Inductive Test Circuit

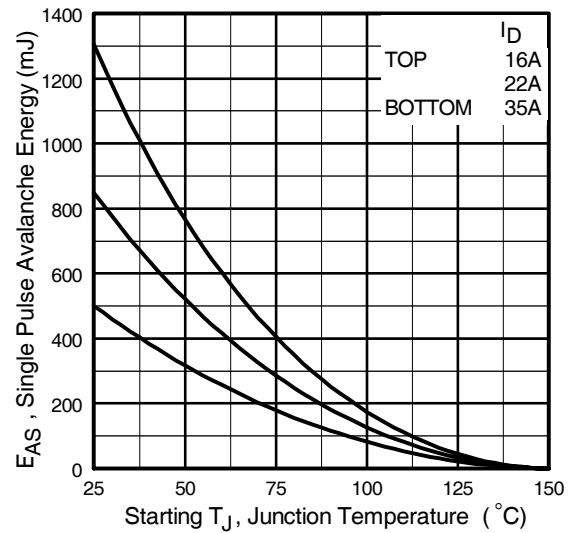


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

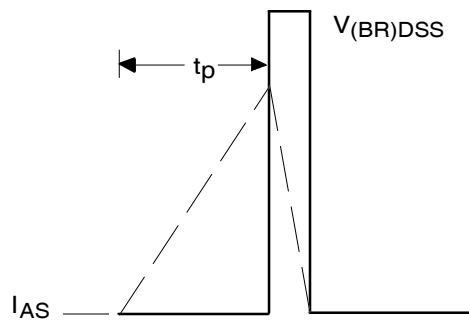


Fig 12b. Unclamped Inductive Waveforms

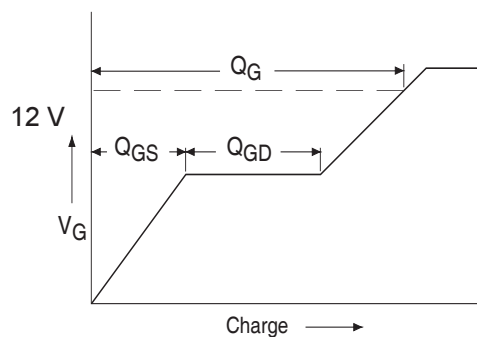


Fig 13a. Basic Gate Charge Waveform

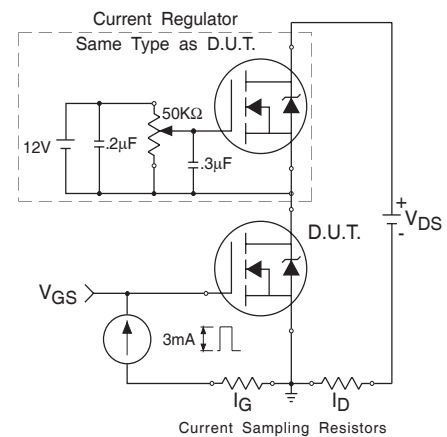
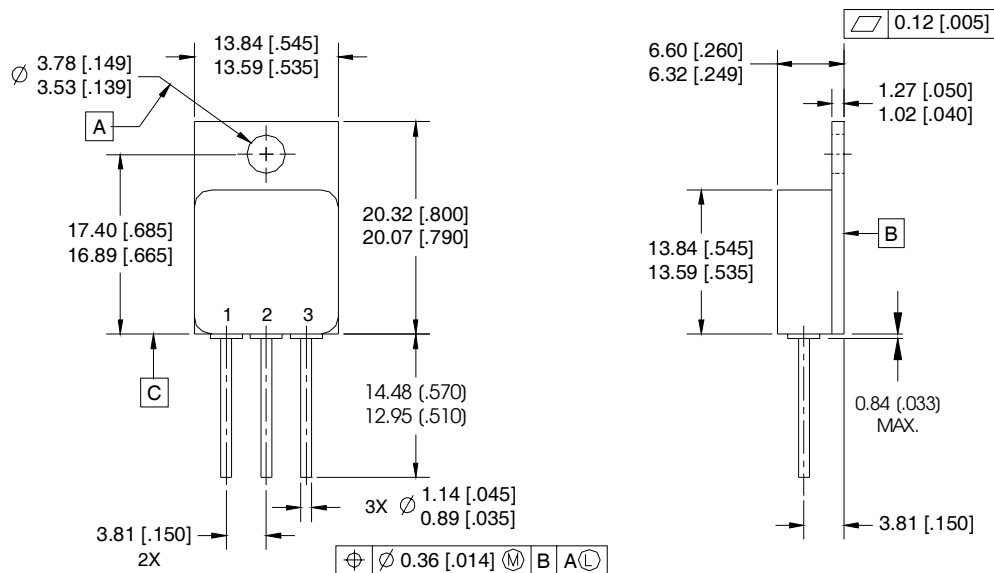


Fig 13b. Gate Charge Test Circuit

Foot Notes:

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ② $V_{DD} = 25V$, starting $T_J = 25^\circ C$, $L = 0.82mH$
Peak $I_L = 35A$, $V_{GS} = 12V$
- ③ $I_{SD} \leq 35A$, $di/dt \leq 220A/\mu s$,
 $V_{DD} \leq 60V$, $T_J \leq 150^\circ C$
- ④ Pulse width $\leq 300 \mu s$; Duty Cycle $\leq 2\%$
- ⑤ **Total Dose Irradiation with V_{GS} Bias.**
12 volt V_{GS} applied and $V_{DS} = 0$ during irradiation per MIL-STD-750, method 1019, condition A.
- ⑥ **Total Dose Irradiation with V_{DS} Bias.**
48 volt V_{DS} applied and $V_{GS} = 0$ during irradiation per MIL-STD-750, method 1019, condition A.

Case Outline and Dimensions — TO-254AA**NOTES:**

1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
3. CONTROLLING DIMENSION: INCH.
4. CONFORMS TO JEDEC OUTLINE TO-254AA.

PIN ASSIGNMENTS

- 1 = DRAIN
2 = SOURCE
3 = GATE

CAUTION**BERYLLIA WARNING PER MIL-PRF-19500**

Package containing beryllia shall not be ground, sandblasted, machined, or have other operations performed on them which will produce beryllia or beryllium dust. Furthermore, beryllium oxide packages shall not be placed in acids that will produce fumes containing beryllium.

International
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IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105

IR LEOMINSTER : 205 Crawford St., Leominster, Massachusetts 01453, USA Tel: (978) 534-5776

TAC Fax: (310) 252-7903

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Data and specifications subject to change without notice. 08/2007