# International Rectifier

# HYBRID - HIGH RELIABILITY RADIATION TOLERANT DC/DC CONVERTER

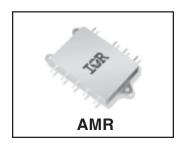
#### **Description**

The AMR28XXD series of DC/DC converter modules has been specifically designed for operation in moderate radiation environments supplementing the higher radiation performance available in the International Rectifier ART2815T converter series. Environments presented to space vehicles operating in low earth orbits, launch boosters, orbiting space stations and similar applications requiring a low power, high performance converter with moderate radiation hardness performance will be optimally served by the AMR28XXD series.

The physical configuration of the AMR28XXD series permits mounting directly to a heat conduction surface without the necessity of signal leads penetrating the heat sink surface. This package configuration permits greater independence in mounting and more mechanical security than traditional packages. International Rectifier's rugged ceramic seal pins are used exclusively in the package thereby assuring long term hermeticity.

The AMR28XXD has been designed for high density using chip and wire hybrid technology that complies with class H requirement of MIL-PRF-38534. Manufactured in a facility fully qualified to MIL-PRF-38534, these converters are fabricated utilizing DSCC qualified processes. For available screening options, refer to device screening table in the data sheet. Applicable generic lot qualification test data including radiation performance can be made available on request. Variations to the standard screening can be accommodated. Consult IR Santa Clara for special requirements.

# AMR28XXD SERIES 28V Input, Dual Output



#### **Features**

- 30 Watts Output Power
- Available in  $\pm$  5,  $\pm$ 12 and  $\pm$ 15 Volt Outputs
- 16 40 VDC Input Range (28 VDC Nominal)
- Total Ionizing Dose > 25KRads (Si)
- SEE Hardened to LET up to 60 MeV.cm<sup>2</sup>/mg
- -55°C to +125°C Operating Range
- Indefinite Short Circuit Protection
- External Synchronization
- Shutdown from External Signal
- Flexible Mounting
- Fully Isolated Input to Output and to Case
- Complimentary EMI Filter Available
- Electrical Performance Similar to ATR28XXD Series

# **Specifications**

Absolute Maximum Ratings		Recommended Operating Conditions			
Input Voltage range	ut Voltage range -0.5V to +50VDC (Continuous),		+16V to +40VDC		
	80V (100ms)	Output Power	Less than or equal to 30W		
Soldering temperature	300°C for 10 seconds	Operating case temperature	-55°C to +125°C		
Storage case temperature	-65°C to +135°C				

# $\textbf{Static Characteristics} \quad \text{-55}^{\circ}\text{C} \leq \text{T}_{CASE} \leq \text{+125}^{\circ}\text{C}, \ \text{V}_{IN} = 28 \ \text{V}_{DC} \pm 5\%, \ \text{C}_{L} = 0, \ \text{unless otherwise specified}.$

Parameter		Group A Subgroups	Test Conditions	Min	Nom	Max	Unit
Input Voltage				16	28	40	V
	AMR2805D AMR2812D AMR2815D AMR2805D AMR2812D AMR2815D	1 1 1 2, 3 2, 3 2, 3	lout=0	±4.95 ±11.88 ±14.85 ±4.90 ±11.70 ±14.70	±5.00 ±12.00 ±15.00	±5.05 ±12.12 ±15.15 ±5.10 ±12.30 ±15.30	V
	AMR2805D AMR2812D AMR2815D	1, 2, 3 1, 2, 3 1, 2, 3	Vin = 16, 28, 40 Volts	600 250 200		5400 2250 1800	mA
	AMR2805D AMR2812D AMR2815D	1, 2, 3 1, 2, 3 1, 2, 3	100% load			30 30 30	W
	3 AMR2805D AMR2812D AMR2815D	1, 2, 3 1, 2, 3 1, 2, 3	Vin = 16, 28, 40 Volts BW = 20 Hz to 2 MHz			60 85 85	$mV_{PP}$
	AMR2805D AMR2812D AMR2815D	1, 2, 3 1, 2, 3 1, 2, 3	V <sub>in</sub> = 16, 28, 40 Volts lout = 0, 50%, and 100% load		±10 ±30 ±40	±30 ±75 ±75	mV
	AMR2805D AMR2812D AMR2815D	1, 2, 3 1, 2, 3 1, 2, 3			±10 ±50 ±50	±50 ±120 ±150	

For Notes to Specifications, refer to page 5

# **AMR28XXD Series**

 $\textbf{Static Characteristics} \text{ (Continued) } -55^{\circ}\text{C} \leq \text{T}_{CASE} \leq +125^{\circ}\text{C}, \text{ V}_{IN} = 28 \text{ V}_{DC} \pm 5\%, \text{ C}_{L} = 0, \text{unless} \text{ otherwise pecified.}$ 

Paramet	ter	Group A Subgroups	Test Conditions	Min	Nom	Max	Unit
Cross Regulation <sup>5</sup>	AMR2805D AMR2812D AMR2815D	1, 2, 3 1, 2, 3 1, 2, 3	10% TO 90% Load change			10 5.0 5.0	%
Input Current No Load Inhibit	AMR2805D AMR2812D AMR2815D AMR2805D AMR2812D AMR2815D	1, 2, 3 1, 2, 3 1, 2, 3 1, 2, 3 1, 2, 3 1, 2, 3	lout=0, Inhibit =open Inhibit shorted to input return		20 20 20 20 8.0 8.0 8.0	70 75 100 15 18 18	mA
Input Ripple Current <sup>3, 4</sup>		1, 2, 3	Vin = 16, 28, 40 Volts, 100% load, BW = 20 Hz to 2 MHz			50	mA <sub>PP</sub>
Efficiency <sup>4</sup>	AMR2805D AMR2812D AMR2815D AMR2805D AMR2812D AMR2815D	1 1 1 2, 3 2, 3 2, 3	100% load	76 80 79 72 75 74			%
Isolation		1	Input to output or any pin to case (except case ground pin) at 500Vdc	100			ΜΩ
Capacitive Load <sup>6, 7</sup>		4	No effect on dc performance			200	μF
Short Circuit Power Dissipation		1, 2, 3				9.0	W
Switching Frequency <sup>4</sup>		4, 5, 6	100% load	500	550	600	KHz
Sync frequency range		4, 5, 6	100% load	500		700	KHz
мтвғ			MIL-HDBK-217F, SF@ Tc=35°C	800			Khrs
Weight						68	g

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 $\textbf{Dynamic Characteristics} \ \ \text{-55°C} \leq \text{T}_{CASE} \leq \text{+125°C}, \ \text{V}_{IN} = 28 \ \text{V}_{DC} \pm 5\%, \ \text{C}_{L} = 0, \ \text{unless otherwise specified}.$ 

Parameter		Group A Subgroups	Test Conditions	Min	Nom	Max	Unit
Output Response To Step							
			Load step 50% ⇔ 100%				
	AMR2805D	4, 5, 6		-400		+400	
	MR2812D	4, 5, 6		-400		+400	
P	AMR2815D	4, 5, 6		-400		+400	mV pk
	AMR2805D	4, 5, 6	Load step 0% ⇔ 50%	-400		+400	
	MR2812D	4, 5, 6		-800		+800	
	MR2815D	4, 5, 6		-800		+800	
Recovery Time, Step Trail Load Changes 4, 9, 10	nsient						
	AMR2805D	4, 5, 6	Load step 50% ⇔ 100%			70	
	MR2812D	4, 5, 6 4, 5, 6				70	
						70 70	μS
<i>,</i>	AMR2815D	4, 5, 6				/0	
	MR2805D	4, 5, 6	Load step 0% => 50%			500	
	MR2812D	4, 5, 6 4, 5, 6	-			500	μS
	MR2815D					500	•
<i>'</i>	VINIUSQ 19D	4, 5, 6	Load step 50% => 0%				
Δ	AMR2805D	4, 5, 6	·			5	
	MR2812D	4, 5, 6				5	ms
	AMR2815D	4, 5, 6				5	
	AIVII 120 13D	4, 3, 0					
Output Response Transient Step Line Changes 4, 7, 11							
			Input step from/to 16 to 40Vdc,				
	AMR2805D	4, 5, 6	100% load	-500		+500	
	AMR2812D	4, 5, 6		-1200		+1200	mV pk
A	AMR2815D	4, 5, 6		-1500		+1500	·
Recovery Time Transient Changes <sup>4, 7, 10, 11</sup>	Step Line		Input step from/to 16 to 40Vdc,				
	AMR2805D	4, 5, 6	100% load			10	
	AMR2812D	4, 5, 6	100701000			10	ma
	AMR2815D	4, 5, 6				10	ms
Turn On Overshoot <sup>4</sup>							
	AMR2805D	4, 5, 6	0% load to 100% load			450	
	AMR2812D	4, 5, 6				600	mV pk
	MR2815D	4, 5, 6				750	···· p··
Turn On Delay 4, 12							
, Sii 20.0y	AMR2805D	4, 5, 6	0% load to 100% load			25	
	MR2812D	4, 5, 6				25	ms
	AMR2815D	4, 5, 6				25	1110
Short Circuit Recovery 7							
	AMR2805D	4, 5, 6				25	
	AMR2812D	4, 5, 6				25	
	AMR2815D	4, 5, 6				25	ms
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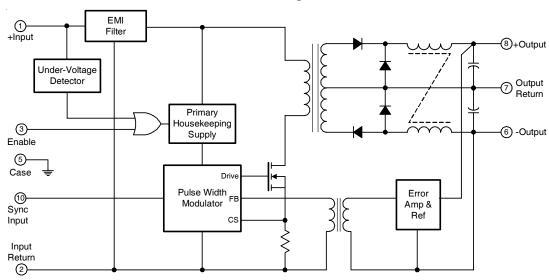
For Notes to Specifications, refer to page 5

#### **AMR28XXD Series**

#### Notes to Specifications

- 1. Parameter guaranteed by line and load regulation tests.
- 2. Up to 90 percent of full power is available from either output provided the total output does not exceed 30 watts.
- 3. Bandwidth guaranteed by design, Tested for 20 Hz to 2 MHz.
- 4. Load current split equally between +V<sub>OUT</sub> and -V<sub>OUT</sub>.
- 5. 3 watt load on output under test. 3 watt to 27 watt load change on other output.
- 6. Capacitive load may be any value from 0 to the maximum limit without compromising DC performance. A capacitive load in excess of the maximum limit will not disturb loop stability but may interfere with the operation of the load fault detection circuitry, appearing as a short circuit during turn-on.
- 7. Parameter shall be tested as part of design characterization and after design or process changes, Parameters shall be guaranteed to the limit specified in Electrical Specifications.
- 8. Load step transition time between 2 and 10 microseconds.
- 9. Recovery time is measured from initiation of the transient to where V<sub>out</sub> has returned to within ±1% of Vout at 50% load
- 10. Input step transient time between 2 and 10 microseconds.
- 11. Turn-on delay time measurement is for either a step application of power at the input or the removal of a ground signal from the inhibit pin while power is applied to the input.

#### **Block Diagram**



#### **Application Information**

#### Inhibit Function (Enable)

Connecting the inhibit input to input common will cause the converter to shut down. It is recommended that the inhibit pin be driven by an open collector device capable of sinking at least 400  $\mu$ A of current. The open circuit voltage of the inhibit input is 10 ±1.0 Vpc.

#### **EMI Filter**

An optional EMI filter is available (AFH461) that will reduce the input ripple current to levels below the limits imposed by MIL-STD-461 CE03.

#### **Device Synchronization**

When multiple DC/DC converters are utilized in a single system, significant low frequency noise may be generated due to a small difference in the switching frequency of the converters (beat frequency noise). Because of the low frequency nature of this noise (typically less than 10 KHz), it is difficult to filter out and may interfere with proper operation of sensitive systems (communication, radar or telemetry). International Rectifier provides synchronization of multiple AMR type converters to match switching frequency of the converter to the frequency of the system clock, thus eliminating this type of noise.

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# **AMR28XXD Series**

#### **Mechanical Outline** Ø 0.125 3 Places -R 0.15 Typ Ŧ 0.040 D x 0.26 L Pins 0 1.600 2.440 2.14 0 1.200 0.800 0 0.400 0 0.420 Φ, Φ, 0.150 Typ Tolerance unless otherwise specified: x.xx ±0.010 x.xxx ±0.005

**Pin Designation** 

: ::: = cc.gmanon					
Pin#	Designation				
1	+ Input				
2	Input Return				
3	Enable				
4	Blank				
5	Case Ground				
6	- Output				
7	Output Return				
8	+ Output				
9	NC				
10	Sync Input				

# **Radiation Specification**

Parameter	Condition	Min	Тур	Max	Unit
Total lonizing Dose	MIL-STD-883, Method 1019.4 Operating bias applied during exposure	25	-	-	KRads (Si)
Heavy Ion (Single event effects)	BNL Dual Van de Graf Generator	60	-	-	MeV•cm² /mg

International Rectifier currently does not have a DSCC certified Radiation Hardness Assurance Program.

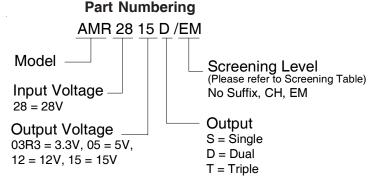
#### **Device Screening**

Requirement	MIL-STD-883 Method	No Suffix ②	CH ②	ЕМ
Temperature Range	_	-55°C to +125°C	-55°C to +125°C	-55°C to +85°C
Element Evaluation	MIL-PRF-38534	Class H	Class H	N/A
Non-Destructive Bond Pull	2023	N/A	N/A	N/A
Internal Visual	2017	Yes	Yes	0
Temperature Cycle	1010	Cond C	Cond C	Cond C
Constant Acceleration	2001, Y1 Axis	3000 Gs	3000 Gs	3000 Gs
PIND	2020	Cond A	Cond A	N/A
Burn-In	1015	320 hrs @ 125°C	320 hrs @ 125°C	48 hrs @ 125°C
Dum-in	1015	(2 x 160hrs)	( 2 x 160hrs )	
Final Electrical	MIL-PRF-38534	-55°C, +25°C,	-55°C, +25°C,	-55°C, +25°C,
( Group A )	& Specification	+125°C	+125°C	+85°C
PDA	MIL-PRF-38534	2%	2%	N/A
Seal, Fine and Gross	1014	Cond A, C	Cond A, C	Cond A
Radiographic	2012	Yes	Yes	N/A
External Visual	2009	Yes	Yes	0

#### Notes:

- ① Best commercial practice.
- ② Device with '/CH' suffix is a DSCC class H compliant without radiation performance. No suffix is a radiation rated device but not available as a DSCC qualified SMD per MIL-PRF-38534.

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Data and specifications subject to change without notice. 09/2008