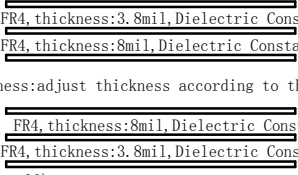


Content Indexing

- 01. Index
- 02. Block Diagram
- 03. Power Tree
- 04. System Power
- 05. RK3288 USB/HSIC Controller
- 06. USB HOST Port
- 07. RK3288 RAM Controller
- 08. RAM-DDR3-4X16bit
- 09. Nand FLASH/eMMC/TF Card
- 10. RK3288 GPIO/POWER
- 11. HDMI OUT
- 12. RK3288 LCD/MI2S Controller
- 13. ES8323
- 14. S/PDIF OUT
- 15. RK3288 Ethernet MAC Controller
- 16. 10/100/1000M-PHY
- 17. RK3288 SDIO0 Controller
- 18. AP6XXX-WIFI/BT
- 19. VGA
- 20. Camera Interface
- 21. DTV-(Reserve)

6 LAYERS PCB STACK

TOP (Signal1)  Cu, thickness: 0.7mil, Plating to 1oz

GND1 FR4, thickness: 3.8mil, Dielectric Constant: 4.3 Cu, thickness: 1.5mil, 1oz

POWER FR4, thickness: 8mil, Dielectric Constant: 4.3 Cu, thickness: 1.5mil, 1oz

FR4, thickness: adjust thickness according to the thickness of board , Dielectric Constant: 4.3

Signal2 Cu, thickness: 1.5mil, 1oz

GND2 FR4, thickness: 8mil, Dielectric Constant: 4.3 Cu, thickness: 1.5mil, 1oz

FR4, thickness: 3.8mil, Dielectric Constant: 4.3

BOTTOM (Signal3) Cu, thickness: 0.7mil, Plating to 1oz

Firefly

Title: Index

File:

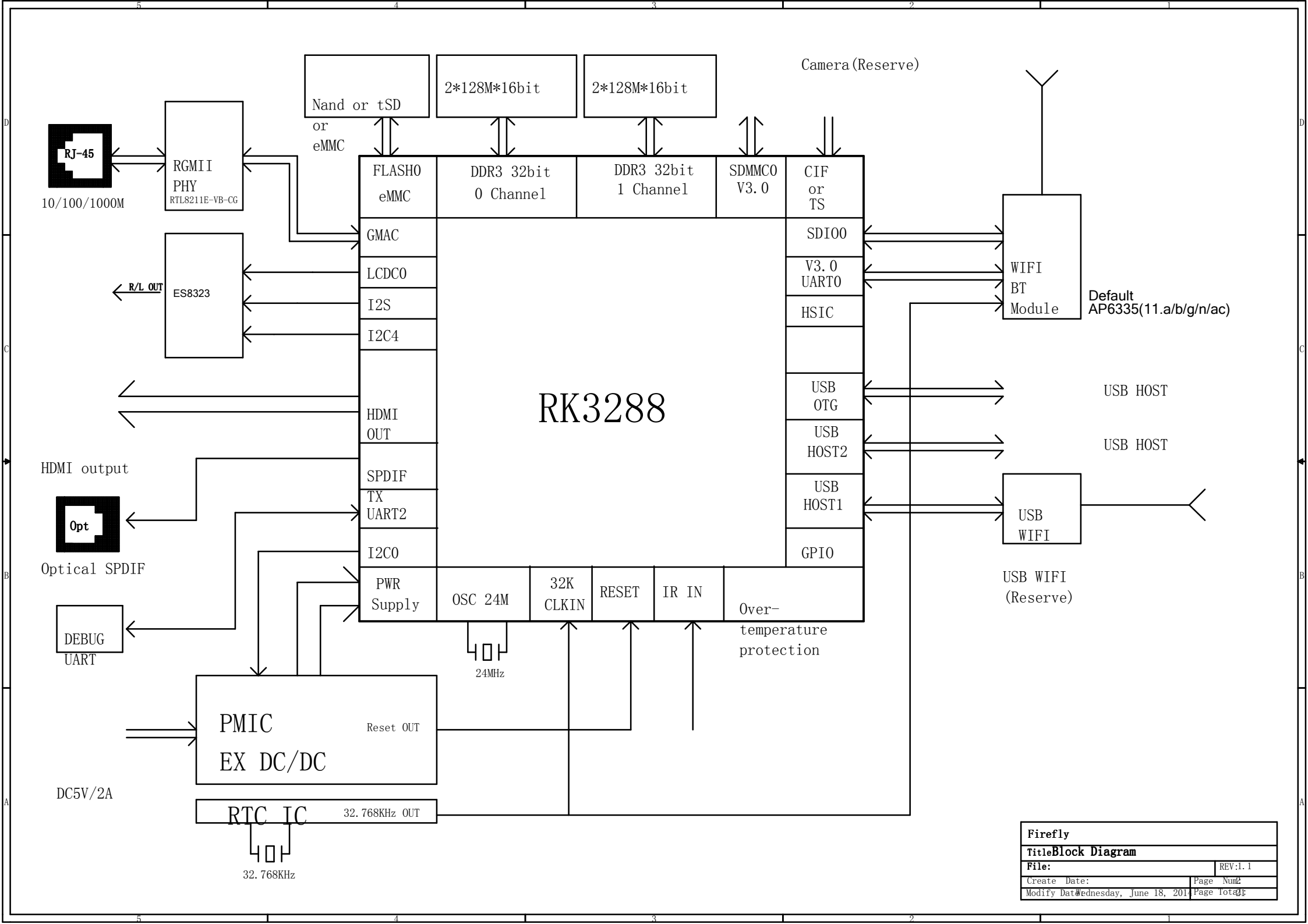
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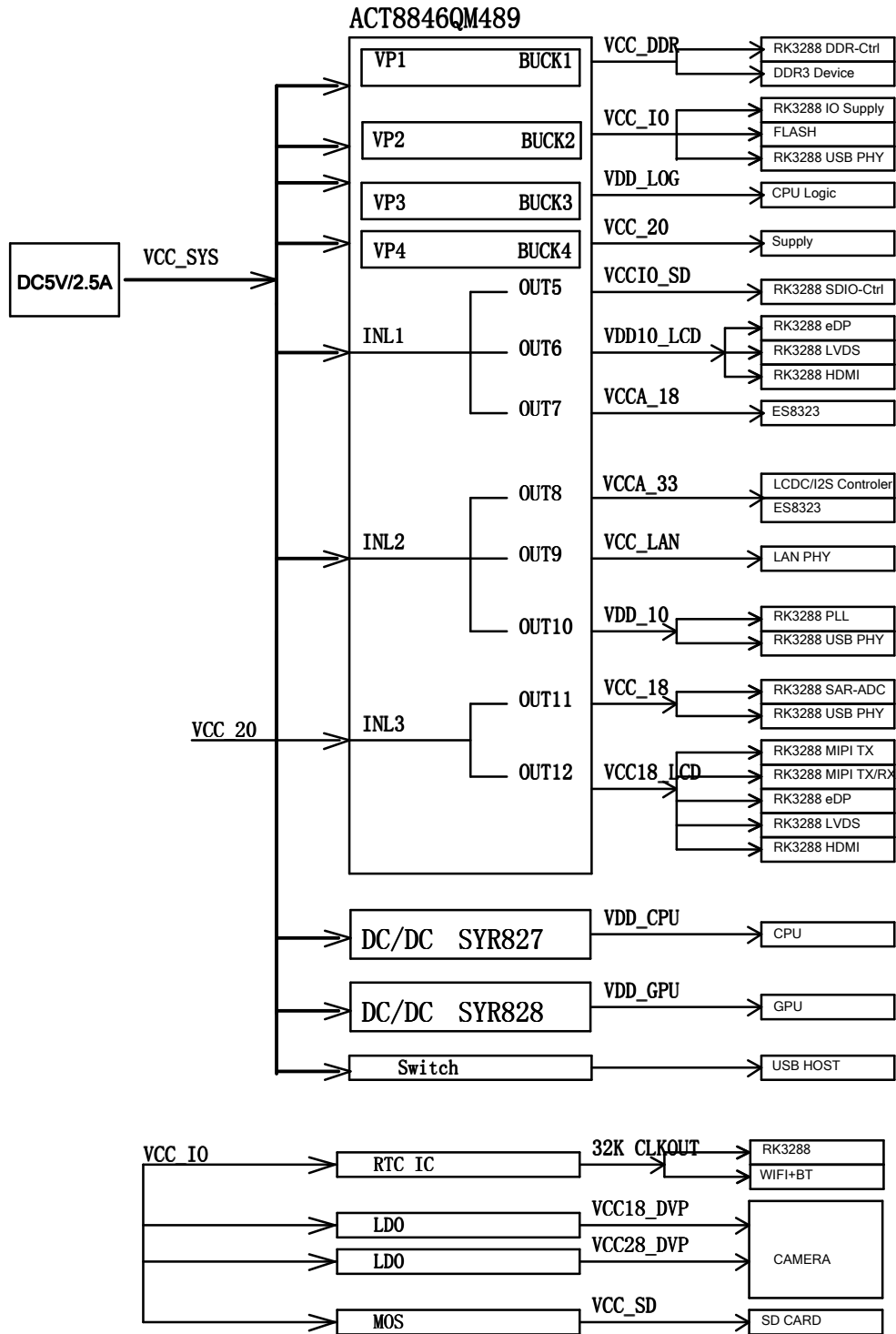
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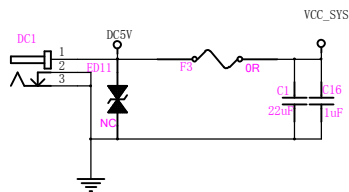
Modify Date: Wednesday, June 18, 2014

Page Total: 21

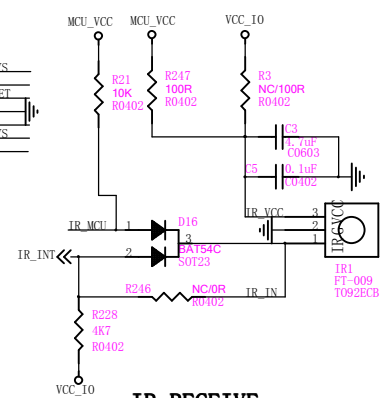
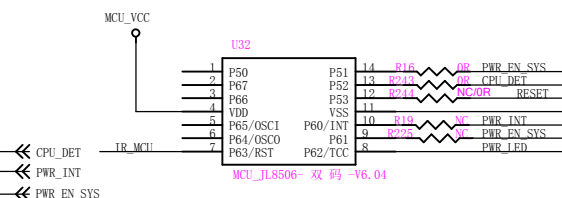
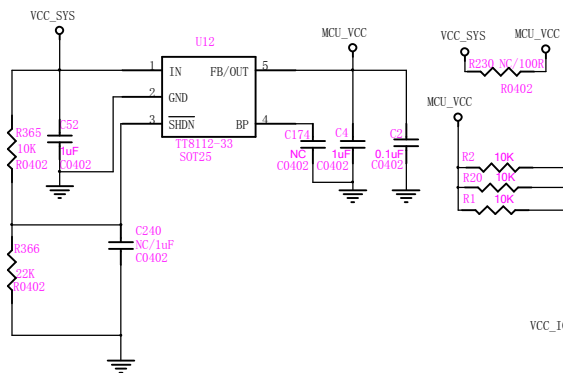




PowerName	PMIC Channel	timer(2mS)	Default	Working
VCC_20	DCDC4	Solt:0	voltage 2.0V	voltage 2.0V
VDD_10	OUT10	Solt:1	1.0V	1.0V
VCC_DDR	DCDC1	Solt:2	1.5V	1.5V (DDR3)
VDD_CPU	EX DCDC	Solt:2A	(FB=1.2V) 1.0V	DVFS
VDD_GPU	EX DCDC	Solt:2B	1.0V	DVFS
VDD_LOG	DCDC3	Solt:3	1.1V	DVFS
VCC_18	OUT11	Solt:4	1.8V	1.8V
VCC_LAN	OUT9	Solt:5	3.3V	3.3V
VCC_IO	DCDC2	Solt:6	3.3V	3.3V
VCCIO_SD	OUT5	Solt:6	3.3V	SD2.0:3.3V
VCCA_33	OUT8	OFF	OFF	SD3.0:1.8V 3.3V
VCCA_18	OUT7	OFF	OFF	1.8V
VDD10_LCD	OUT6	OFF	OFF	1.0V
VCC18_LCD	OUT12	OFF	OFF	1.8V



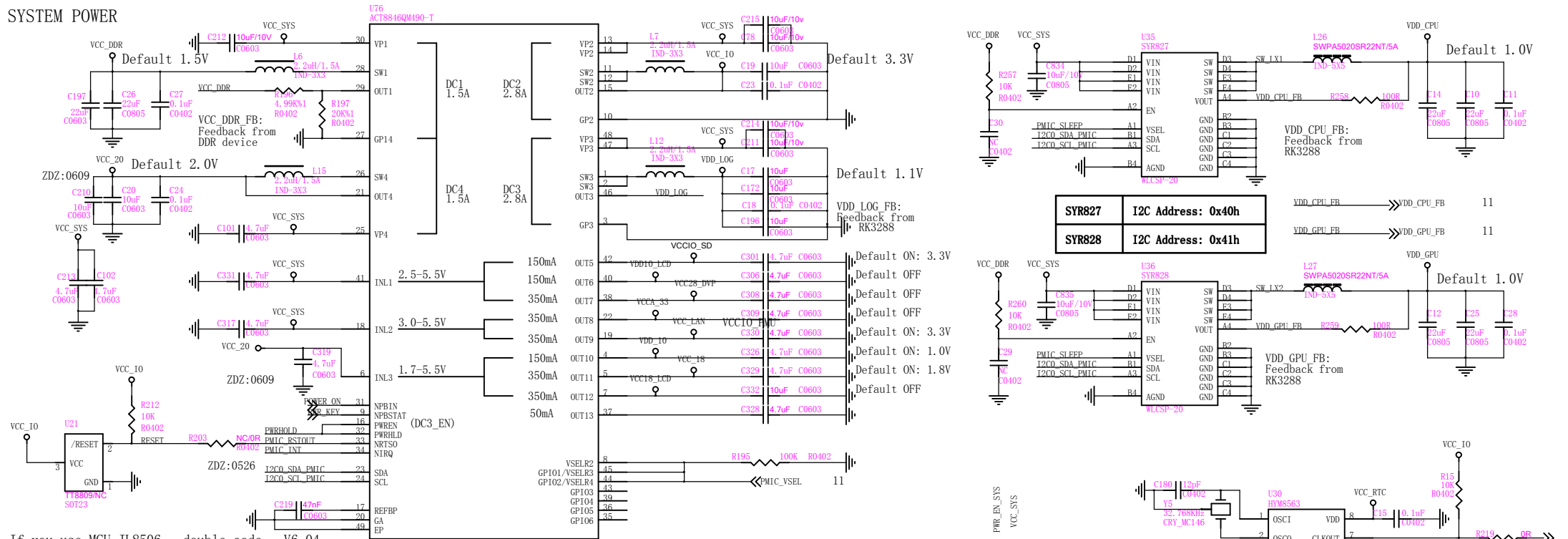
5V/2A



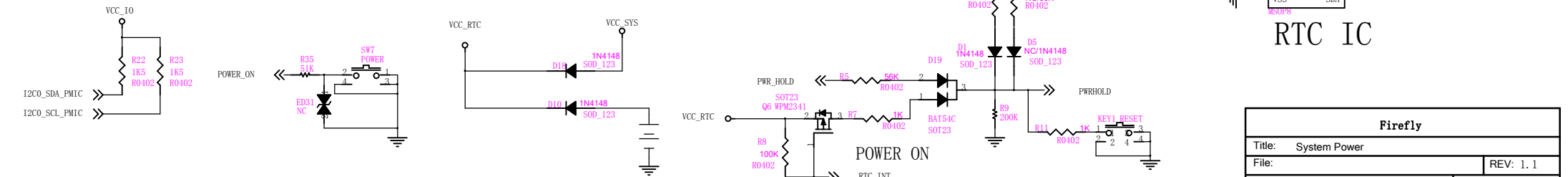
Working LED

IR RECEIVE

SYSTEM POWER

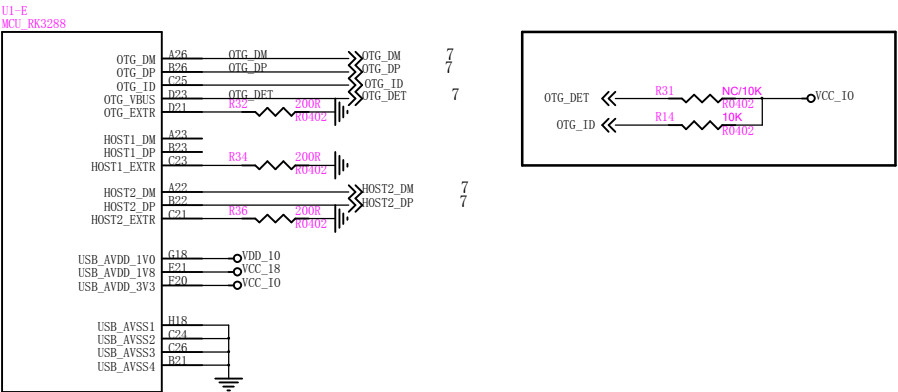


If you use MCU_JL8506 - double code - V6.04
Need to use external reset IC.
R203 don't stick.

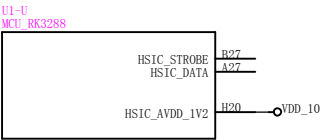
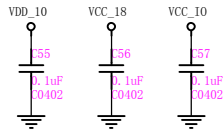


RTC IC

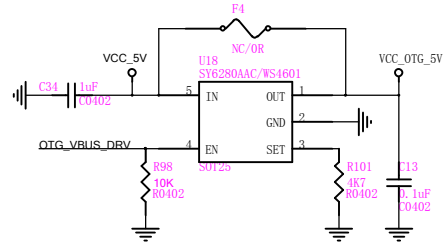
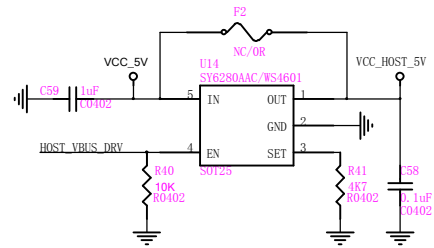
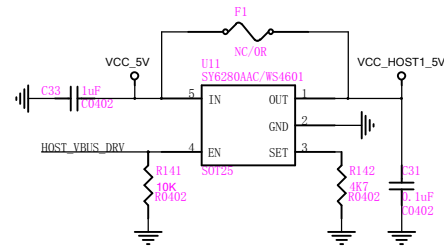
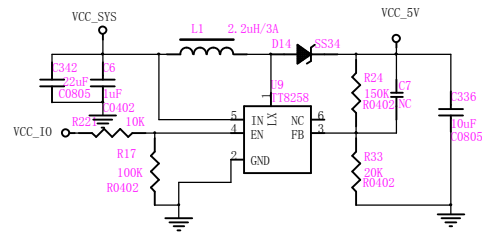
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File:	Page Num: 4
Create Date:	Page total: 21
Modify Date: Wednesday, June 18, 2014	



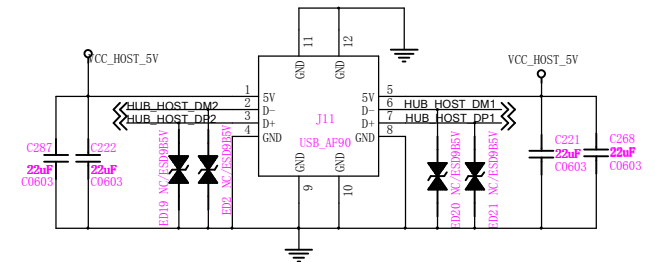
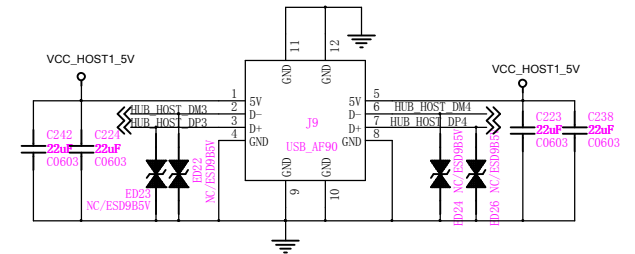
RK3288_E



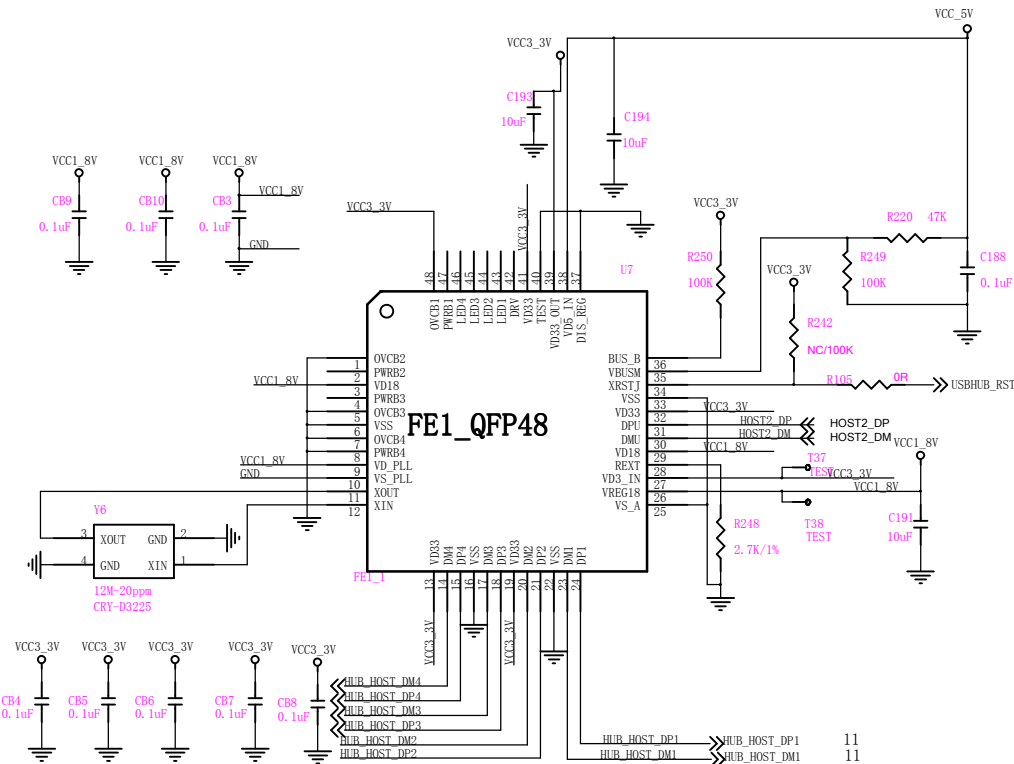
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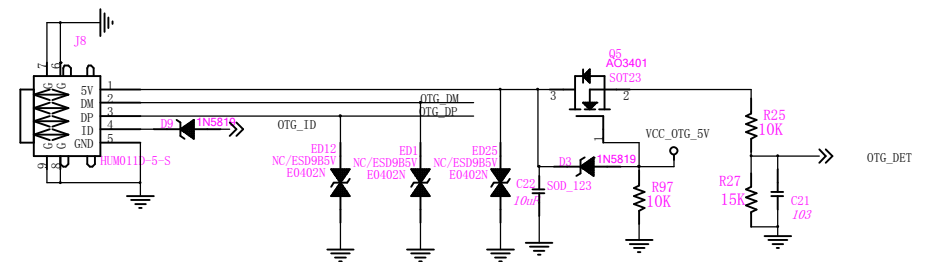
USB POWER



HOST

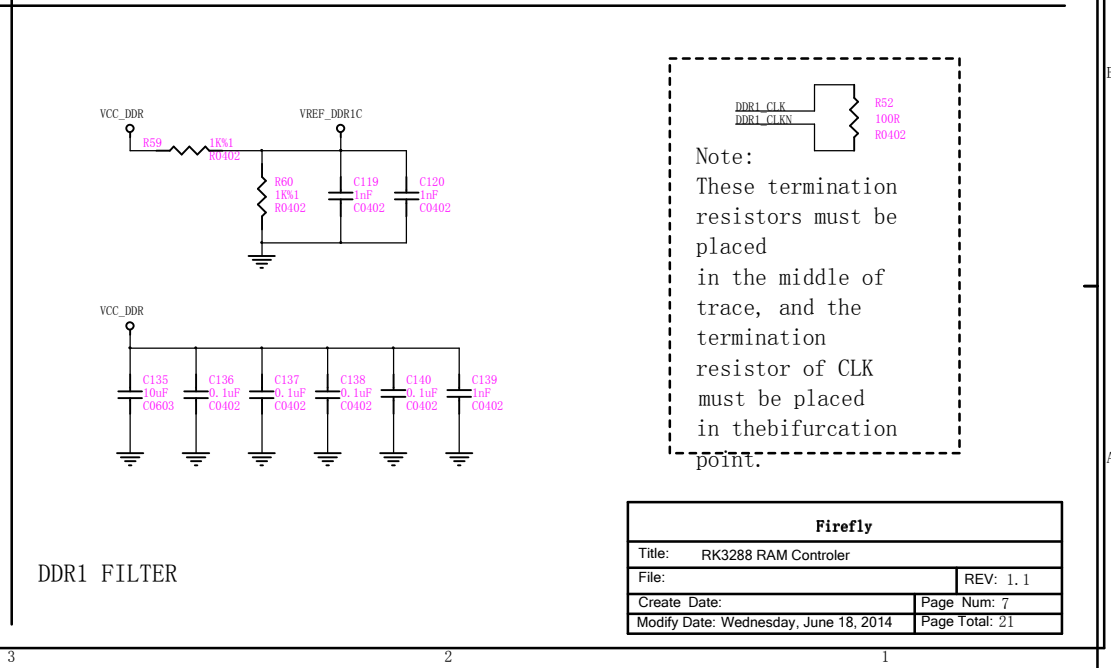
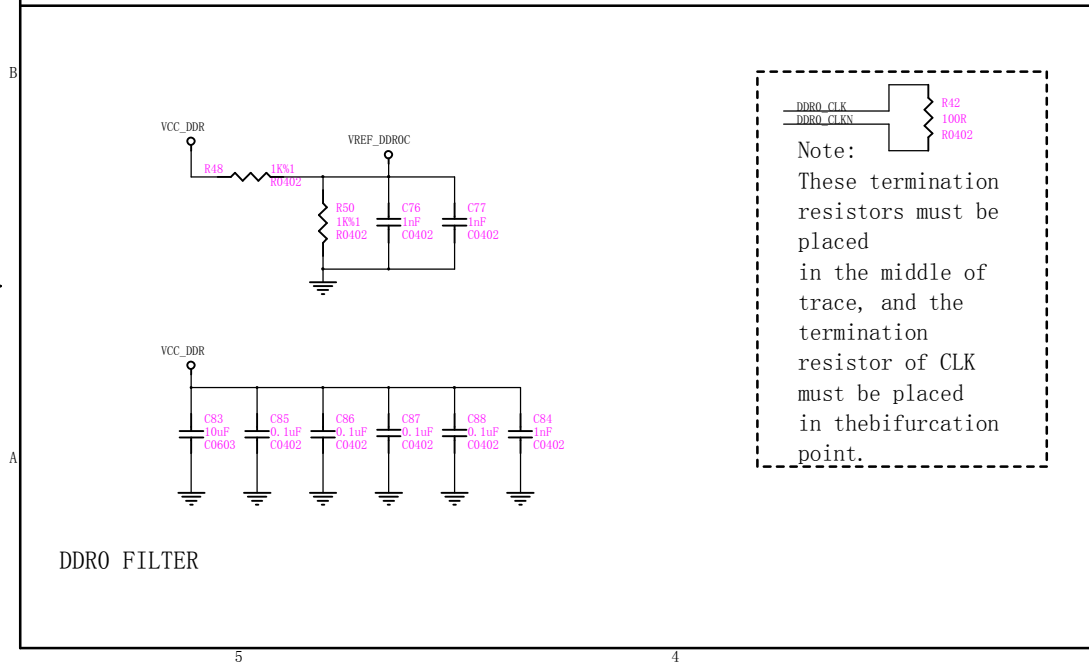
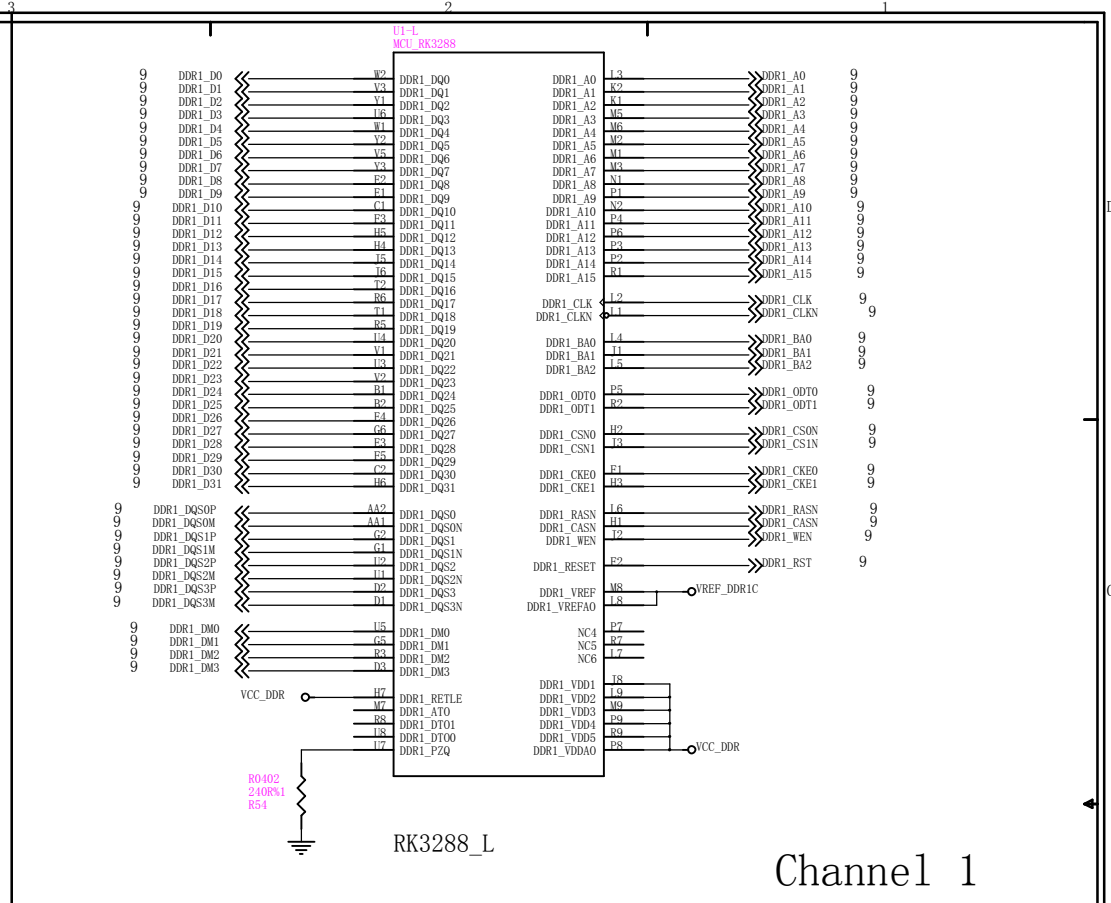
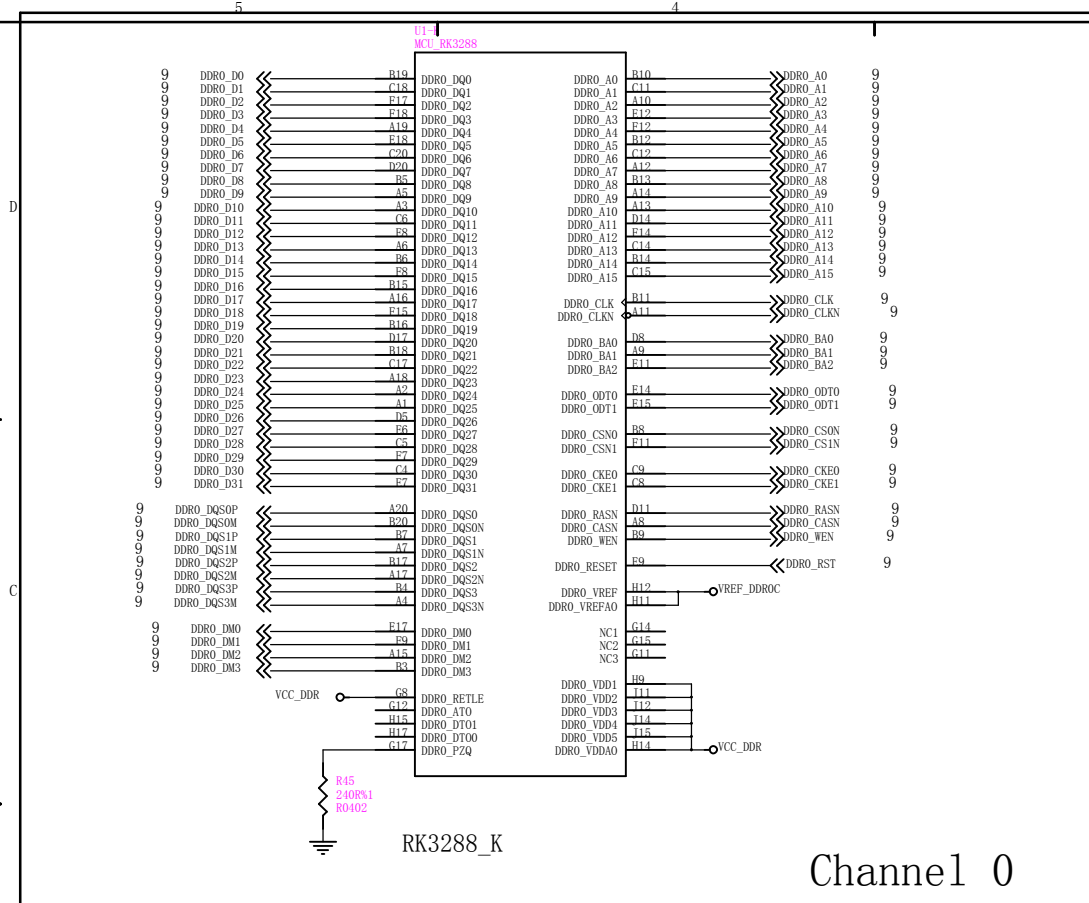


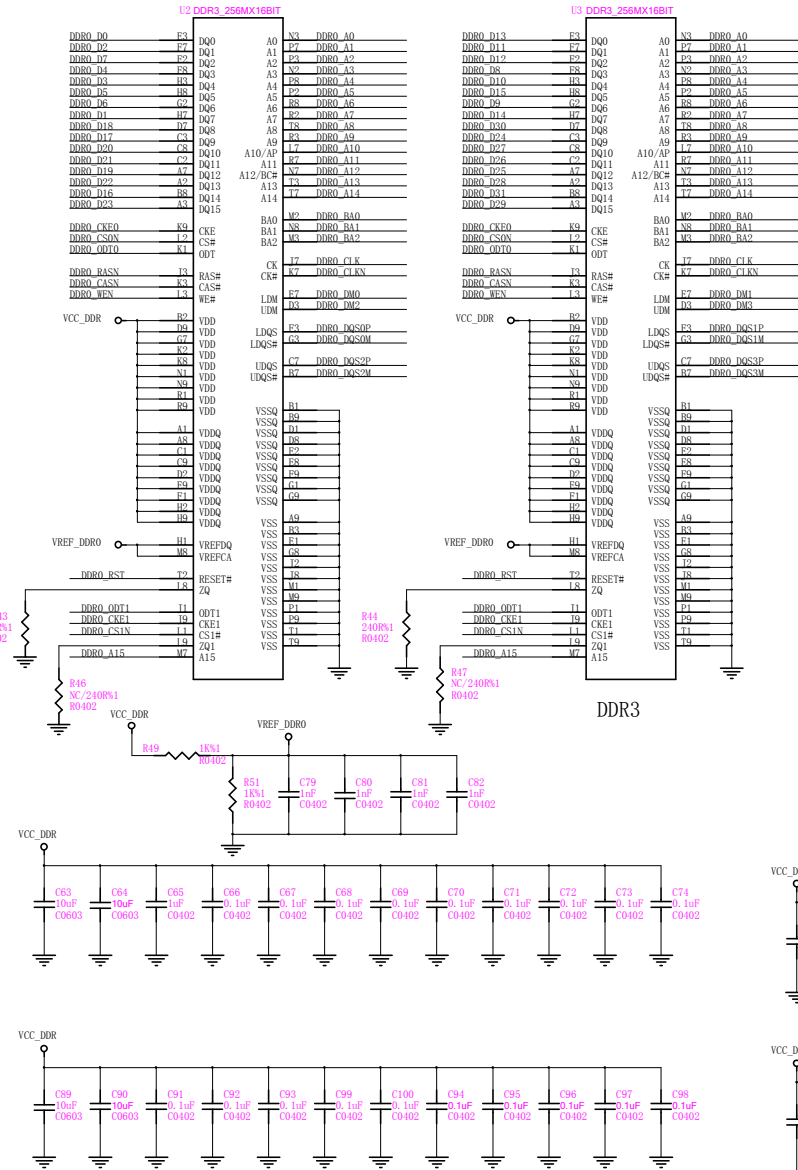
HUB CHIP

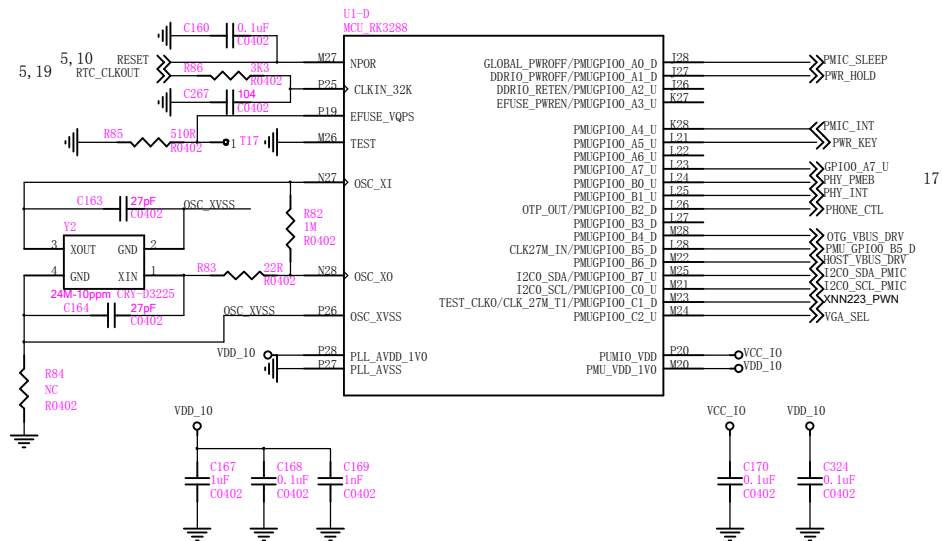


OTG

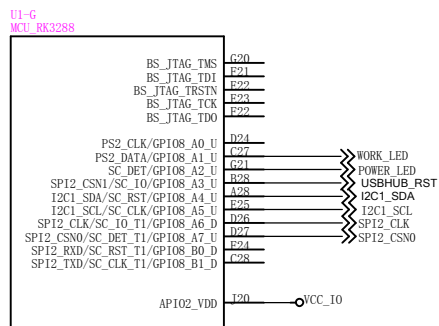
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File:	REV: 1.1
Create Date:	Page Num: 6
Modify Date: Wednesday, June 18, 2014	Page Total: 21



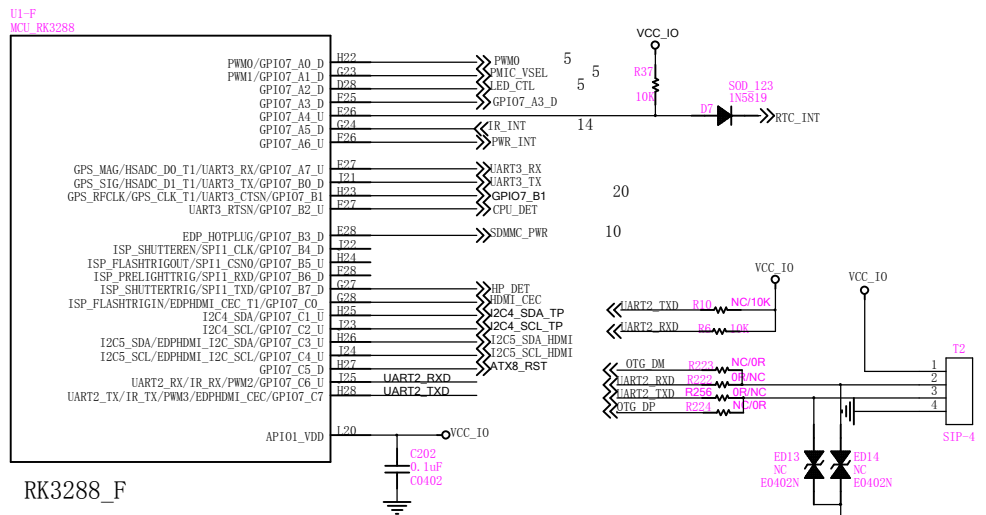




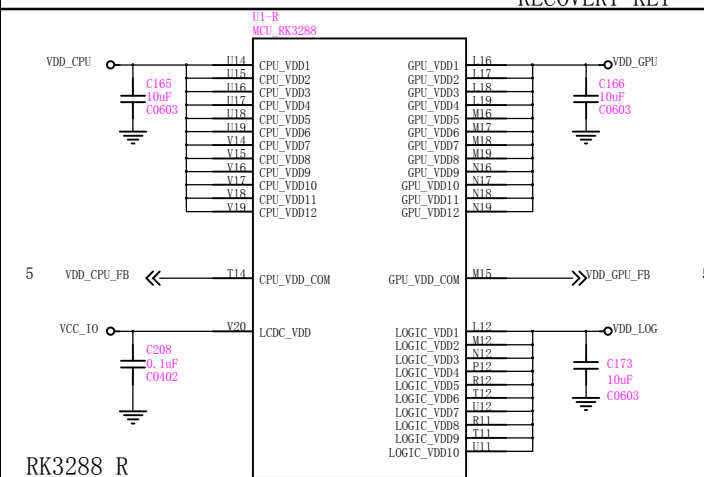
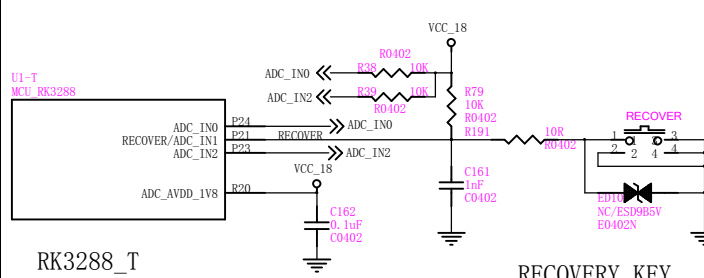
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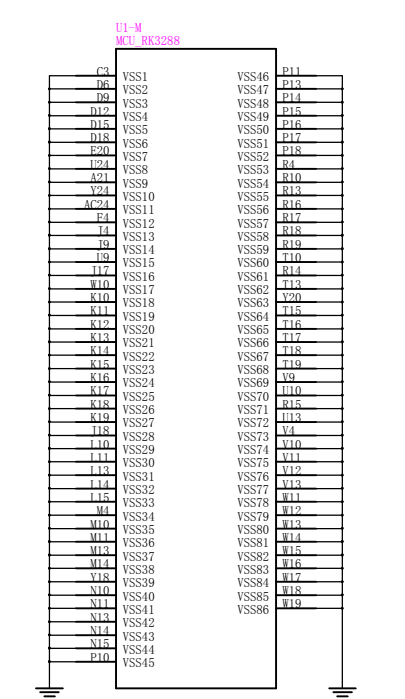
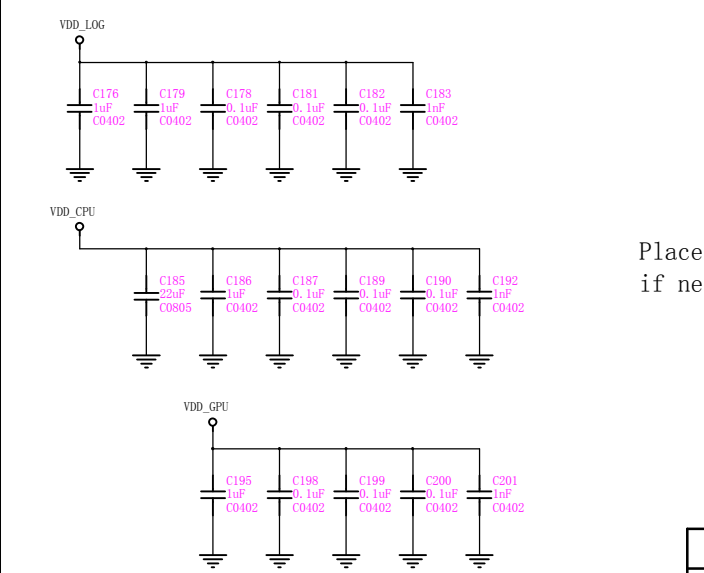
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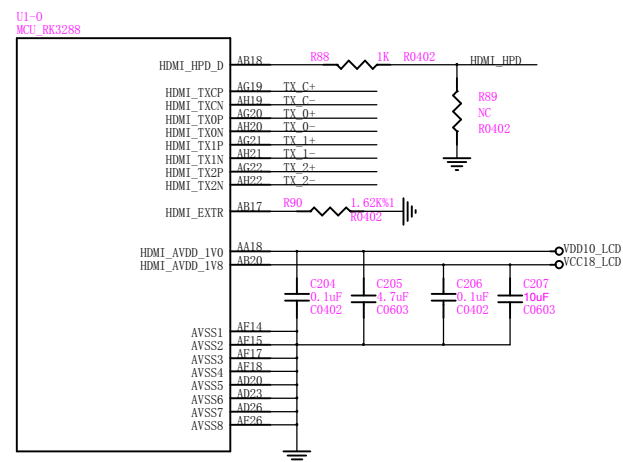
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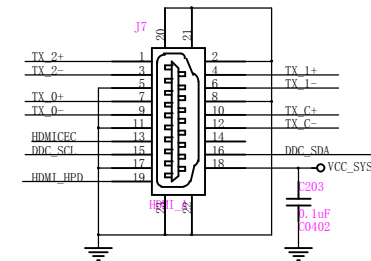
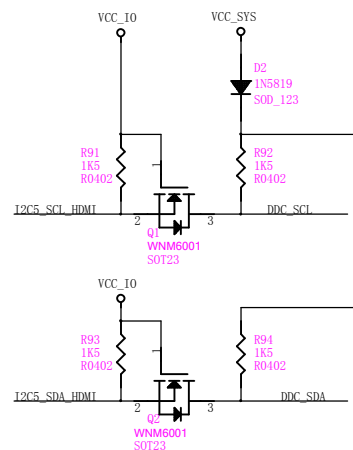
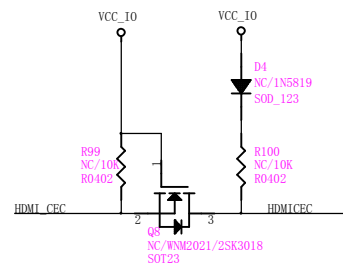
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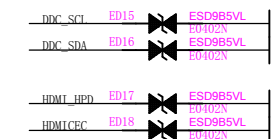
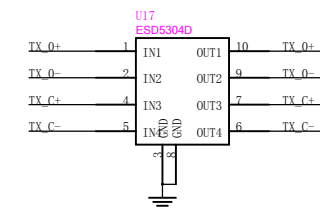
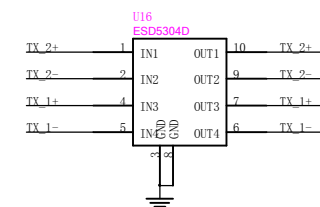
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File:		REV: 1.1
Create Date:		Page Num: 10
Modify Date: Wednesday, June 18, 2014		Page Total: 21



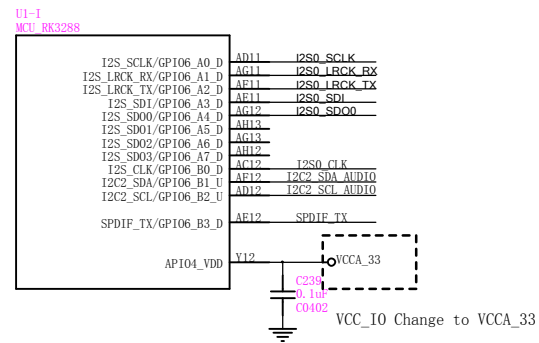
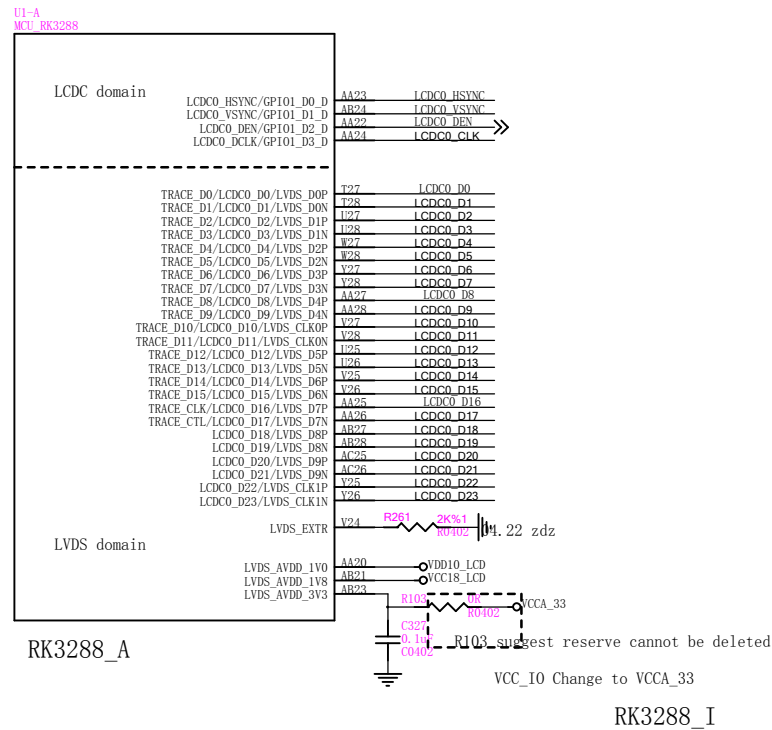
RK3288_0

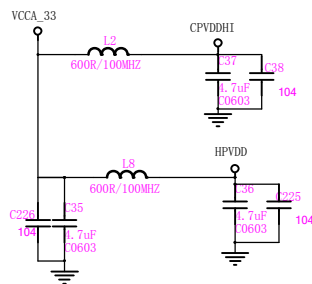
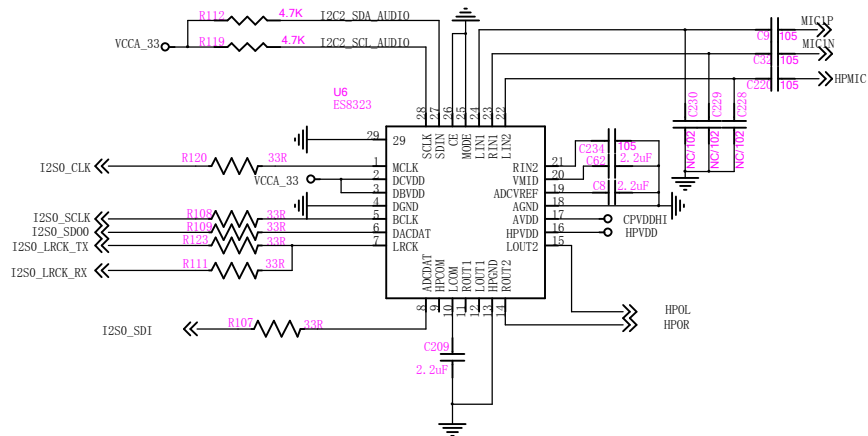


HDMI OUT

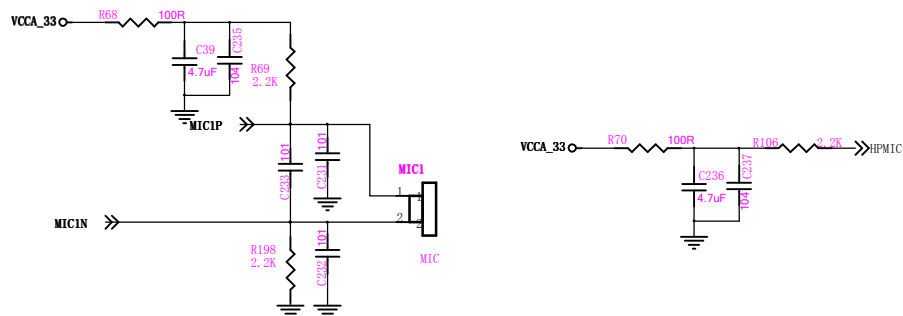


Firefly		
Title: HDMI OUT		
REV: 1.1	File:	
Page Num:11	Create Date:	
Page Total:21	Modify Date: Wednesday, June 18, 2014	

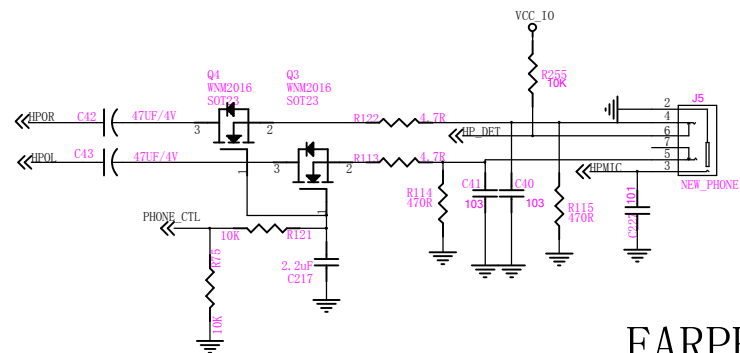




ES8323

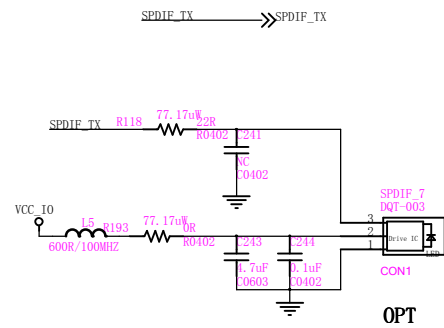


MIC

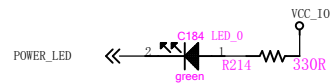
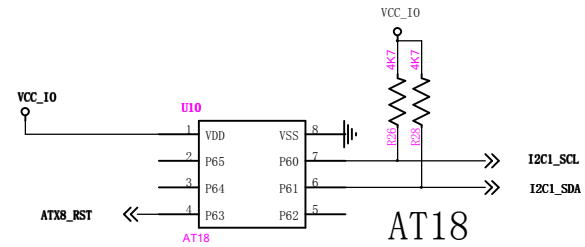


EARPHONE

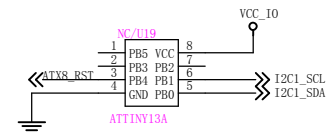
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Create Date:	Page Num:13
Modify Date:Wednesday, June 18, 2014	Page Total:21



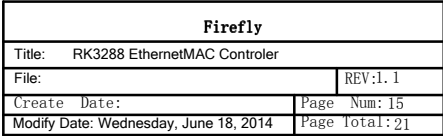
Optical S/PDIF OUT

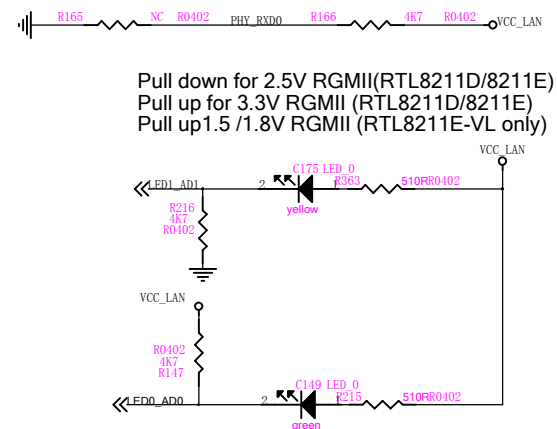
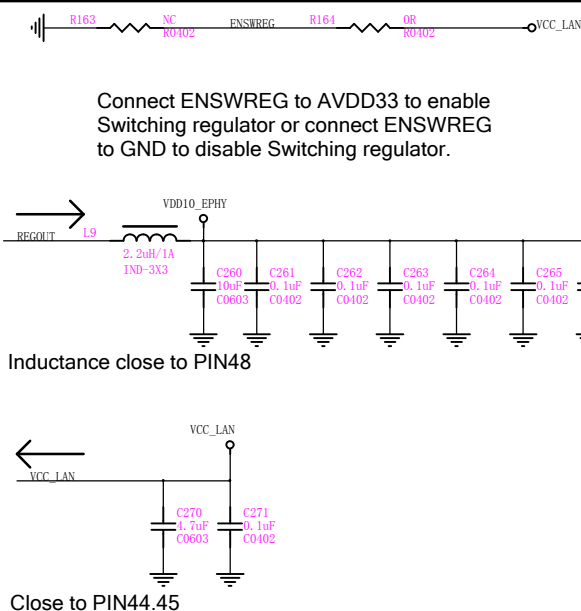
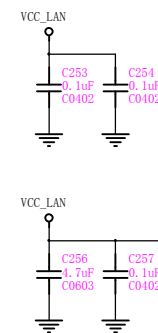
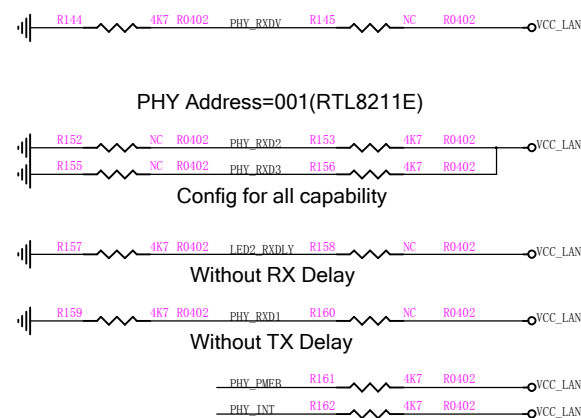
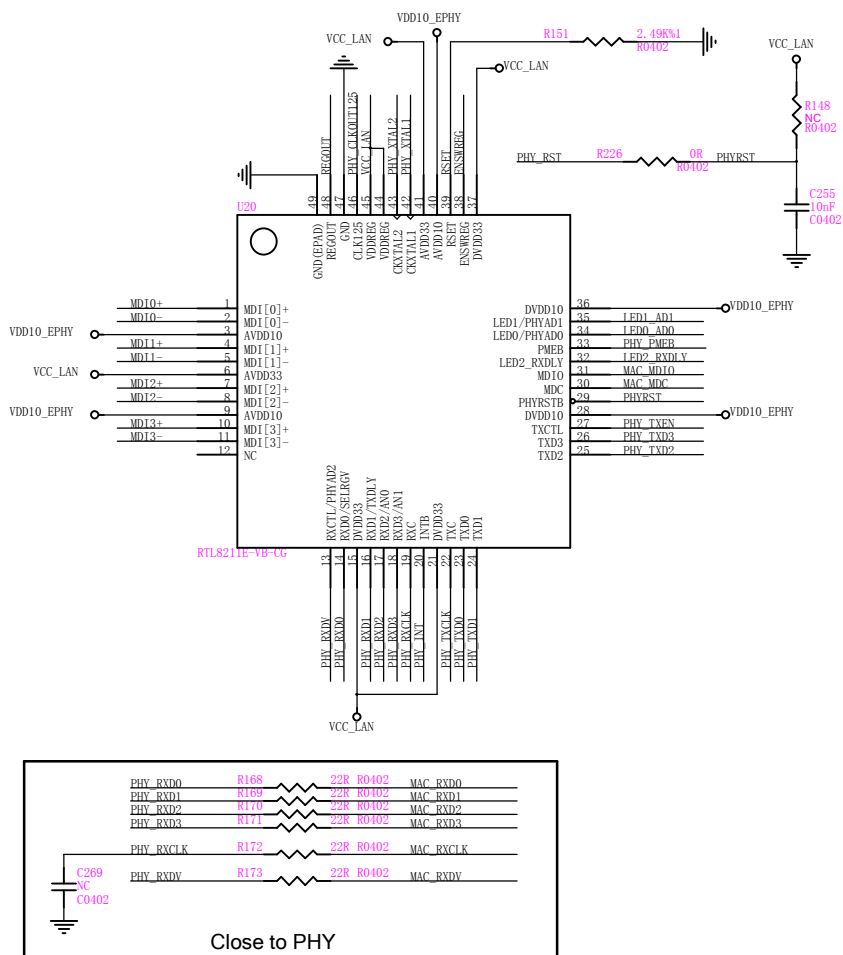
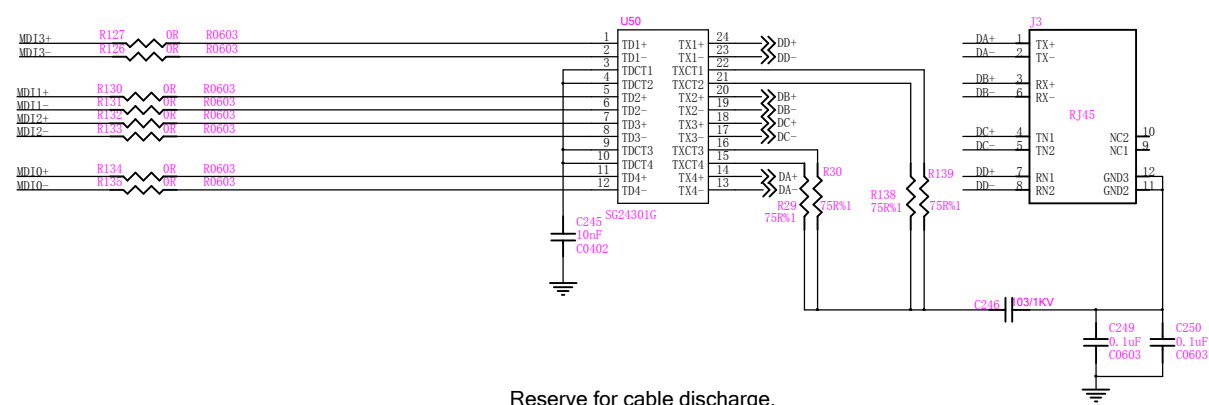
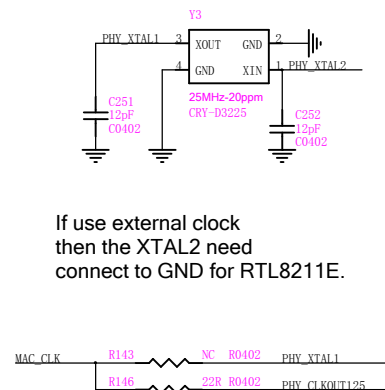


WORKING_LED

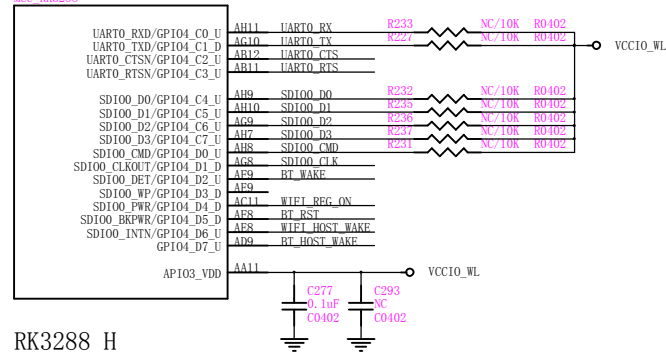


ATTINY13A

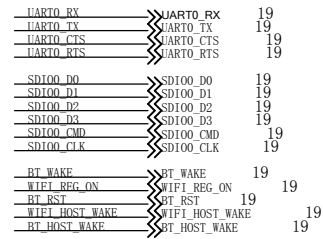


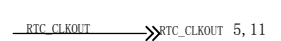


U1-H
MCU_RK3288

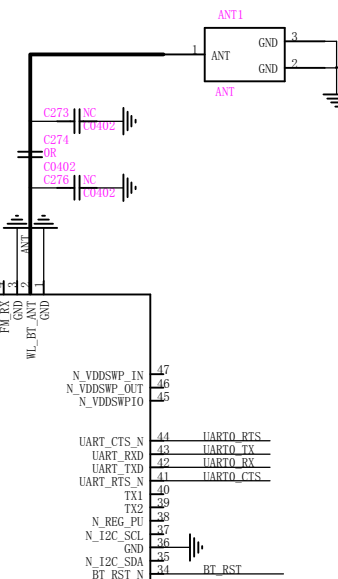
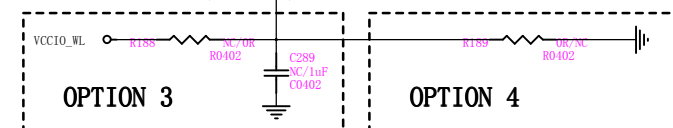
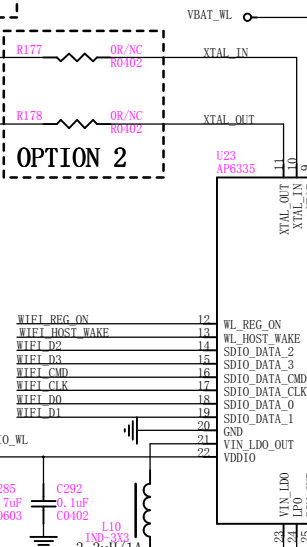


RK3288_H





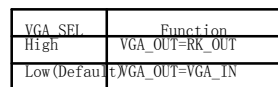
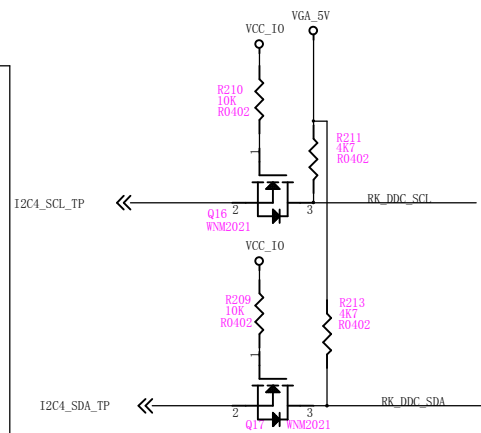
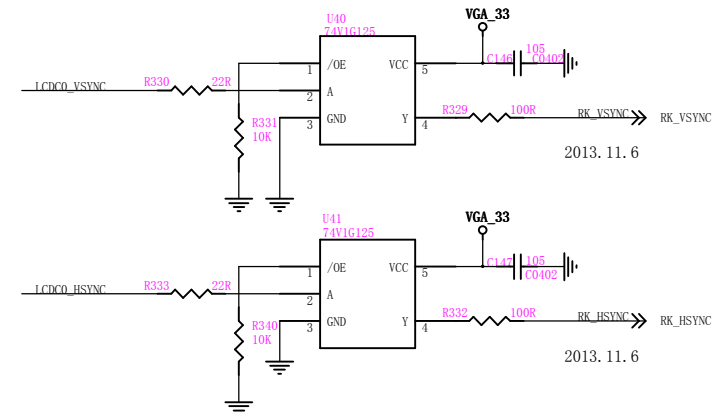
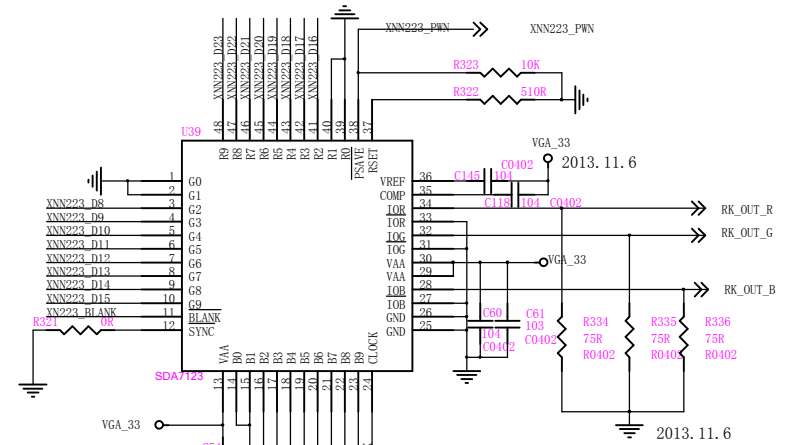
Note:
Adjusted the load capacitance
according to the crystal specification.

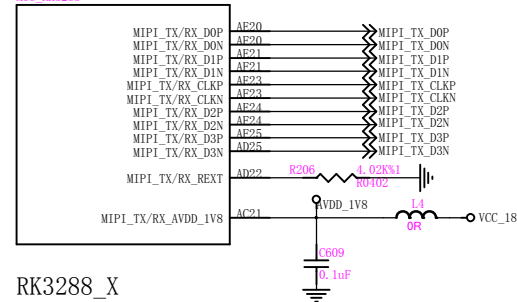
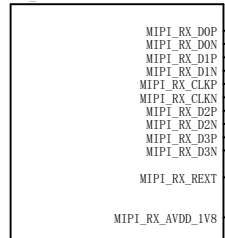
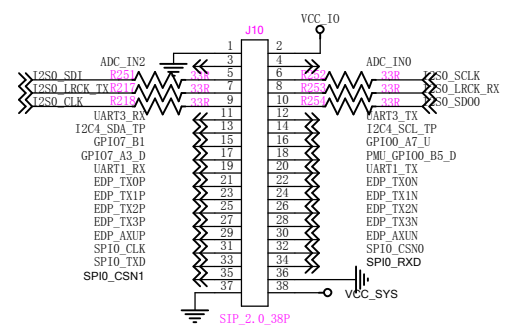
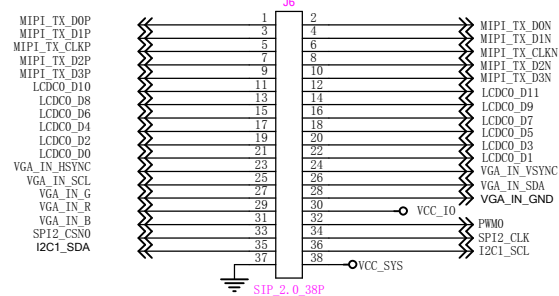
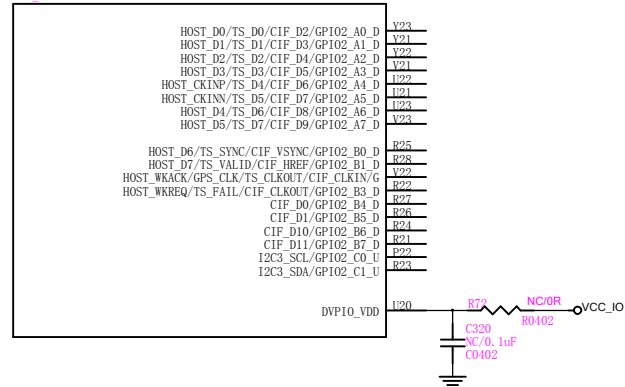


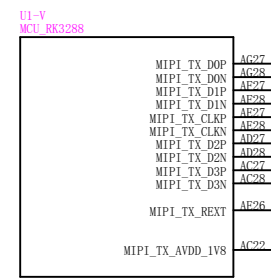
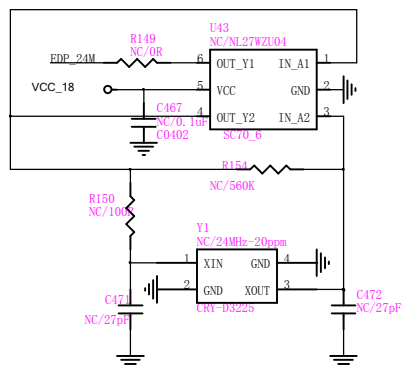
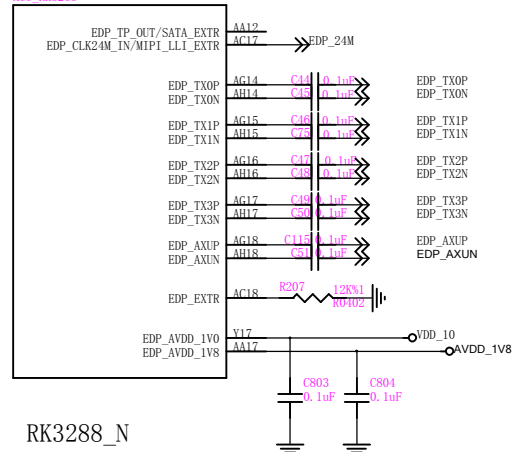
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AP6210	Yes	No	Yes	No	No
AP6234	No	Yes	No	No	No
AP6330	No	Yes	No	No	No
AP6335 (Default)	No	Yes	No	Yes	Yes

Note:
Yes:Inside the box to
No:The box is not posted

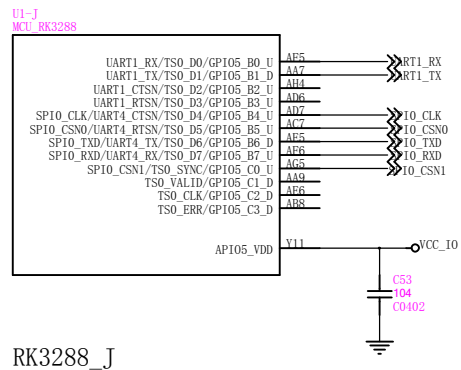
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File:	REV:1. 1
Create Date:	Page Num: 18
Modify Date: Wednesday, June 18, 2014	Page Total: 21







Firefly			
Title: Camera Interface			
File:			REV:1.1
Create Date:	Page	Num: 20	
Modify Date: Wednesday, June 18, 2014	Page	Total: 21	



RK3288_J

Firefly		
Title: DTV-(Reserve)		
File:	REV:l.1	
Create Date:	Page	Num: 21
Modify Date: Wednesday, June 18, 2014	Page Total:	21