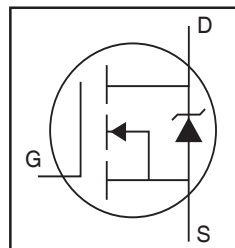


# AUIRLS3034

HEXFET® Power MOSFET

## Features

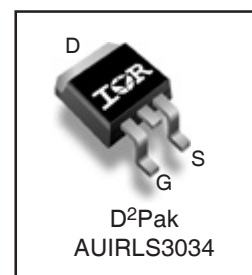
- Advanced Process Technology
- Ultra Low On-Resistance
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Repetitive Avalanche Allowed up to Tjmax
- Lead-Free, RoHS Compliant
- Automotive Qualified \*



$V_{DSS}$	<b>40V</b>
$R_{DS(on)}$ <b>typ.</b>	<b>1.4mΩ</b>
<b>max.</b>	<b>1.7mΩ</b>
$I_D$ (Silicon Limited)	<b>343A</b> ①
$I_D$ (Package Limited)	<b>195A</b>

## Description

Specifically designed for Automotive applications, this HEXFET® Power MOSFET utilizes the latest processing techniques to achieve extremely low on-resistance per silicon area. Additional features of this design are a 175°C junction operating temperature, fast switching speed and improved repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in Automotive applications and a wide variety of other applications.



<b>G</b>	<b>D</b>	<b>S</b>
Gate	Drain	Source

## Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T<sub>A</sub>) is 25°C, unless otherwise specified.

Symbol	Parameter	Max.	Units
$I_D$ @ T <sub>C</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V (Silicon Limited)	343①	A
$I_D$ @ T <sub>C</sub> = 100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V (Silicon Limited)	243 ①	
$I_D$ @ T <sub>C</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V (Package Limited)	195	
$I_{DM}$	Pulsed Drain Current ②	1372	
$P_D$ @ T <sub>C</sub> = 25°C	Maximum Power Dissipation	375	W
	Linear Derating Factor	2.5	W/°C
V <sub>GS</sub>	Gate-to-Source Voltage	±20	V
E <sub>AS</sub>	Single Pulse Avalanche Energy (Thermally Limited) ③	255	mJ
I <sub>AR</sub>	Avalanche Current ②	See Fig. 14, 15, 22a, 22b,	A
E <sub>AR</sub>	Repetitive Avalanche Energy ②		mJ
dv/dt	Peak Diode Recovery ④	4.6	V/ns
T <sub>J</sub>	Operating Junction and	-55 to + 175	°C
T <sub>STG</sub>	Storage Temperature Range		
	Soldering Temperature, for 10 seconds (1.6mm from case)	300	
	Mounting torque, 6-32 or M3 screw	10lbf·in (1.1N·m)	

## Thermal Resistance

Symbol	Parameter	Typ.	Max.	Units
R <sub>θJC</sub>	Junction-to-Case ⑤⑥	—	0.4	°C/W
R <sub>θJA</sub>	Junction-to-Ambient (PCB Mount) ⑤	—	40	

HEXFET® is a registered trademark of International Rectifier.

\*Qualification standards can be found at <http://www.irf.com/>

**Static Electrical Characteristics @  $T_J = 25^\circ\text{C}$  (unless otherwise specified)**

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	40	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.04	—	V/°C	Reference to $25^\circ\text{C}$ , $I_D = 5mA$ ②
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	1.4	1.7	mΩ	$V_{GS} = 10V, I_D = 195A$ ⑤
		—	1.6	2.0		$V_{GS} = 4.5V, I_D = 172A$ ⑤
$V_{GS(th)}$	Gate Threshold Voltage	1.0	—	2.5	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
$g_{fs}$	Forward Transconductance	286	—	—	S	$V_{DS} = 10V, I_D = 195A$
$R_{G(int)}$	Internal Gate Resistance	—	2.1	—	Ω	
$I_{DSS}$	Drain-to-Source Leakage Current	—	—	20	μA	$V_{DS} = 40V, V_{GS} = 0V$
		—	—	250		$V_{DS} = 40V, V_{GS} = 0V, T_J = 125^\circ\text{C}$
$I_{GSS}$	Gate-to-Source Forward Leakage	—	—	100	nA	$V_{GS} = 20V$
	Gate-to-Source Reverse Leakage	—	—	-100		$V_{GS} = -20V$

**Dynamic Electrical Characteristics @  $T_J = 25^\circ\text{C}$  (unless otherwise specified)**

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$Q_g$	Total Gate Charge	—	108	162	nC	$I_D = 185A$
$Q_{gs}$	Gate-to-Source Charge	—	29	—		$V_{DS} = 20V$
$Q_{gd}$	Gate-to-Drain ("Miller") Charge	—	54	—		$V_{GS} = 4.5V$ ⑤
$Q_{sync}$	Total Gate Charge Sync. ( $Q_g - Q_{gd}$ )	—	54	—		$I_D = 185A, V_{DS} = 0V, V_{GS} = 4.5V$
$t_{d(on)}$	Turn-On Delay Time	—	65	—	ns	$V_{DD} = 26V$
$t_r$	Rise Time	—	827	—		$I_D = 195A$
$t_{d(off)}$	Turn-Off Delay Time	—	97	—		$R_G = 2.1\Omega$
$t_f$	Fall Time	—	355	—		$V_{GS} = 4.5V$ ⑤
$C_{iss}$	Input Capacitance	—	10315	—	pF	$V_{GS} = 0V$
$C_{oss}$	Output Capacitance	—	1980	—		$V_{DS} = 25V$
$C_{rss}$	Reverse Transfer Capacitance	—	935	—		$f = 1.0MHz$
$C_{oss \text{ eff. (ER)}}$	Effective Output Capacitance (Energy Related) ⑦	—	2378	—		$V_{GS} = 0V, V_{DS} = 0V \text{ to } 32V$ ⑦
$C_{oss \text{ eff. (TR)}}$	Effective Output Capacitance (Time Related) ⑥	—	2986	—		$V_{GS} = 0V, V_{DS} = 0V \text{ to } 32V$ ⑥

**Diode Characteristics**

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$I_S$	Continuous Source Current (Body Diode)	—	—	343 ①	A	MOSFET symbol showing the integral reverse p-n junction diode.
$I_{SM}$	Pulsed Source Current (Body Diode) ②	—	—	1372		
$V_{SD}$	Diode Forward Voltage	—	—	1.3	V	$T_J = 25^\circ\text{C}, I_S = 195A, V_{GS} = 0V$ ⑤
$t_{rr}$	Reverse Recovery Time	—	39	—	ns	$T_J = 25^\circ\text{C}$ $V_R = 34V,$
		—	41	—		$T_J = 125^\circ\text{C}$ $I_F = 195A$
$Q_{rr}$	Reverse Recovery Charge	—	39	—	nC	$T_J = 25^\circ\text{C}$ $di/dt = 100A/\mu s$ ⑤
		—	46	—		$T_J = 125^\circ\text{C}$
$I_{RRM}$	Reverse Recovery Current	—	1.7	—	A	$T_J = 25^\circ\text{C}$
$t_{on}$	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

**Notes:**

- ① Calculated continuous current based on maximum allowable junction temperature Bond wire current limit is 195A. Note that current limitation arising from heating of the device leads may occur with some lead mounting arrangements.
- ② Repetitive rating; pulse width limited by max. junction temperature.
- ③ Limited by  $T_{Jmax}$ , starting  $T_J = 25^\circ\text{C}$ ,  $L = 0.013mH$   
 $R_G = 25\Omega$ ,  $I_{AS} = 195A$ ,  $V_{GS} = 10V$ . Part not recommended for use above this value.
- ④  $I_{SD} \leq 195A$ ,  $di/dt \leq 841A/\mu s$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J \leq 175^\circ\text{C}$ .

- ⑤ Pulse width  $\leq 400\mu s$ ; duty cycle  $\leq 2\%$ .
- ⑥  $C_{oss \text{ eff. (TR)}}$  is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .
- ⑦  $C_{oss \text{ eff. (ER)}}$  is a fixed capacitance that gives the same energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .
- ⑧ When mounted on 1" square PCB (FR-4 or G-10 Material).  
For recommended footprint and soldering techniques refer to application note # AN-994.
- ⑨  $R_\theta$  is measured at  $T_J$  approximately  $90^\circ\text{C}$ .
- ⑩  $R_{\theta JC}$  value shown is at time zero.

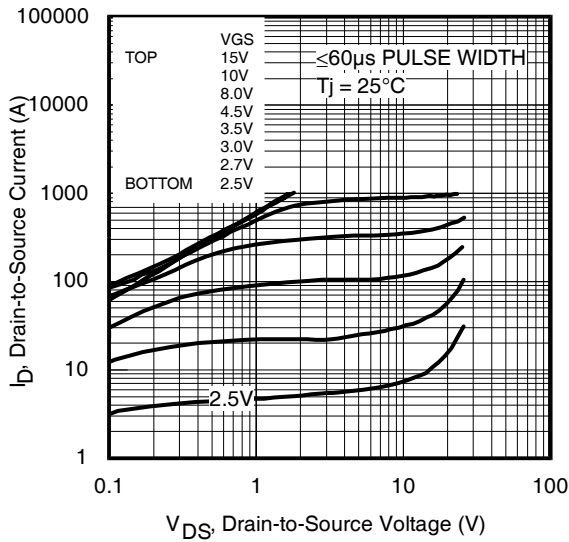
# **Qualification Information<sup>†</sup>**

<b>Qualification Level</b>		Automotive (per AEC-Q101) <sup>††</sup>	
		Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.	
<b>Moisture Sensitivity Level</b>		D <sup>2</sup> Pak	MSL1
<b>ESD</b>	Machine Model	Class M4 (+/- 800V) <sup>†††</sup> AEC-Q101-002	
	Human Body Model	Class H3A (+/- 6000V) <sup>†††</sup> AEC-Q101-001	
	Charged Device Model	Class C5 (+/- 2000V) <sup>†††</sup> AEC-Q101-005	
<b>RoHS Compliant</b>		Yes	

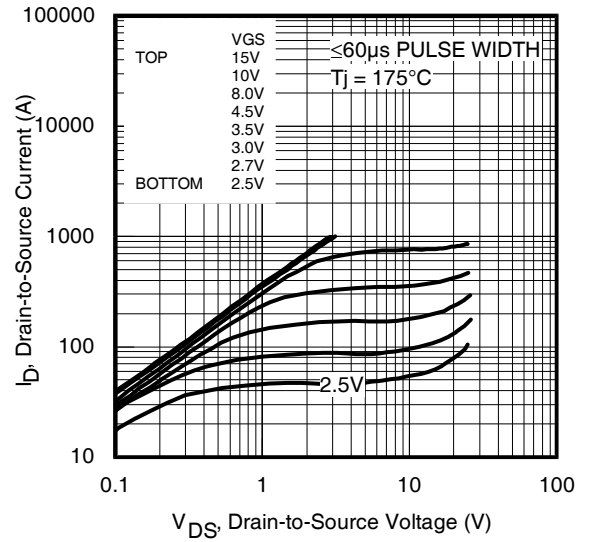
<sup>†</sup> Qualification standards can be found at International Rectifier's web site: <http://www.irf.com/>

<sup>††</sup> Exceptions (if any) to AEC-Q101 requirements are noted in the qualification report.

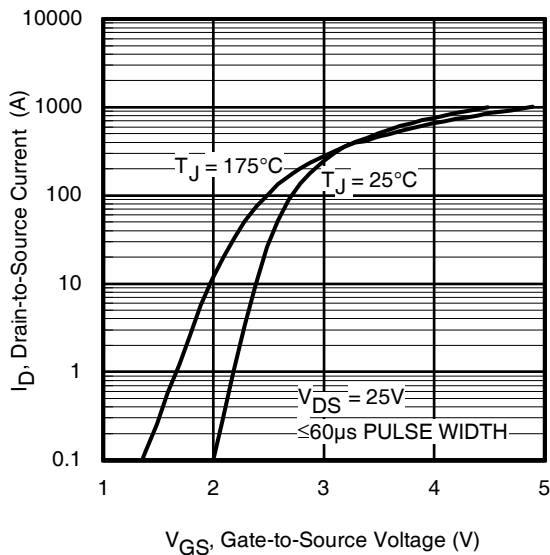
<sup>†††</sup> Highest passing voltage.



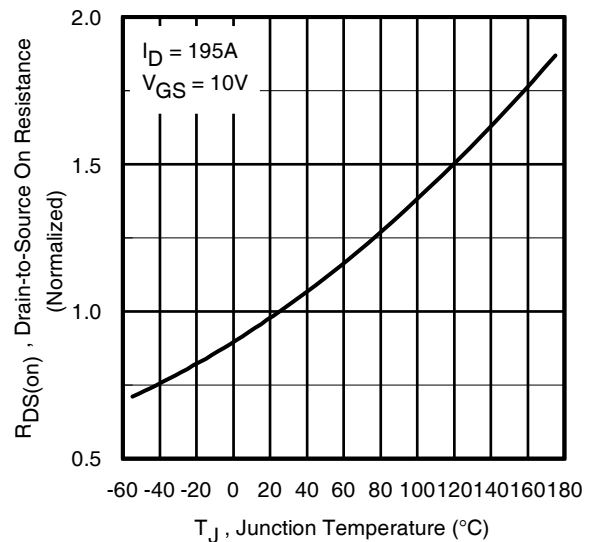
**Fig 1.** Typical Output Characteristics



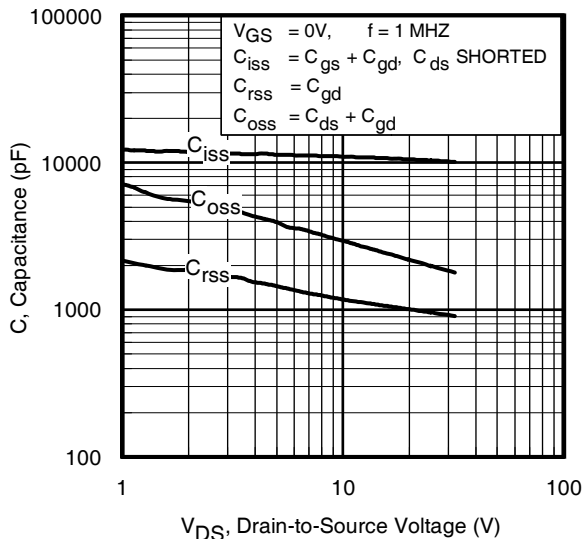
**Fig 2.** Typical Output Characteristics



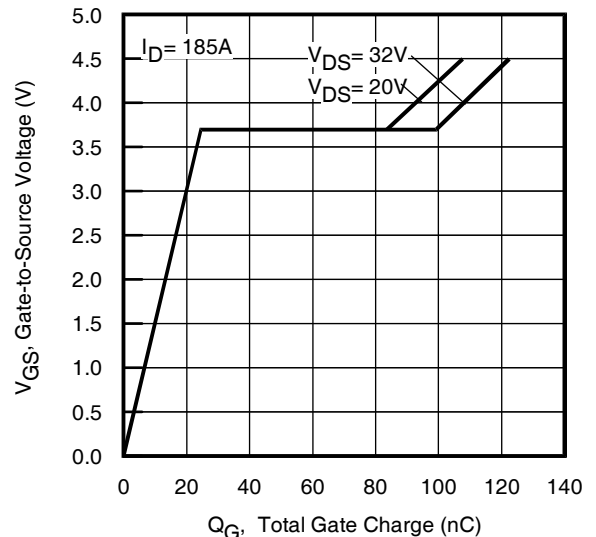
**Fig 3.** Typical Transfer Characteristics



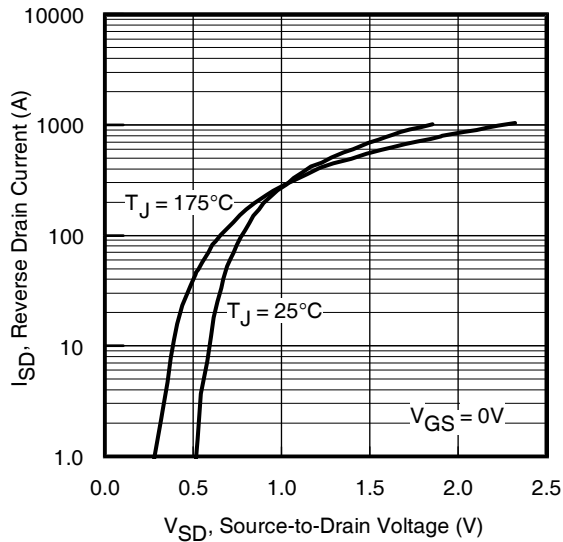
**Fig 4.** Normalized On-Resistance vs. Temperature



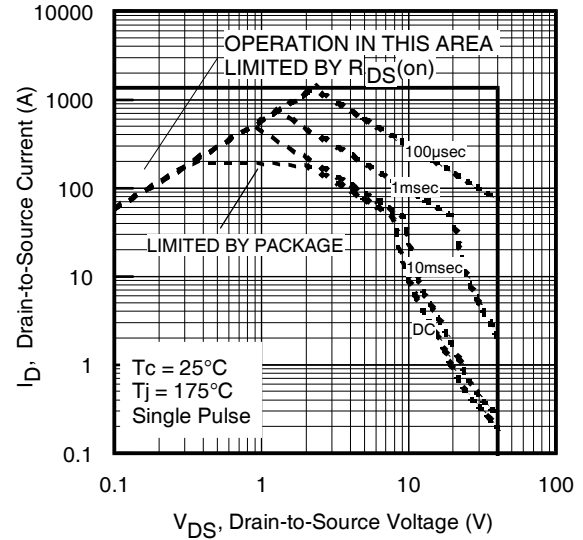
**Fig 5.** Typical Capacitance vs. Drain-to-Source Voltage



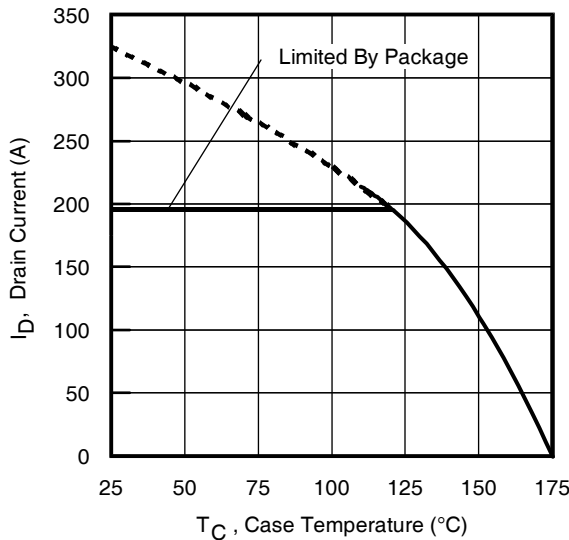
**Fig 6.** Typical Gate Charge vs. Gate-to-Source Voltage



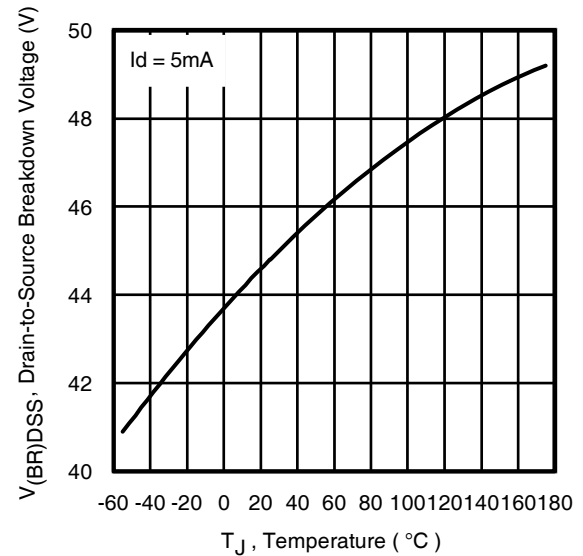
**Fig 7.** Typical Source-Drain Diode Forward Voltage



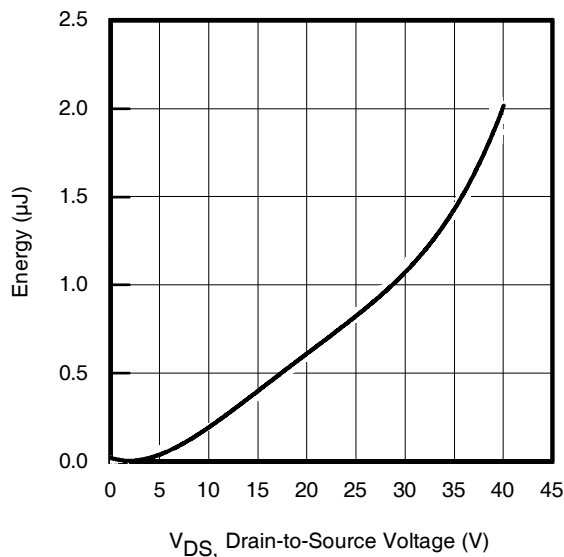
**Fig 8.** Maximum Safe Operating Area



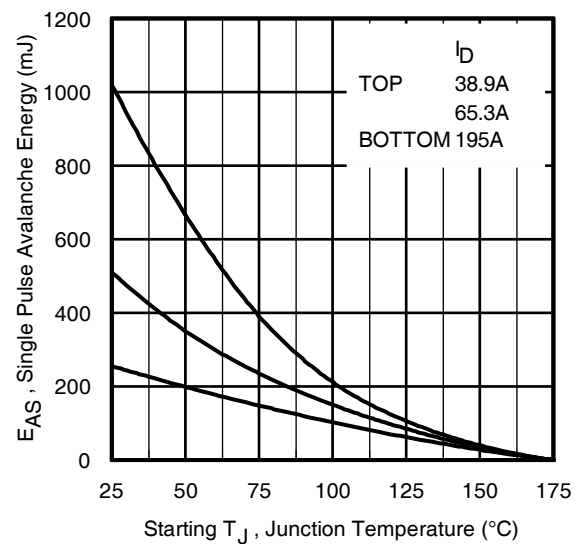
**Fig 9.** Maximum Drain Current vs. Case Temperature



**Fig 10.** Drain-to-Source Breakdown Voltage



**Fig 11.** Typical  $C_{OSS}$  Stored Energy



**Fig 12.** Maximum Avalanche Energy vs. Drain Current

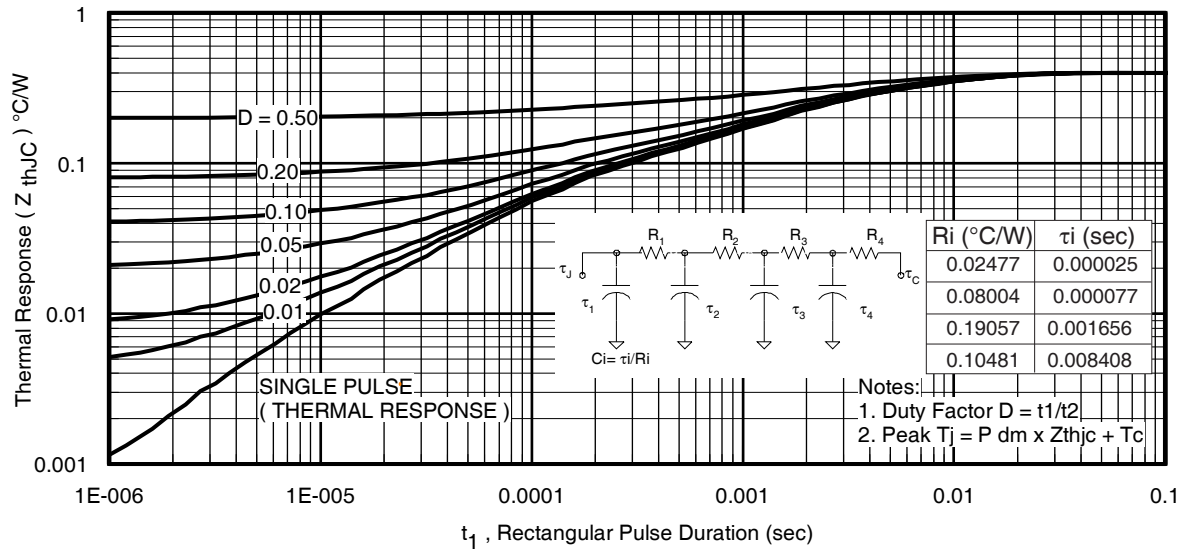


Fig 13. Maximum Effective Transient Thermal Impedance, Junction-to-Case

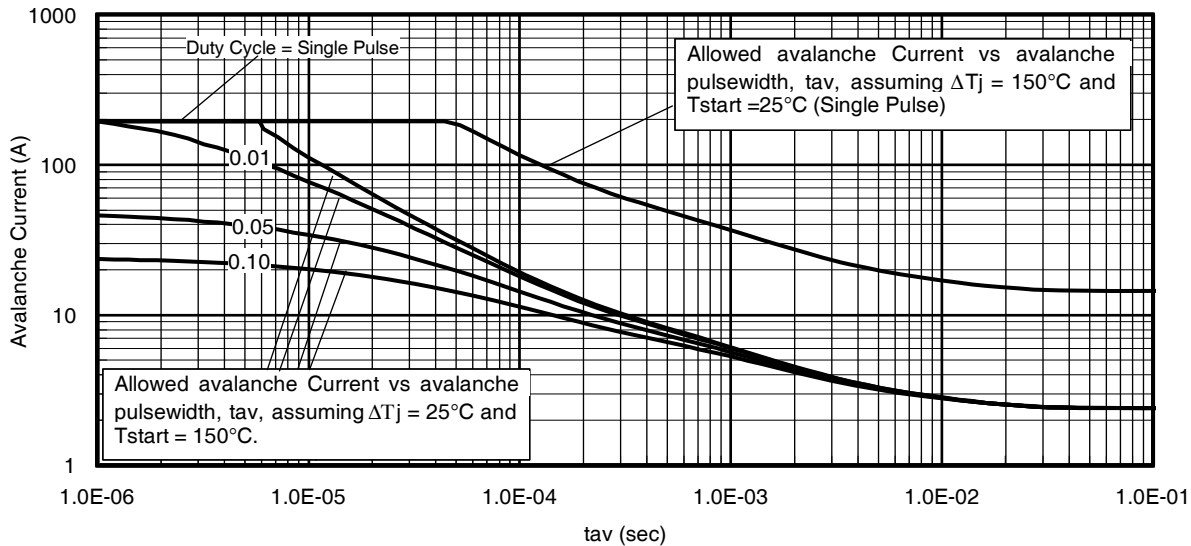
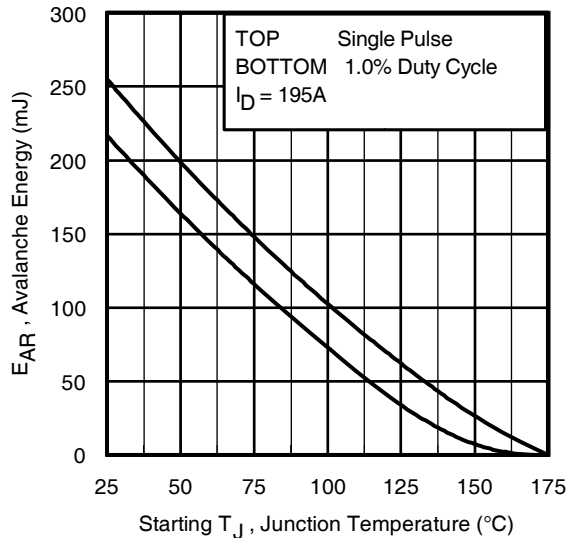


Fig 14. Typical Avalanche Current vs. Pulsewidth



Notes on Repetitive Avalanche Curves, Figures 14, 15:  
(For further info, see AN-1005 at [www.irf.com](http://www.irf.com))

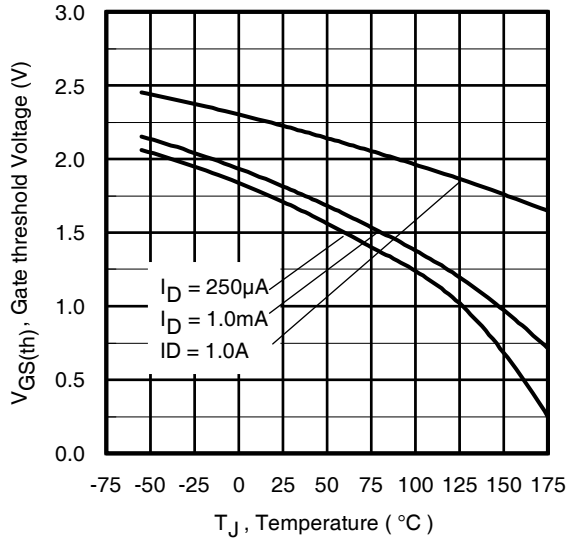
1. Avalanche failures assumption:  
Purely a thermal phenomenon and failure occurs at a temperature far in excess of  $T_{jmax}$ . This is validated for every part type.
2. Safe operation in Avalanche is allowed as long as  $T_{jmax}$  is not exceeded.
3. Equation below based on circuit and waveforms shown in Figures 16a, 16b.
4.  $P_{D(ave)}$  = Average power dissipation per single avalanche pulse.
5. BV = Rated breakdown voltage (1.3 factor accounts for voltage increase during avalanche).
6.  $I_{av}$  = Allowable avalanche current.
7.  $\Delta T$  = Allowable rise in junction temperature, not to exceed  $T_{jmax}$  (assumed as 25°C in Figure 14, 15).  
 $t_{av}$  = Average time in avalanche.  
 $D$  = Duty cycle in avalanche =  $t_{av} \cdot f$   
 $Z_{thJC}(D, t_{av})$  = Transient thermal resistance, see Figures 13)

$$P_{D(ave)} = 1/2 (1.3 \cdot BV \cdot I_{av}) = \Delta T / Z_{thJC}$$

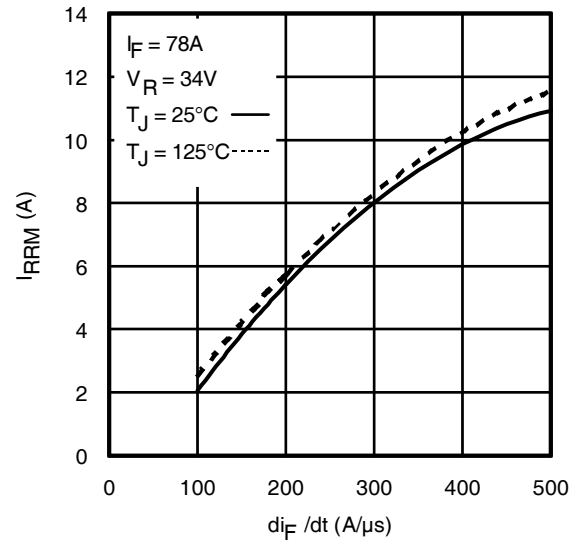
$$I_{av} = 2\Delta T / [1.3 \cdot BV \cdot Z_{th}]$$

$$E_{AS(AR)} = P_{D(ave)} \cdot t_{av}$$

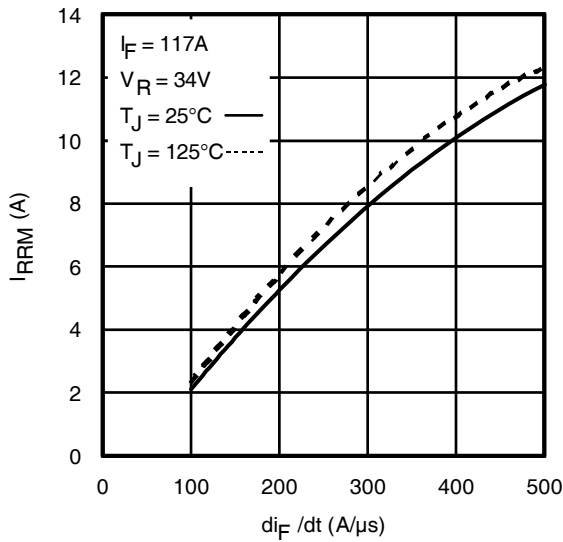
Fig 15. Maximum Avalanche Energy vs. Temperature



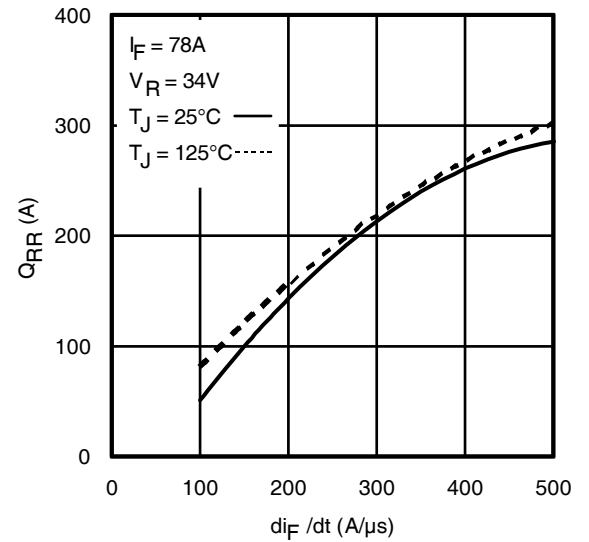
**Fig. 16.** Threshold Voltage vs. Temperature



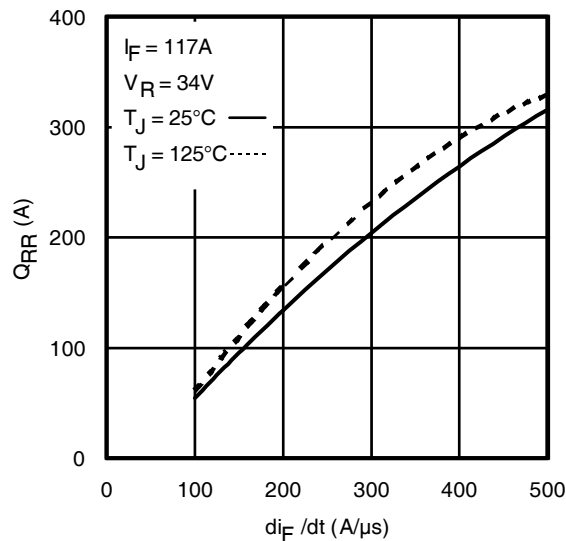
**Fig. 17 -** Typical Recovery Current vs.  $di_F/dt$



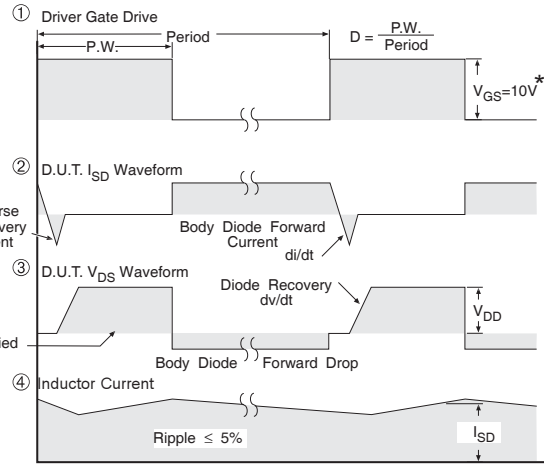
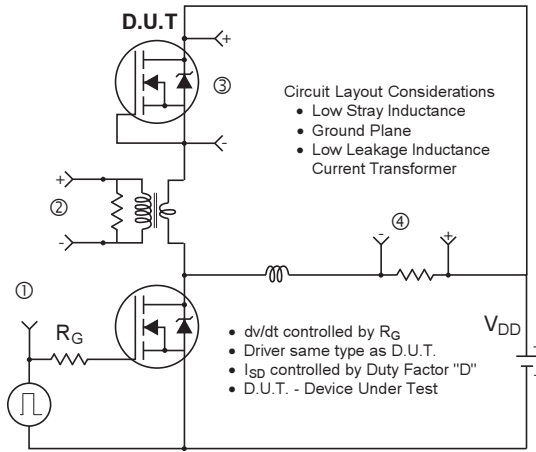
**Fig. 18 -** Typical Recovery Current vs.  $di_F/dt$



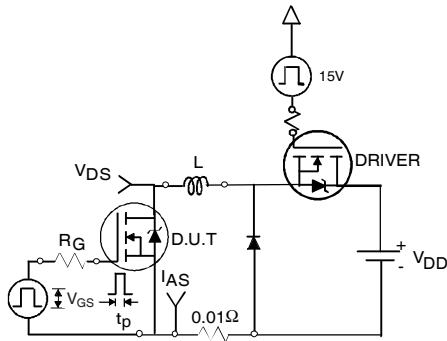
**Fig. 19 -** Typical Stored Charge vs.  $di_F/dt$



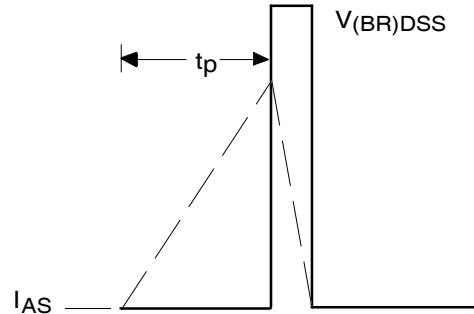
**Fig. 20 -** Typical Stored Charge vs.  $di_F/dt$



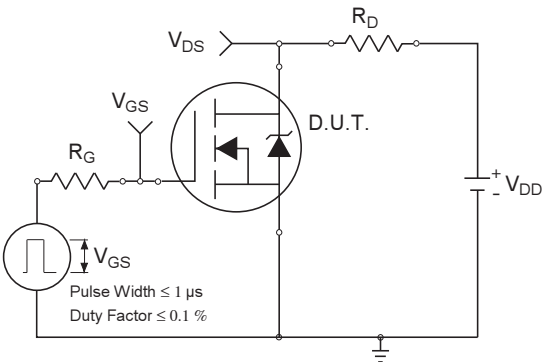
**Fig 21. Peak Diode Recovery  $dv/dt$  Test Circuit for N-Channel HEXFET® Power MOSFETs**



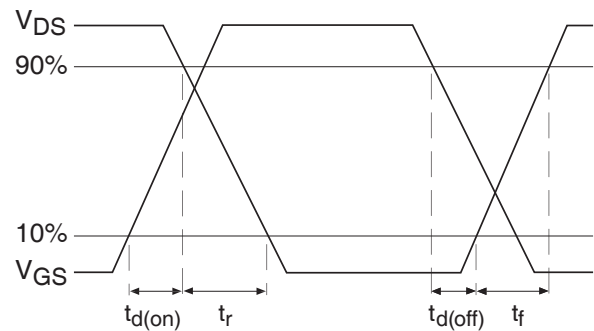
**Fig 22a. Unclamped Inductive Test Circuit**



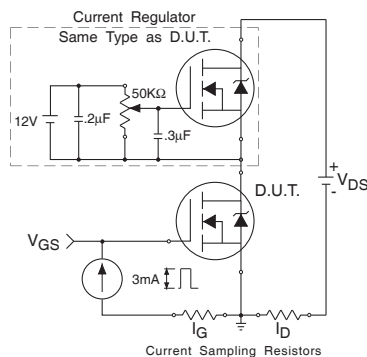
**Fig 22b. Unclamped Inductive Waveforms**



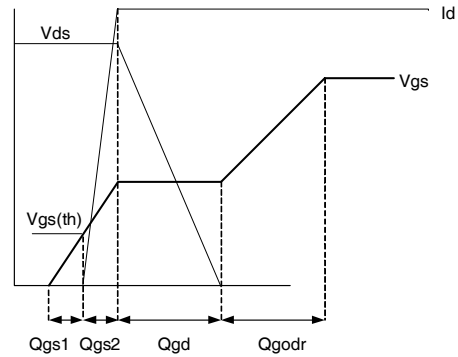
**Fig 23a. Switching Time Test Circuit**



**Fig 23b. Switching Time Waveforms**



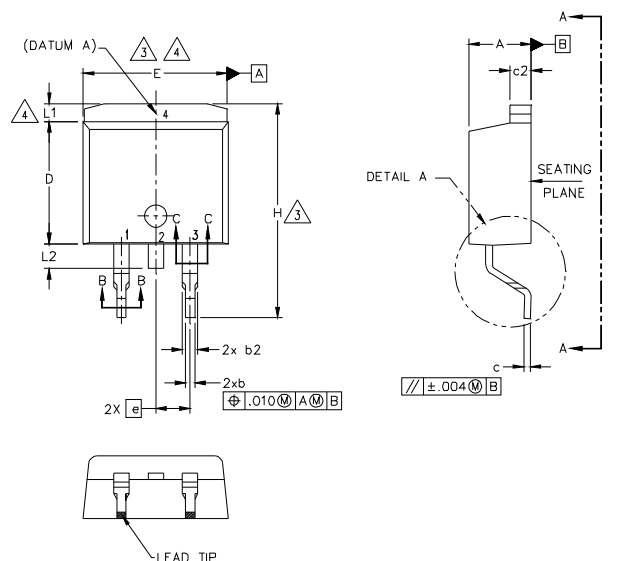
**Fig 24a. Gate Charge Test Circuit**



**Fig 24b. Gate Charge Waveform**



## D<sup>2</sup>Pak Package Outline (Dimensions are shown in millimeters (inches))



S Y M B O L	D I M E N S I O N S				N O T E S
	M I L L I M E T E R S		I N C H E S		
	M I N.	M A X.	M I N.	M A X.	
A	4.06	4.83	.160	.190	
A1	0.00	0.254	.000	.010	
b	0.51	0.99	.020	.039	
b1	0.51	0.89	.020	.035	5
b2	1.14	1.78	.045	.070	
b3	1.14	1.73	.045	.068	5
c	0.38	0.74	.015	.029	
c1	0.38	0.58	.015	.023	5
c2	1.14	1.65	.045	.065	
D	8.38	9.65	.330	.380	3
D1	6.86	—	.270		4
E	9.65	10.67	.380	.420	3,4
E1	6.22	—	.245		4
e	2.54 BSC		.100 BSC		
H	14.61	15.88	.575	.625	
L	1.78	2.79	.070	.110	
L1	—	1.65	—	.066	4
L2	1.27	1.78	—	.070	
L3	0.25 BSC		.010 BSC		
L4	4.78	5.28	.188	.208	

## LEAD ASSIGNMENTS

## DIODES

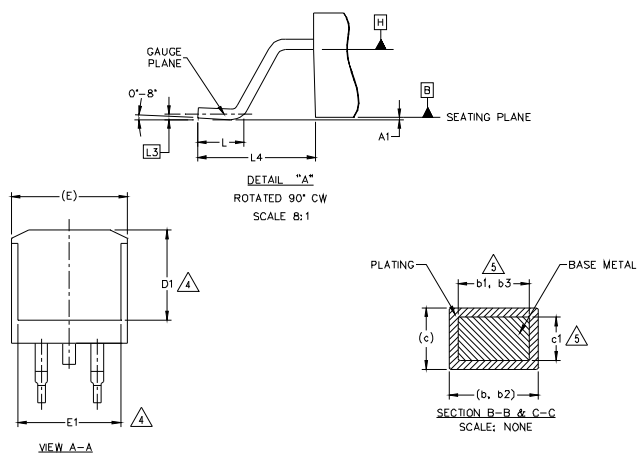
- 1.- ANODE (TWO DIE) / OPEN (ONE DIE)  
2, 4.- CATHODE  
3.- ANODE

## HEXFET

- 1, - GATE  
2, 4, - DRAIN  
3, - SOURCE

## IGBTs, CoPACK

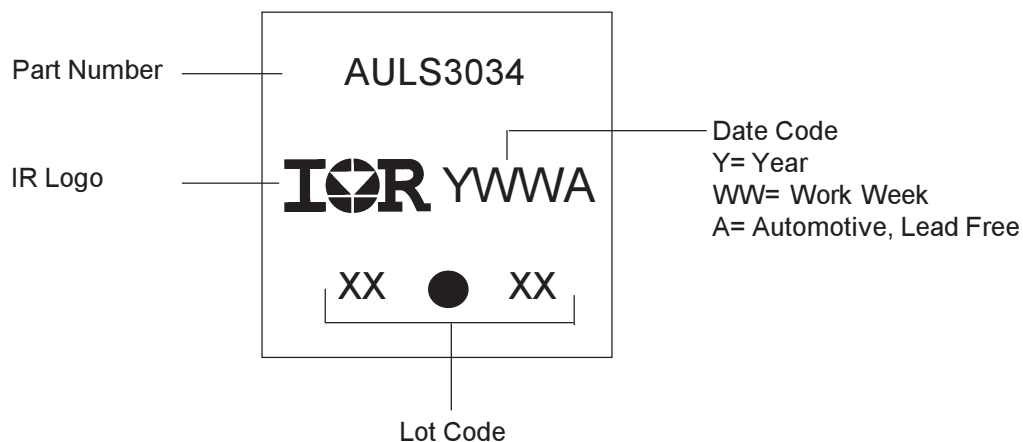
- 1.- GATE
- 2, 4.- COLLECTOR
- 3.- EMITTER



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [0.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
4. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION E, L1, D1 & E1.
5. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
6. DATUM A & B TO BE DETERMINED AT DATUM PLANE H.
7. CONTROLLING DIMENSION: INCH.
8. OUTLINE CONFORMS TO JEDEC OUTLINE TO-263AB.

## D<sup>2</sup>Pak Part Marking Information



Note: For the most current drawing please refer to IR website at: <http://www.irf.com/package/>

**TRR**

**FEED DIRECTION**

**TRL**

**FEED DIRECTION**

1.60 (.063)  
1.50 (.059)

4.10 (.161)  
3.90 (.153)

1.85 (.073)  
1.65 (.065)

11.60 (.457)  
11.40 (.449)

1.60 (.063)  
1.50 (.059)

1.75 (.069)  
1.25 (.049)

10.90 (.429)  
10.70 (.421)

16.10 (.634)  
15.90 (.626)

0.368 (.0145)  
0.342 (.0135)

15.42 (.609)  
15.22 (.601)

24.30 (.957)  
23.90 (.941)

4.72 (.136)  
4.52 (.178)

13.50 (.532)  
12.80 (.504)

Ø 330.00 (14.173) MAX.

27.40 (1.079)  
23.90 (.941)

Ø 60.00 (2.362) MIN.

30.40 (1.197) MAX.

26.40 (1.039)  
24.40 (.961)

**NOTES :**

1. COMFORMS TO EIA-418.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION MEASURED @ HUB.
4. INCLUDES FLANGE DISTORTION @ OUTER EDGE.

www.irf.com

## Ordering Information

Base part number	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIRLS3034	D2Pak	Tube	50	AUIRLS3034
		Tape and Reel Left	800	AUIRLS3034TRL
		Tape and Reel Right	800	AUIRLS3034TRR

## IMPORTANT NOTICE

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