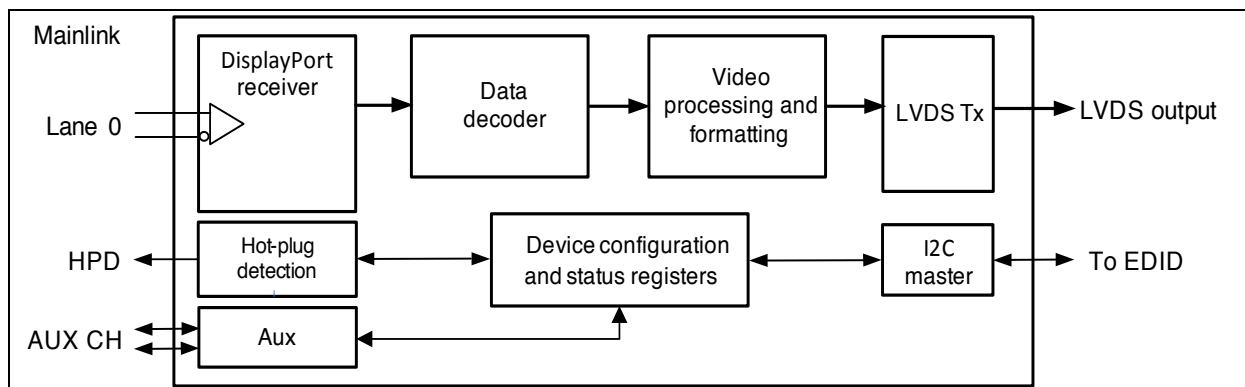


Low Cost DisplayPort™ to LVDS Converter

ANX3111 is a low cost high quality DisplayPort to LVDS converter offering up to 18-bits per pixel and single channel LVDS output support. It provides a bridge between DisplayPort-enabled next generation source devices (such as GPUs) and existing LVDS displays. Featuring low power and EMI reduction techniques and its low cost and small form factor; it is ideal for motherboards and systems with embedded displays.

Features

- VESA compliant DisplayPort 1.1a receiver
 - Configurable 1 lane operation
 - 1.62/2.7Gbps data rate support
 - AUX channel link up to 1Mbps bandwidth
 - All DisplayPort receiver side registers accessible by the AUX channel and I2C slave interface
 - Spread spectrum clock (SSC) support for improved EMI performance
- Single channel LVDS output
 - Single channel: up to 120 MHz pixel clock
 - Up to 18 bits per pixel
- Video modes
 - Up to 1680×1050@60Hz
 - 18 bits per pixel support
- LVDS spread spectrum clocking for reduced EMI
- On-chip ring oscillator, no external clock required
- Low BOM cost, no local MCU required
 - Fully controllable via DisplayPort Tx through AUX channel
- Low power design supporting several power saving modes
- Selectable 3.3/1.2V or 2.5/1.2V operation
- Package
 - 64-pin QFN, pin compatible with ANX3110
 - RoHS compliant and Halogen free



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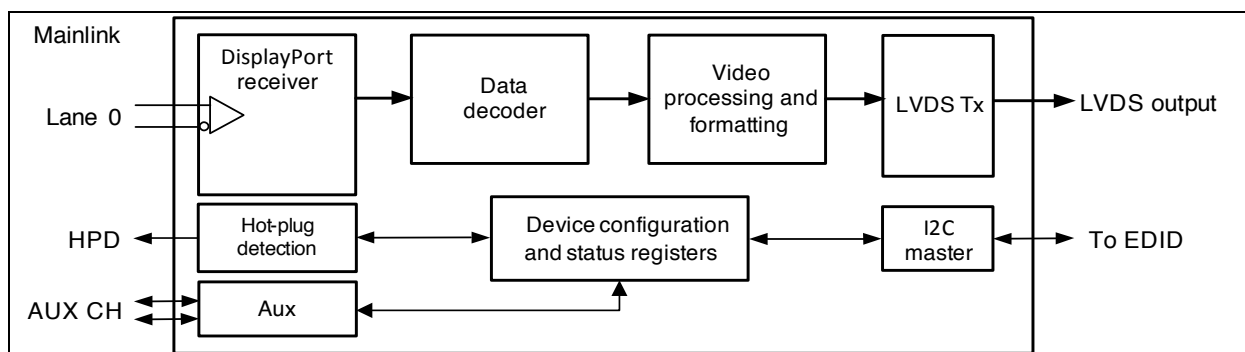
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Low Cost DisplayPort™ to LVDS Converter

ANX3112 is a low cost high quality DisplayPort to LVDS converter offering up to 18-bits per pixel and single channel LVDS output support. It provides a bridge between DisplayPort-enabled next generation source devices (such as GPUs) and existing LVDS displays. Featuring low power and EMI reduction techniques and its low cost and small form factor, ANX3112 is ideal for motherboards and systems with embedded displays.

Features

- VESA compliant DisplayPort 1.1a receiver
 - Configurable 1 lane operation
 - 1.62/2.7Gbps data rate support
 - AUX channel link up to 1Mbps bandwidth
 - All DisplayPort receiver side registers accessible by the AUX channel and I2C slave interface
 - Spread spectrum clock (SSC) support for improved EMI performance
- Single channel LVDS output
 - Single channel: up to 120 MHz pixel clock
 - Up to 18-bits per pixel
- Video modes
 - Up to 1680 x 1050@60Hz
 - 18 bits per pixel support
- LVDS spread spectrum clocking for reduced EMI
- On-chip ring oscillator, no external clock required
- Low BOM cost, no local MCU required
 - Fully controllable via DisplayPort Tx through AUX channel
- Low power design supporting several power saving modes
- Selectable 3.3/1.2V or 2.5/1.2V operation
- Package
 - 36-pin QFN
 - RoHS compliant and Halogen free



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