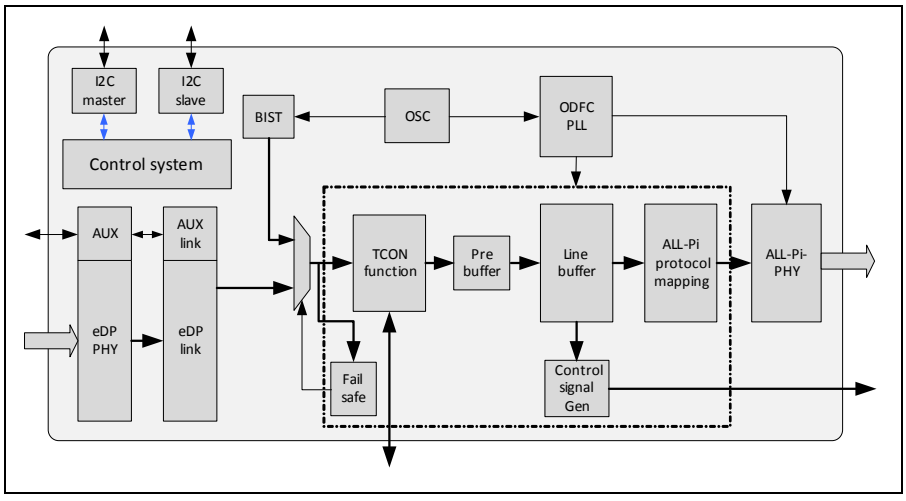
Preliminary
ANX1851
Product Brief

2-lane FullHD eDP TCON with 6-port All-PI Output

The ANX1851 chip is an eDP timing controller (TCON) designed for displays and panels. It adopts Analogix's new P2P interface ALL-PI, which offers lower cost and power consumption with low EMI. The ANX1851 chip supports 2-lane DisplayPort input 6-port ALL-PI output with video resolution of 1920x1200.

Features

- TCON functions
  - Support FHD (1920x1080@60) and WUXGA (1920x1200@60)
  - Support gate D-IC/GIP timing through GPIO
  - Power-up gate output masking to avoid DC/DC over-loading
  - Programmable fail-safe mode control configurable BIST and AGP pattern
  - 10-bit Gamma correction table for each color
  - Support various FRC pattern configurations
  - PWM generator, PWM pass-through, and PWM product modes
  - 0-D dimming function for backlight low power
  - Support color engine process function
  - 18/24 bpp color depth
- eDP input
  - 2-lane DisplayPort 1.2/ eDP 1.3 interface
  - RBR (1.62Gpbs), HBR(2.7Gbps)
  - Support DRRs/NvDRRs/SDRRs functions
- ALL-PI output
  - 6-port/1-pair configuration
  - Compatible with ALL-PI v1.0
  - ALL-PI TX supports up to 1.2Gbps per pair
  - Support various swap modes: pair swap, p/n swap, RGB swap, etc
  - Support Z-inversion (column, inversion), and N-line inversion
  - Support DRD mode
  - Adjustable output swing and common mode
  - CON line to initialize SD-IC
- System IO functions for special applications
  - EDID access through AUX channel
  - On chip filtered reset
  - EEPROM shared for EDID and configuration power requirements
  - I2C standard mode (100KHz) and fast mode (400KHz) support
  - On-chip SSCG (+/-1% with 0.25% step, 10/20/30/40KHz, center spreading)
- No external crystal required
- 2.5VIO and 1.2VCORE power supply or 3.3VIO and 1.2VCORE power supply support
- 60-pPin QFN package (5x9mm)
- RoHS compliant and Halogen free package



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