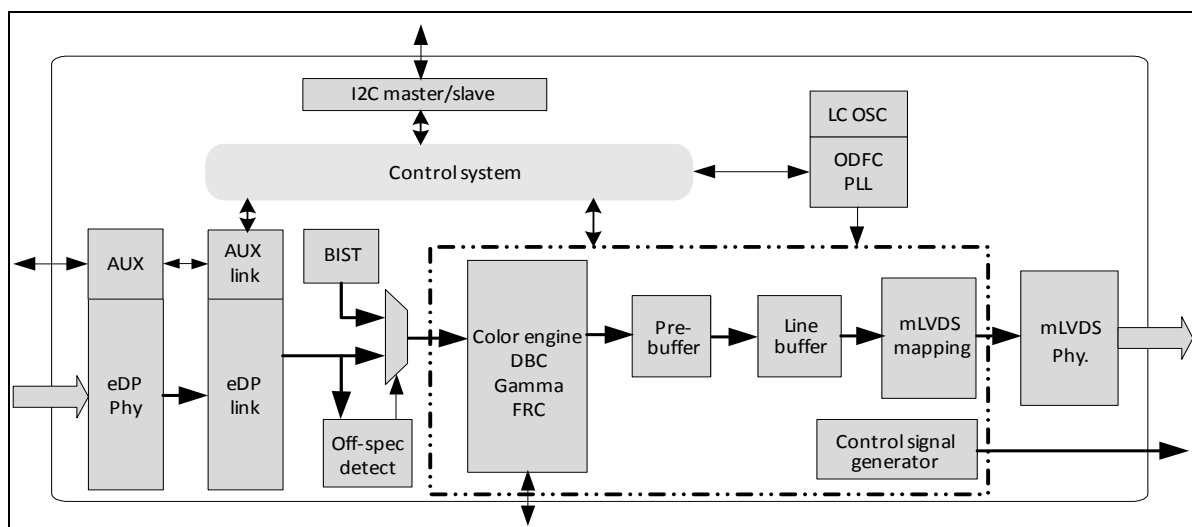


Low Power 1-lane DisplayPort™ TCON

The ANX1811 is an ultra low power 1-lane DisplayPort timing-controller (TCON) for portable device displays/panels. As a member of the Rocky family products, the ANX1811 supports display resolution up to 1600 X 900, 8bpc at the reduced-blanking mode with the lowest power consumption.

Features

- eDP input
 - Single lane DisplayPort 1.1a/ eDP 1.2 interface
 - RBR (1.62Gbps), HBR (2.7Gbps)
 - 0.5% down spread spectrum clock generator (SSCG)
 - Normal/fast/no link training
- High speed mini-LVDS panel interface
 - 1 port, 3 pair configuration for 6-bit/8-bit
 - mLVDS supports up to 660Mbps/channel
 - Various swap mode for polarity, pairs and RGB
 - Z-inversion (column, inversion), and N-line inversion
 - Support DRD mode
 - Adjustable output swing
 - Support for clock phase adjustment
 - On-chip SSCG (+/-1% with 0.25% step 10/20/30/40KHz, center spreading)
- System IO functions with (non) standard application
 - EDID access through AUX channel
 - Support DRRs/NvDRRs/SDRRs functions
 - On-chip filtered reset
 - EEPROM shared for EDID and configuration power requirements
- TCON function
 - Support WXGA, HD, up to HD+ (UXGA)
 - Support gate D-IC/GIP timing through GPIO
 - Power-up gate output masking to avoid DC/DC over-loading
 - Programmable fail-safe mode control configurable BIST and AGP pattern
 - 10-bit Gamma correction table for each color
 - Support various FRC pattern configurations
 - HiFRC supported
 - PWM generator, PWM pass-through and PWM product modes
 - 0-D dimming function for backlight low power
 - Support color engine process function
- Other functions
 - No external crystal required
 - 1.0V and 2.5V power supplies
 - 90mW (maximum) extremely low power dissipation
 - 4.5 x 6.5mm, 46 QFN (0.4mm pitch) package
- Package: QFN46 with E-PAD (TCON)
- RoHS compliant and Halogen free package



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