

### **Dual N-Channel Enhancement Mode MOSFET**

### **Description**

The ACE4922 is the Dual N-Channel enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching, low in-line power loss, and resistance to transients are needed.

### **Features**

- N-Channel
  20V/0.95A, RDS(ON)=380mΩ@V<sub>GS</sub>=4.5V
  20V/0.75A, RDS(ON)=450mΩ@V<sub>GS</sub>=2.5V
  - 20V/0.65A, RDS(ON)= $800m\Omega@V_{GS}=1.8V$
- Super high density cell design for extremely low R<sub>DS(ON)</sub>
- Exceptional on-resistance and maximum DC current capability

## **Application**

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

### **Absolute Maximum Ratings**

Parameter	Symbol	Max	Unit	
Drain-Source Voltage	$V_{DSS}$	20	٧	
Gate-Source Voltage	$V_{GSS}$	±20	V	
Continuous Drain Current (T <sub>J</sub> =150°C)		1.2	Α	
T <sub>A</sub> =80°C		0.9		
Pulsed Drain Current	I <sub>DM</sub>	4	Α	
Continuous Source Current (Diode Conduction)	I <sub>S</sub>	0.6	Α	
Power Dissipation	D	0.35	W	
T <sub>A</sub> =70°C	P <sub>D</sub>	0.19	VV	
Operating Junction Temperature	TJ	-55/150	°С	
Storage Temperature Range	T <sub>STG</sub>	-55/150	°С	

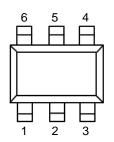


# **ACE4922**

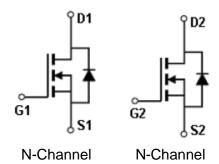
## **Dual N-Channel Enhancement Mode MOSFET**

## **Packaging Type**

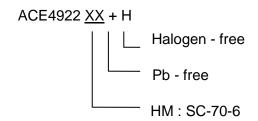
SC-70-6



SC-70-6	Description	
1	Source 1	
2	Gate 1	
3	Drain 2	
4	Source 2	
5	Gate 2	
6	Drain 1	



## **Ordering information**





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## **Electrical Characteristics**

 $T_A=25^{\circ}C$ , unless otherwise noted

Parameter	Symbol	Conditions Min.		Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250 uA				V	
Gate Threshold Voltage	$V_{GS(th)}$	$V_D$ =VGS, $I_D$ =250uA	0.35		1.0		
Gate Leakage Current	I <sub>GSS</sub>	$V_{DS}=0V, V_{GS}=\pm 12V$			100	nA	
Zero Gate Voltage Drain Current		$V_{DS}$ =20V, $V_{GS}$ =0V			1	uA	
	I <sub>DSS</sub>	$V_{DS}$ =20V, $V_{GS}$ =0V $T_J$ =55 $^{\circ}$ C			5		
On-State Drain Current	I <sub>D(ON)</sub>	$V_{DS}{\ge}4.5V,V_{GS}{=}5V$	0.7			Α	
D : 0		$V_{GS}$ =4.5V, $I_{D}$ =0.95A	0.26 0.38	0.38			
Drain-Source On-Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =2.5V, $I_{D}$ =0.75A		0.32	0.45	Ω	
		$V_{GS}$ =1.8V, $I_{D}$ =0.65A		0.42	0.80		
Forward Transconductance	gfs	$V_{DS}=10V, I_{D}=0.4A$	1.0			S	
Diode Forward Voltage	$V_{SD}$	$I_S$ =0.15A, $V_{GS}$ =0V	0.8		1.2	٧	
		Dynamic					
Total Gate Charge	$Q_g$			1.2	1.5		
Gate-Source Charge	$Q_{gs}$	$V_{DS}$ =10V, $V_{GS}$ =4.5V, $I_{D}$ =0.6A		0.2		nC	
Gate-Drain Charge	$Q_{gd}$			0.3			
Turn-On Time	td(on)			5	10		
	tr	$V_{DD}$ =10V, $R_L$ =10 $\Omega$ , $I_D$ =0.5A,		8	15		
Turne Off Time a	td(off)	$V_{GEN}$ =4.5V, $R_{G}$ =6 $\Omega$		10	18	nS	
Turn-Off Time	tf			1.2	2.8		





0.0

0.3

0.6

Qg-Total Gate Charge (nC)

0.9

1.2

1.5

0

25

T<sub>J</sub>-Junction Temperature (°C)

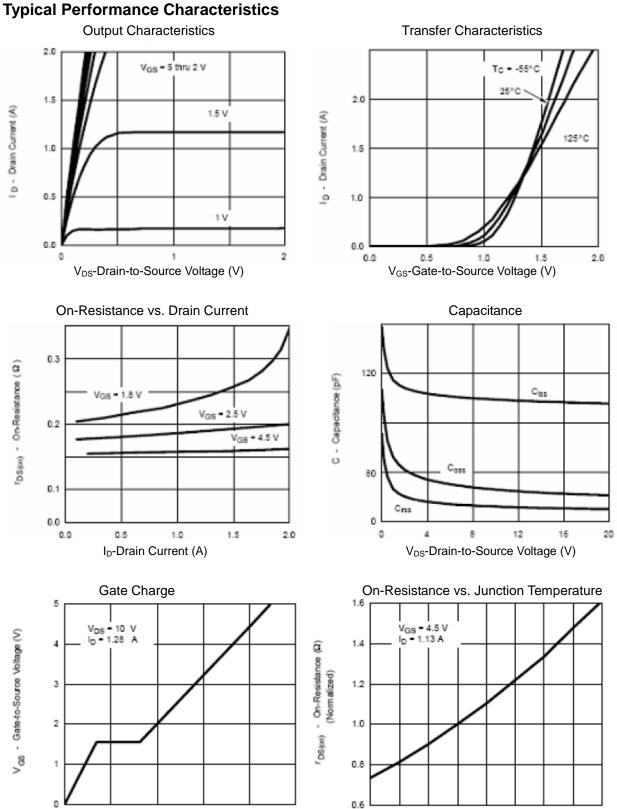
50

75

100

125

150



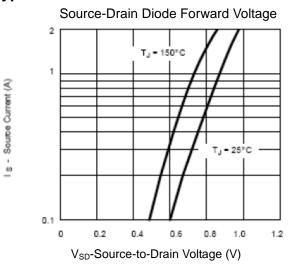


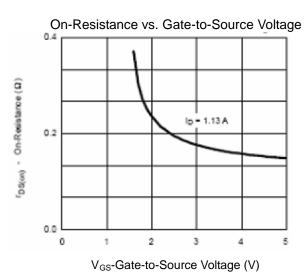


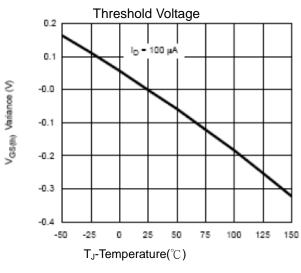
Normalized Effective Transient Thermal Impedance

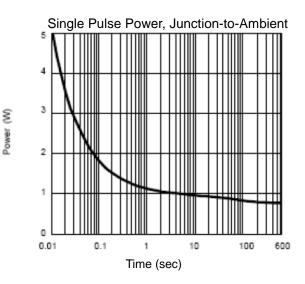
## **Dual N-Channel Enhancement Mode MOSFET**

## **Typical Performance Characteristics**

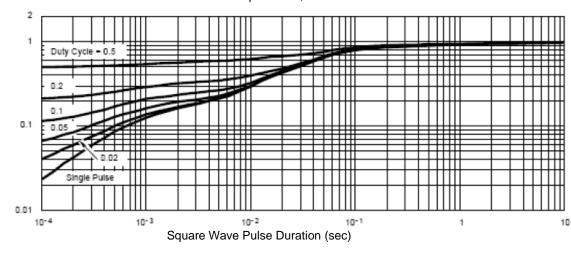








### Normalized Thermal Transient Impedance, Junction-to-Foot



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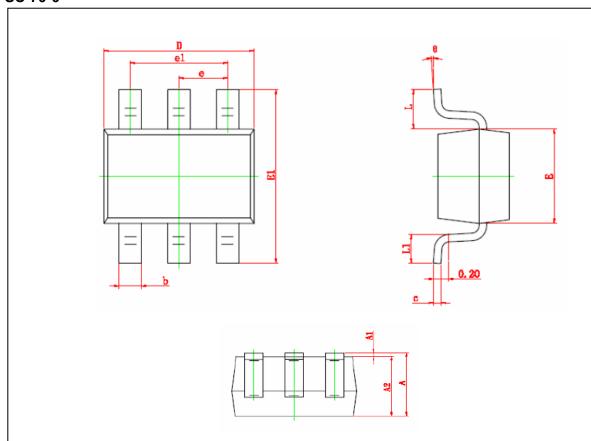




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# **Packing Information**

## SC-70-6



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
Α	0.900	1.100	0.035	0.043	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.000	0.035	0.039	
b	0.150	0.350	0.006	0.014	
С	0.080	0.150	0.003	0.006	
D	2.000	2.200	0.079	0.087	
E	1.150	1.350	0.045	0.053	
E1	2.150	2.450	0.085	0.096	
е	0.650 TYP		0.026 TYP		
e1	1.200	1.400	0.047	0.055	
L	0.525 REF		0.021 REF		
L1	0.260	0.460	0.010	0.018	
θ	0°	8°	0°	8°	

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## **ACE4922**

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### Notes

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- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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