

#### **Description**

The ACE1551A is the N-Channel enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching, low in-line power loss, and resistance to transients are needed.

#### **Features**

N-Channel

 $20V/0.95A, R_{DS(ON)} = 380 \text{m}\Omega @V_{GS} = 4.5V$ 

 $20V/0.75A, R_{DS(ON)} = 450 \text{m}\Omega@V_{GS} = 2.5V$ 

 $20V/0.65A, R_{DS(ON)} = 800 m\Omega@V_{GS} = 1.8V$ 

 $20V/0.65A, R_{DS(ON)} = 1000 m\Omega@V_{GS} = 1.5V$ 

- Super high density cell design for extremely low R<sub>DS(ON)</sub>
- Exceptional on-resistance and maximum DC current capability

#### **Applications**

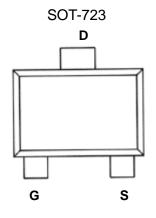
- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

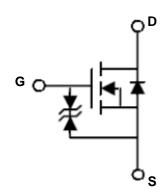
#### **Absolute Maximum Ratings**

Parameter	Symbol	Max	Unit
Drain-Source Voltage	$V_{DSS}$	20	V
Gate-Source Voltage	$V_{GSS}$	±12	V
Continuous Drain Current (TJ=150°C)	I <sub>D</sub>	0.65	Α
Pulsed Drain Current	$I_{DM}$	4	Α
Continuous Source Current (Diode Conduc	I <sub>S</sub>	0.3	Α
Power Dissipation	P <sub>D</sub>	0.15	W
Operating Junction Temperature / Storage Tempera	T <sub>J</sub> /T <sub>STG</sub>	-55/150	οС

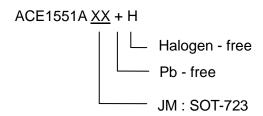


## **Packaging Type**





## **Ordering information**



#### **Electrical Characteristics**

 $T_A=25^{\circ}C$ , unless otherwise specified.

Parameter	Symbol	mbol Conditions		Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250 uA				<	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{DS}=250uA$	0.35		1.0		
Gate Leakage Current	I <sub>GSS</sub>	$V_{DS}$ =0V, $V_{GS}$ =±12V			30	uA	
Zero Gate Voltage Drain	I <sub>DSS</sub>	$V_{DS}$ =20V, $V_{GS}$ =0V			1	uA	
Current		$V_{DS}$ =20V, $V_{GS}$ =0V, $T_J$ =55 $^{\circ}$ C		5			
On-State Drain Current	$I_{D(on)}$	V <sub>DS</sub> ≥ 4.5V,V <sub>GS</sub> =5V	0.7			Α	
Drain-Source On-Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =4.5V, $I_{D}$ =0.95A		0.26	0.38		
		$V_{GS}$ =2.5V, $I_{D}$ =0.75A		0.32	0.45		
		$V_{GS}$ =1.8V, $I_{D}$ =0.65A		0.42	0.8	Ω	
		V <sub>GS</sub> =1.5V, I <sub>D</sub> =0.65A		0.5	1.0		
Forward Transconductance	gfs	$V_{DS} = 10V, I_{D} = 0.4A$		1.0		S	
Diode Forward Voltage	$V_{SD}$	$I_{SD}$ =0.15A, $V_{GS}$ =0V		0.8	1.2	V	
Dynamic							
Total Gate Charge	$Q_g$	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.6A		1.2	1.5	nC	
Gate-Source Charge	$Q_{gs}$			0.2			

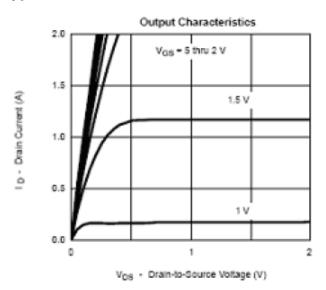


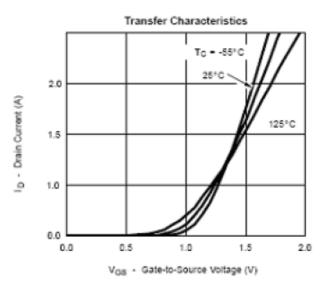
# **ACE1551A**

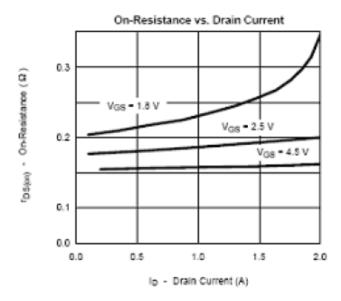
# **N-Channel Enhancement Mode MOSFET**

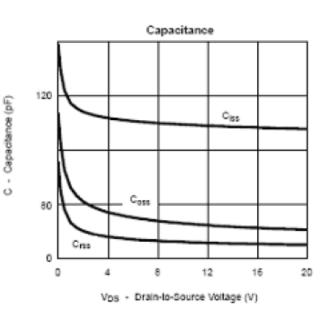
Gate-Drain Charge	$Q_gd$		0.3		
Turn On Time	td(on)	$V_{GEN}$ =4.5V, $I_D$ =0.5A, $V_{DD}$ =10V, $R_G$ =6 $\Omega$ , $R_L$ =10 $\Omega$	5	10	
Turn-On Time	tr		8	15	
Turn-Off Time	td(off)		10	18	nS
	tf		1.2	2.8	

### **Typical Performance Characteristics**







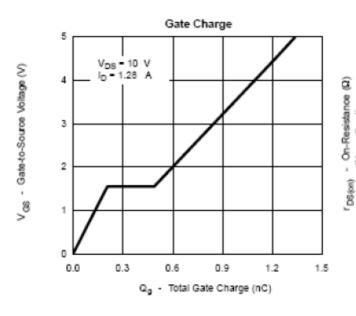


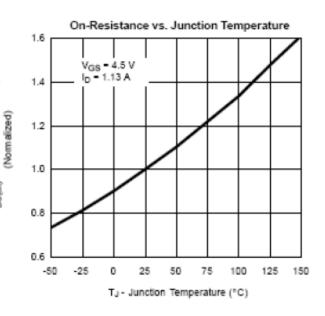


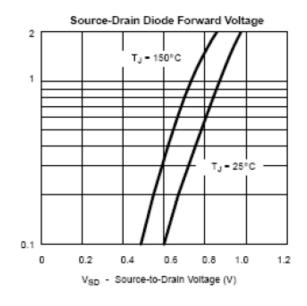
Is - Source Current (A)

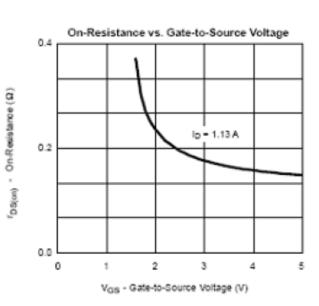
## ACE1551A N-Channel Enhancement Mode MOSFET

## **Typical Performance Characteristics**



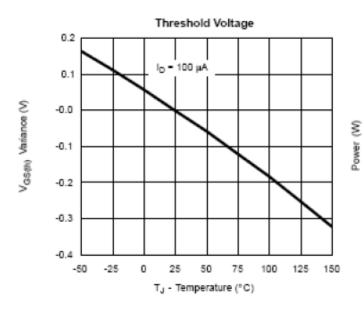


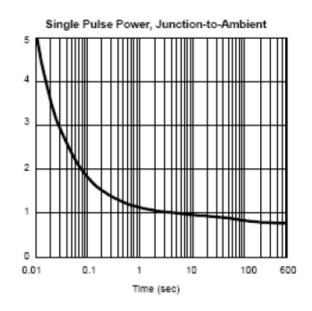


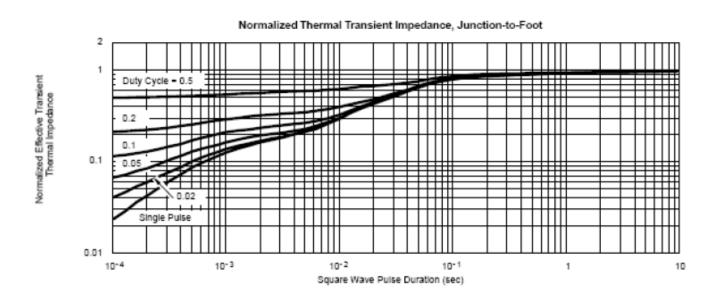




## **Typical Performance Characteristics**



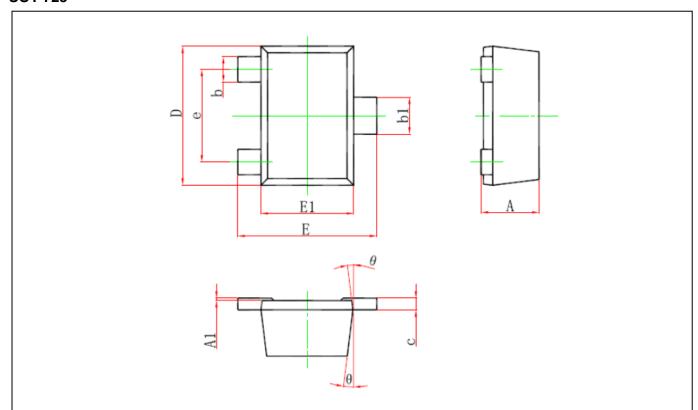






# **Packing Information**

### SOT-723



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α		0.500		0.020	
A1	0.000	0.050	0.000	0.002	
b	0.170	0.270	0.007	0.011	
b1	0.270	0.370	0.011	0.015	
С		0.150		0.006	
D	1.150	1.250	0.045	0.049	
E	1.150	1.250	0.045	0.049	
E1	0.750	0.850	0.030	0.033	
е	0.800TYP.		0.031TYP.		
θ	7° REF.		7° REF.		



#### Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD. As sued herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and shoes failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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