

FAST RECOVERY RECTIFIERS

VOLTAGE RANGE: 100 --- 600 V

CURRENT: 16 A

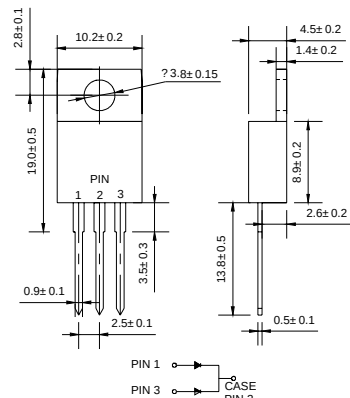
FEATURES

- ◇ Low cost
- ◇ Diffused junction
- ◇ Low leakage
- ◇ Low forward voltage drop
- ◇ High current capability
- ◇ Easily cleaned with Freon, Alcohol, Isopropanol and similar solvents
- ◇ The plastic material carries U/L recognition 94V-0

MECHANICAL DATA

- ◇ Case: JEDEC TO-220AB, molded plastic
- ◇ Terminals: solderable per MIL- STD-202, Method 208
- ◇ Polarity: Color band denotes cathode
- ◇ Weight: 0.071 ounces, 2.006 grams
- ◇ Mounting position: Any

TO-220AB



Dimensions in millimeters

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase,half wave,60 Hz,resistive or inductive load. For capacitive load,derate by 20%.

		FR 1610C	FR 1620C	FR 1640C	FR 1660C	UNITS
Maximum recurrent peak reverse voltage	V_{RRM}	100	200	400	600	V
Maximum RMS voltage	V_{RMS}	70	140	280	420	V
Maximum DC blocking voltage	V_{DC}	100	200	400	600	V
Maximum average forward rectified current @T _A =75°C	$I_{F(AV)}$	16				A
Peak forward surge current 8.3ms single half-sine-wave superimposed on rated load @T _J =125°C	I_{FSM}	200				A
Maximum instantaneous forward voltage @ 8.0 A	V_F	1.3				V
Maximum reverse current @T _A =25°C at rated DC blocking voltage @T _A =100°C	I_R	10 150				μA
Maximum reverse recovery time (Note1)	t_{rr}	150			250	ns
Typical junction capacitance (Note2)	C_J	65				pF
Typical thermal resistance (Note3)	$R_{\theta JA}$	3.0				°C/W
Operating junction temperature range	T _J	- 55---- +150				°C
Storage temperature range	T _{STG}	- 55---- + 150				°C

NOTE:1. Measured with $I_F=0.5A$, $I_R=1A$, $I_{rr}=0.25A$.

2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

3. Thermal resistance from junction to ambient.

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FIG.1 – FORWARD DERATING CURVE

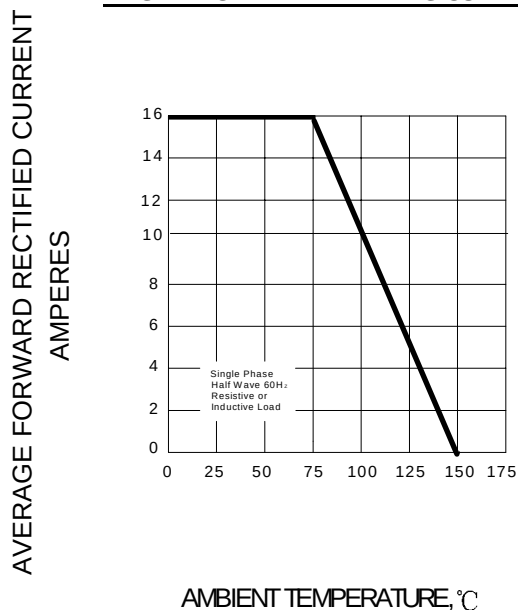


FIG.2-PEAK FORWARD SURGE CURRENT

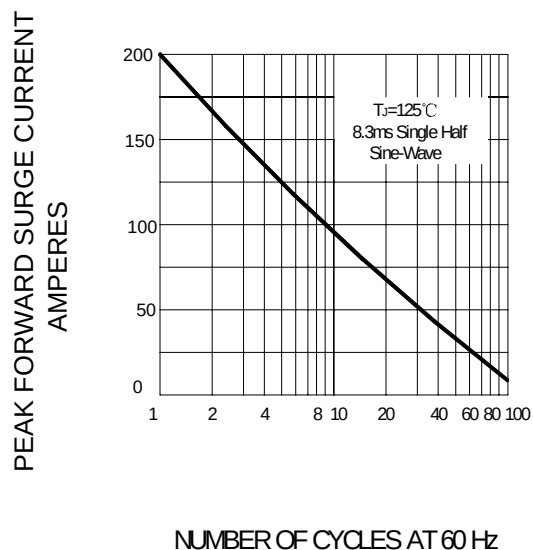


FIG.3-TYPICAL JUNCTION CAPACITANCE

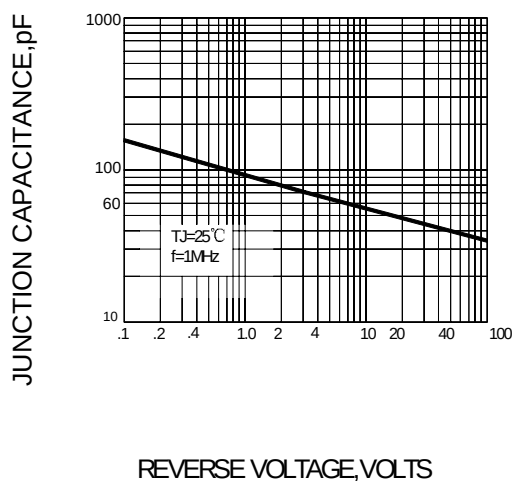


FIG.4 –TYPICAL FORWARD CHARACTERISTIC

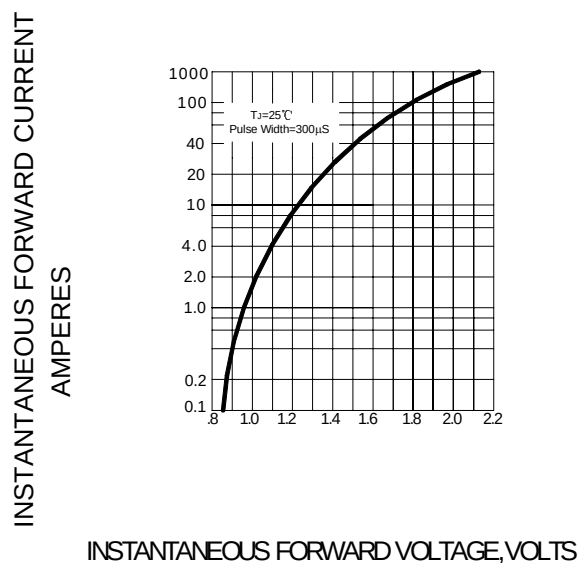


FIG5 – REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

