

ULTRA FAST RECTIFIERS

VOLTAGE RANGE: 100 --- 800 V
CURRENT: 10 A

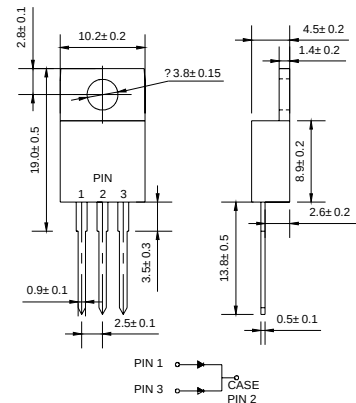
FEATURES

- ◇ Low cost
- ◇ Diffused junction
- ◇ Glass passivated chip junction
- ◇ Low forward voltage drop
- ◇ High current capability
- ◇ Easily cleaned with freon, alcohol, Isopropanol and similar solvents
- ◇ The plastic material carries U/L recognition 94V-0

MECHANICAL DATA

- ◇ Case: JEDEC TO-220AB
- ◇ Terminals: Solderable per MIL-STD-202, Method 208
- ◇ Polarity: As marked
- ◇ Weight: 0.071 ounces, 2.006 gram
- ◇ Mounting position: Any

TO-220AB



Dimensions in millimeters

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate by 20%.

		UF 1010C	UF 1020C	UF 1030C	UF 1040C	UF 1060C	UF 1080C	UNITS
Maximum recurrent peak reverse voltage	V _{RRM}	100	200	300	400	600	800	V
Maximum RMS voltage	V _{RMS}	70	140	210	280	420	560	V
Maximum DC blocking voltage	V _{DC}	100	200	300	400	600	800	V
Maximum average forward rectified current @T _C =100 °C	I _{F(AV)}	10						A
Peak forward surge current 8.3ms single half-sine-wave superimposed on rated load @T _J =125°C	I _{FSM}	125						A
Maximum instantaneous forward voltage @ 5.0A	V _F	1.0		1.3		1.7		V
Maximum reverse current @T _A =25°C at rated DC blocking voltage @T _A =125°C	I _R	10 500						μ A
Maximum reverse recovery time (Note1)	t _{rr}	50				100		ns
Typical junction capacitance (Note2)	C _J	80				50		pF
Typical thermal resistance (Note3)	R _{θJA}	60						°C/W
Operating junction temperature range	T _J	- 55 ----- + 150						°C
Storage temperature range	T _{STG}	- 55 ----- + 150						°C

NOTE: 1. Measured with $I_F=0.5\text{A}$, $I_R=1\text{A}$, $t_{rr}=0.25\text{A}$.

2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

3. Thermal resistance junction to ambient

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FIG.1 -- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC

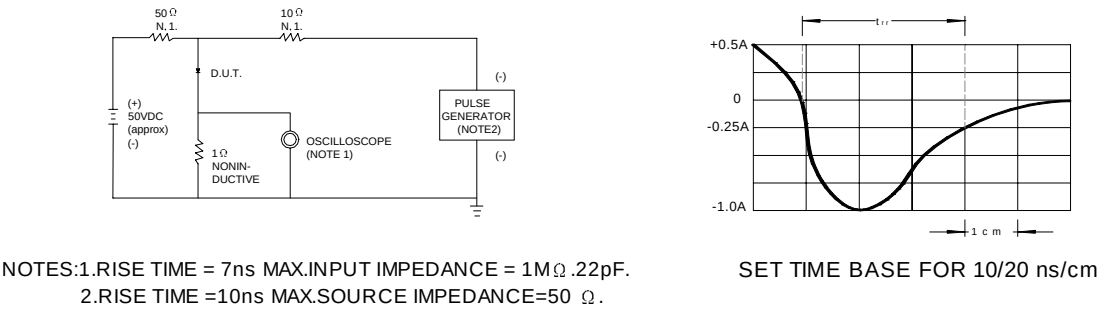


FIG.2 -- TYPICAL FORWARD CHARACTERISTIC

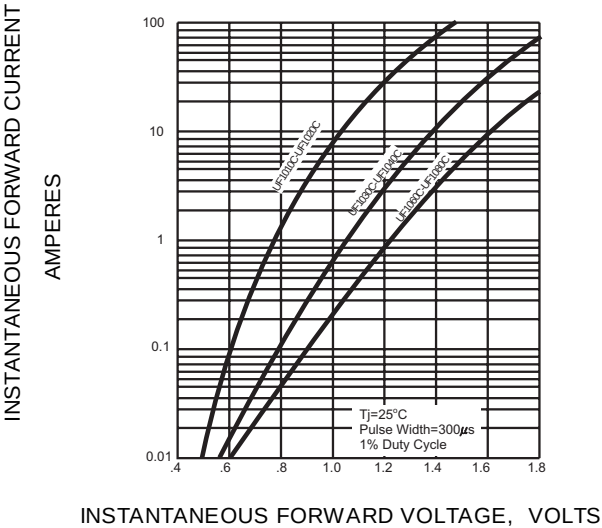


FIG.3 -- FORWARD DERATING CURVE

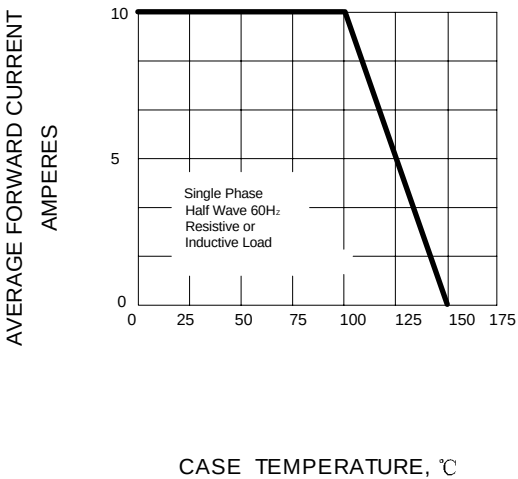


FIG.4 -- TYPICAL JUNCTION CAPACITANCE

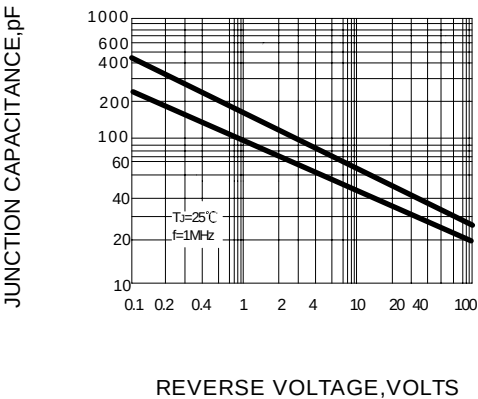


FIG.5 -- PEAK FORWARD SURGE CURRENT

