

Features:

- Isolated mounting base 2500V~
- Pressure contact technology with Increased power cycling capability
- Space and weight savings

Typical Applications:

- AC/DC Motor drives
- Various rectifiers
- DC supply for PWM inverter

$I_{T(AV)}$ **1000A**
 V_{DRM}/V_{RRM} **600~1800V**
 I_{TSM} **28 KA**
 I^2t **3920 10³A²S**



SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T _j (°C)	VALUE			UNIT
				Min	Type	Max	
I _{T(AV)}	Mean on-state current	180° half sine wave 50Hz Single side cooled, T _c =85°C	125			1000	A
I _{T(RMS)}	RMS on-state current		125			1570	A
V _{DRM} V _{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	V _{DRM} &V _{RRM} tp=10ms V _{DSM} &V _{RSM} = V _{DRM} &V _{RRM} +100V	125	600		1800	V
I _{DRM} I _{RRM}	Repetitive peak current	V _{DM} = V _{DRM} V _{RM} = V _{RRM}	125			55	mA
I _{TSM}	Surge on-state current	10ms half sine wave, V _R =0.6V _{RRM}	125			28.0	KA
I ² t	I ² T for fusing coordination					3920	A ² s*10 ³
V _{TO}	Threshold voltage		125			0.80	V
r _T	On-state slop resistance					0.15	mΩ
V _{TM}	Peak on-state voltage	I _{TM} =3000A	25			1.96	V
dv/dt	Critical rate of rise of off-state voltage	V _{DM} =67%V _{DRM}	125			800	V/μs
di/dt	Critical rate of rise of on-state current	I _{TM} =2000A, Gate source 1.5A t _r ≤0.5μs Repetitive	125			100	A/μs
I _{GT}	Gate trigger current			30		200	mA
V _{GT}	Gate trigger voltage	V _A =12V, I _A =1A	25	1.0		3.0	V
I _H	Holding current			20		200	mA
V _{GD}	Non-trigger gate voltage	V _{DM} =67%V _{DRM}	125	0.2			V
R _{th(j-c)}	Thermal resistance Junction to case	Single side cooled				0.034	°C /W
R _{th(c-h)}	Thermal resistance case to heatsink	Single side cooled				0.020	°C /W
V _{iso}	Isolation voltage	50Hz, R.M.S, t=1min, I _{iso} :1mA(MAX)		2500			V
F _m	Thermal connection torque(M12)				12		N·m
	Mounting torque(M8)				12		N·m
T _{stg}	Stored temperature			-40		125	°C
W _t	Weight				3800		g
Outline	412F3						

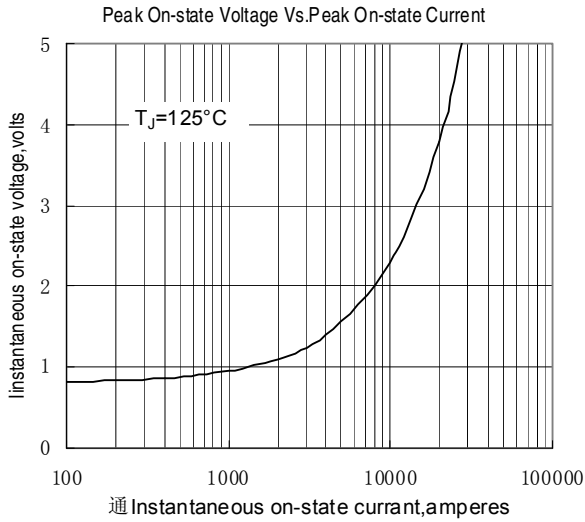


Fig.1

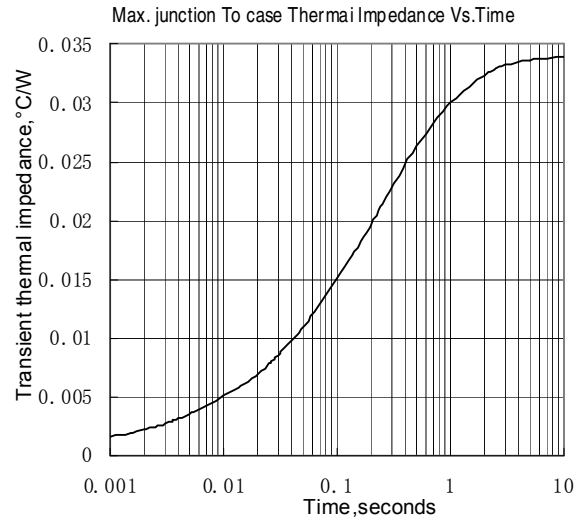


Fig.2

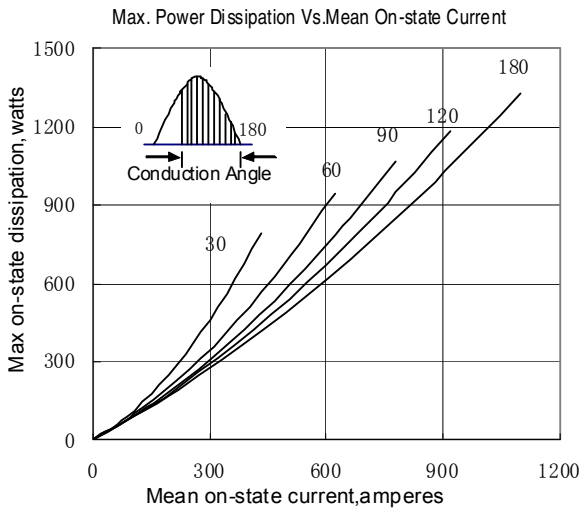


Fig.3

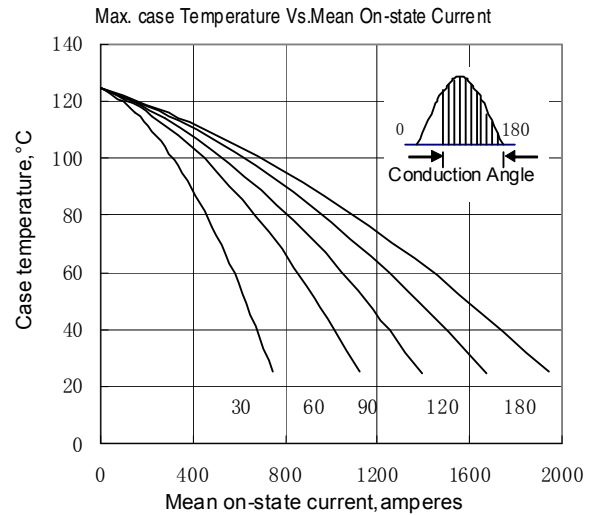


Fig.4

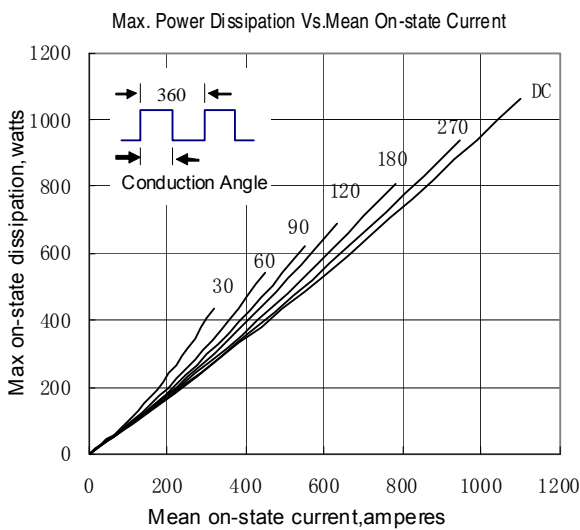


Fig.5

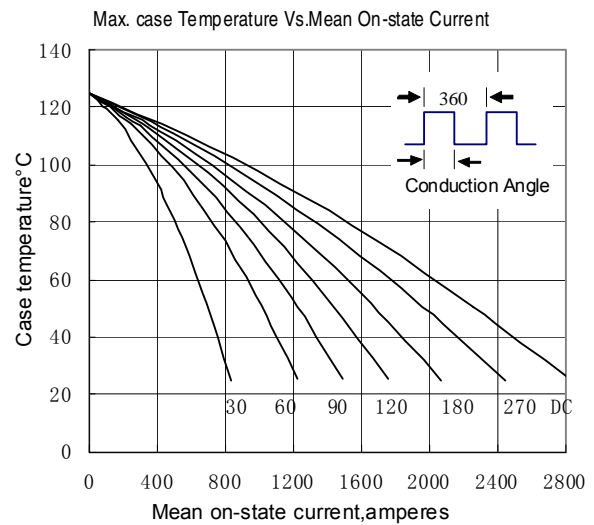


Fig.6

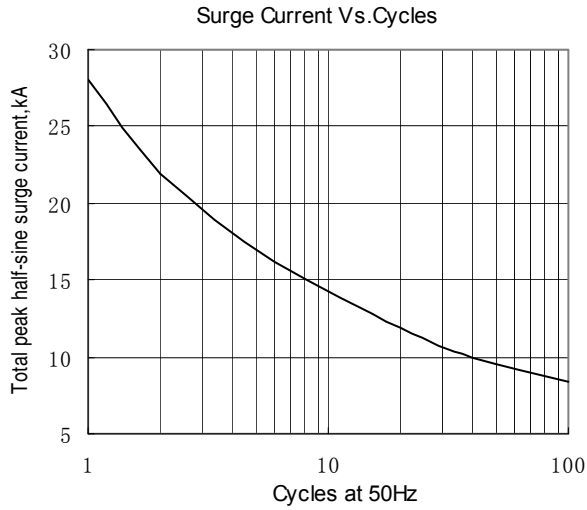


Fig.7

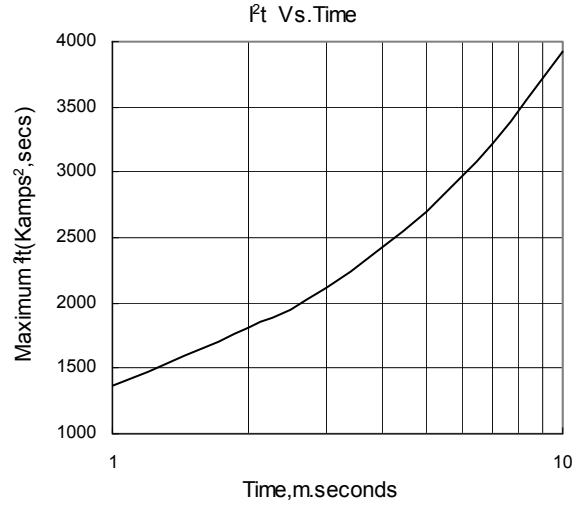


Fig.8

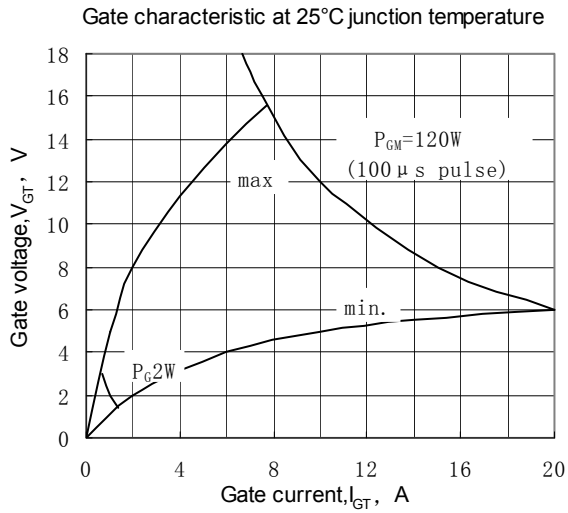


Fig.9

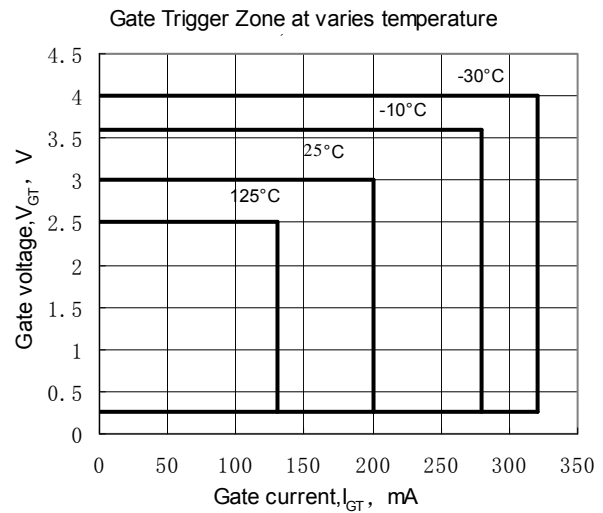


Fig.10

Outline:

