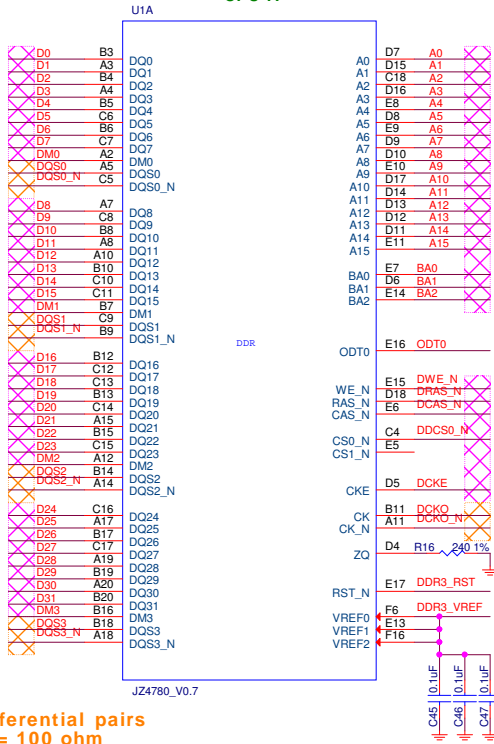
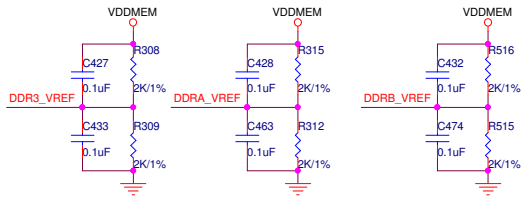
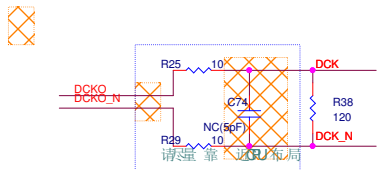


Equilong BUS
Z0= 50 ohm

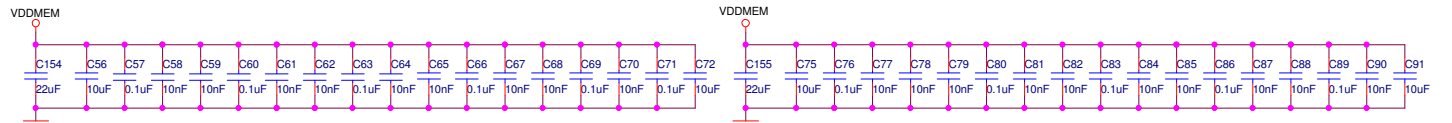
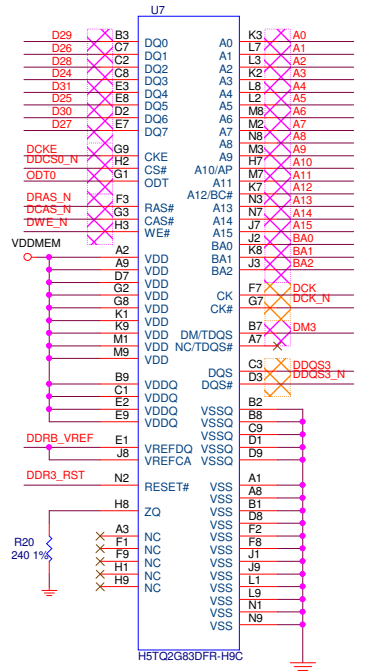
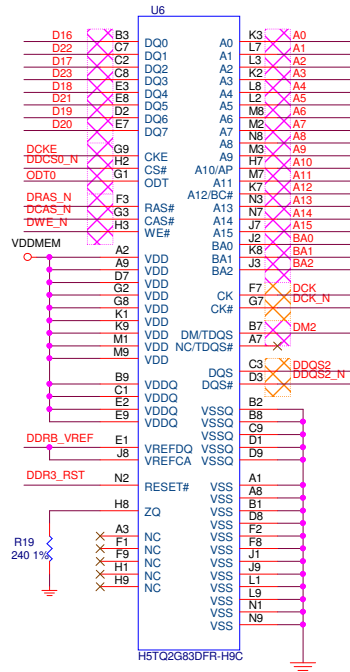
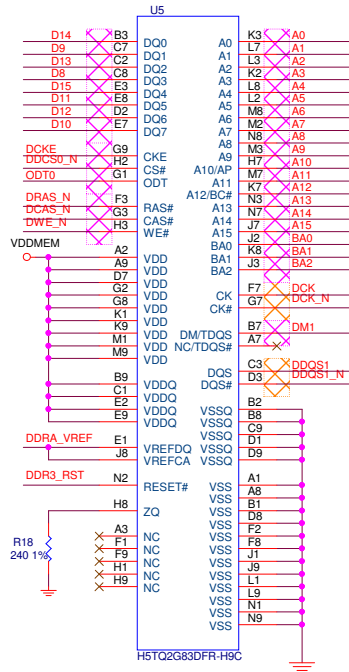
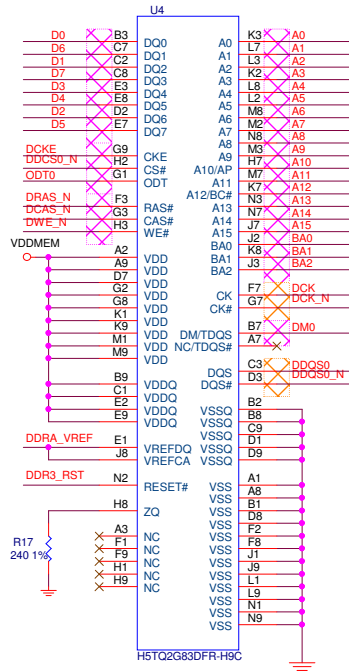
CPU-A



Differential pairs
Z0= 100 ohm



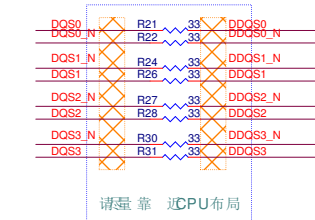
The trace DDR_VREF is 20 mils wide at least.



MEM Power Decoupling Capacitors
Place near The Power Pins as Close as Possible

Layout时尽量靠近CPU布局,并给DDR3电源管脚放置一个去耦电容.

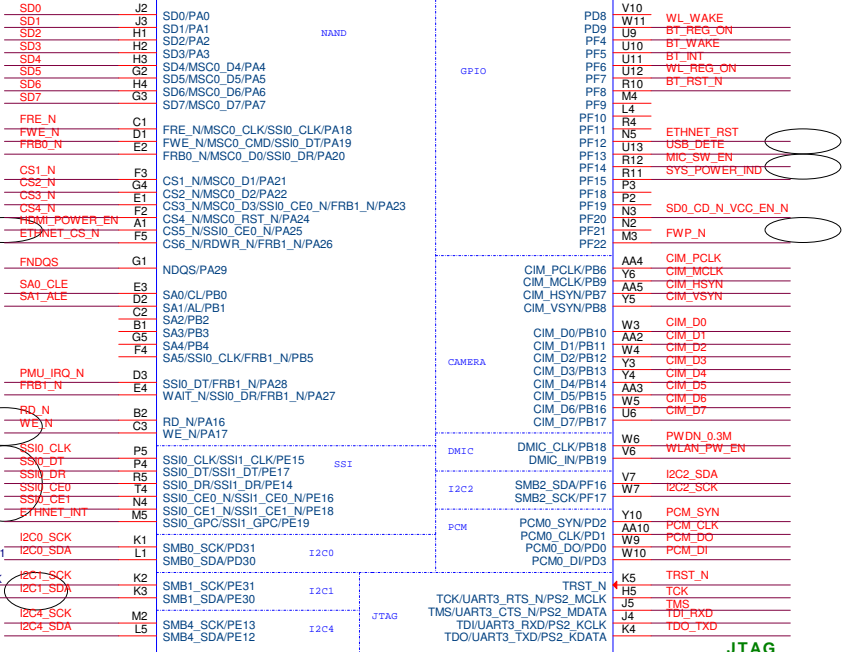
DDR Power Decoupling Capacitors



请尽量靠近CPU布局

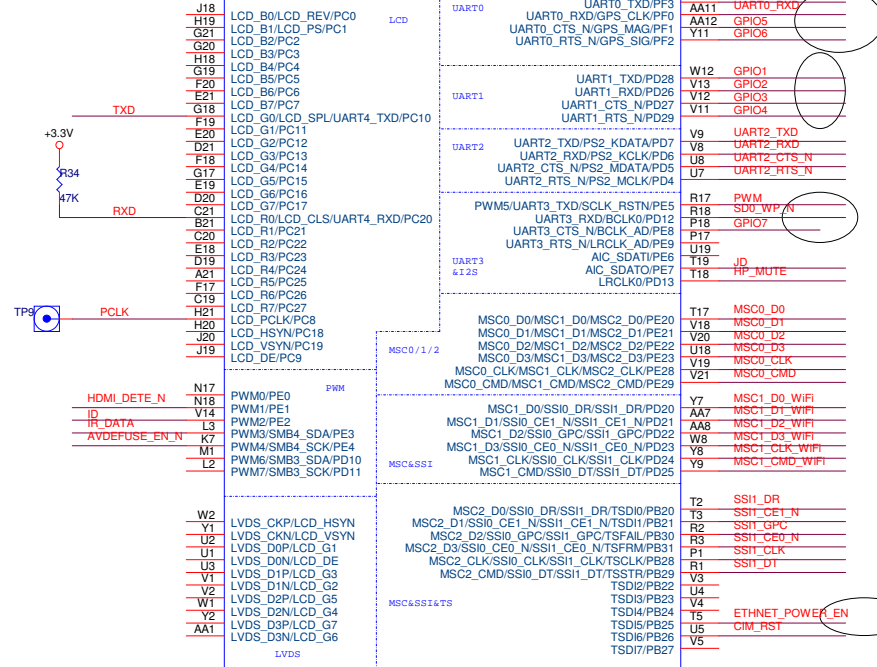
INGENIC SEMICONDUCTOR CO.,LTD		
Title C120_JZ4780		
Size A3	Document Number MEMORY_DDR3x4	Rev V1.0
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U1B CPU-B

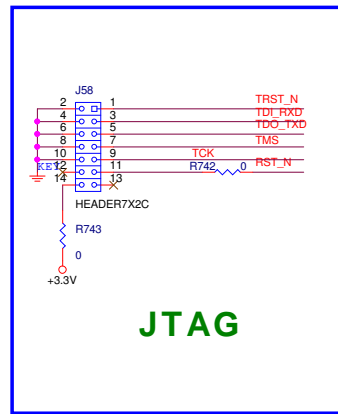
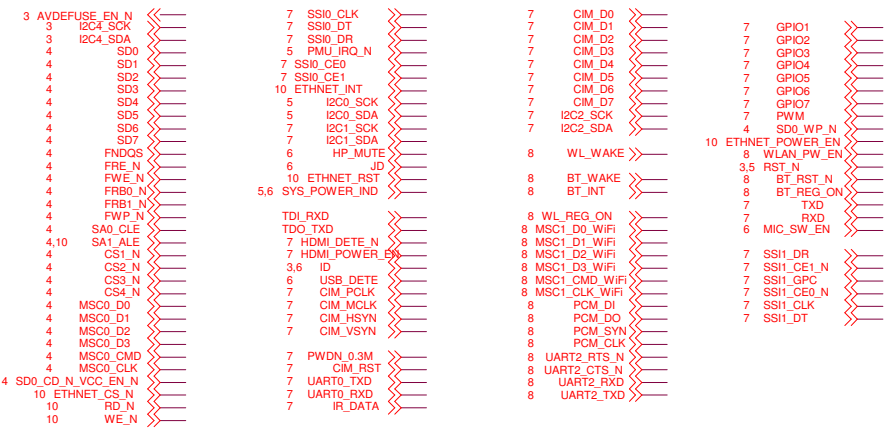


JZ4780_V0.7

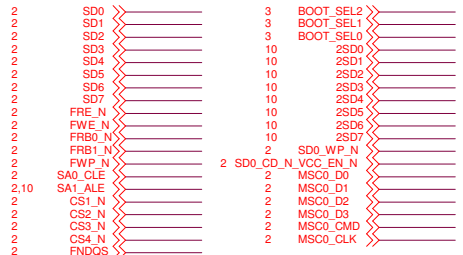
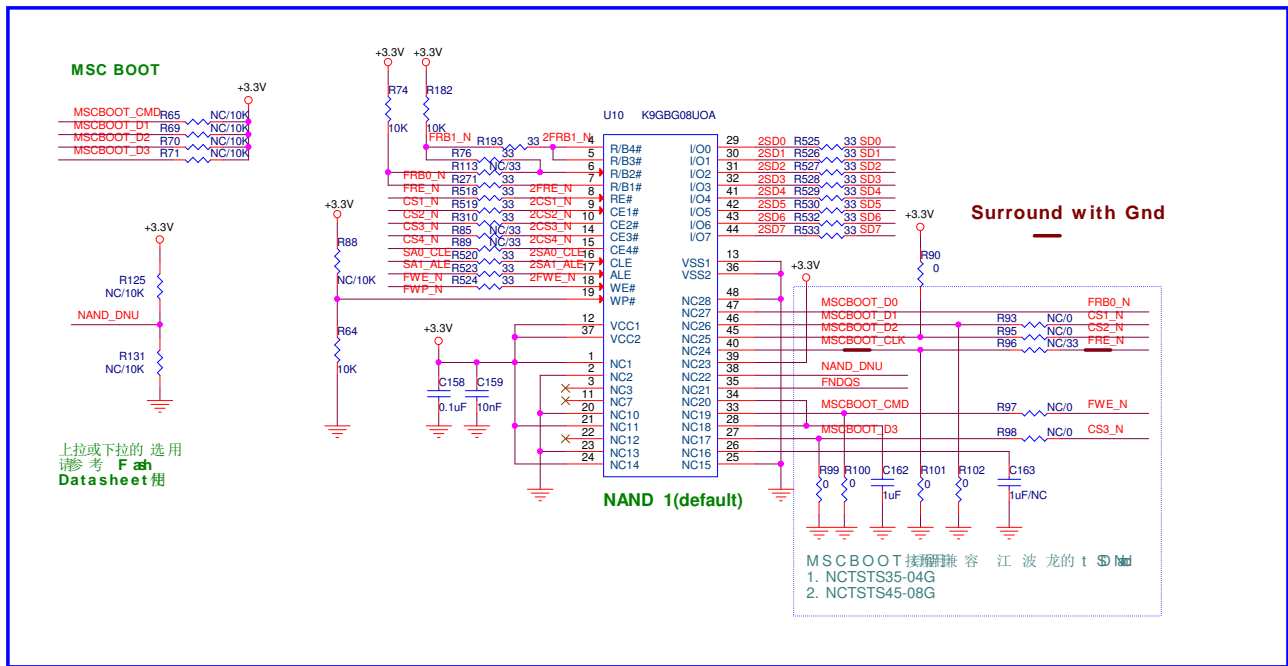
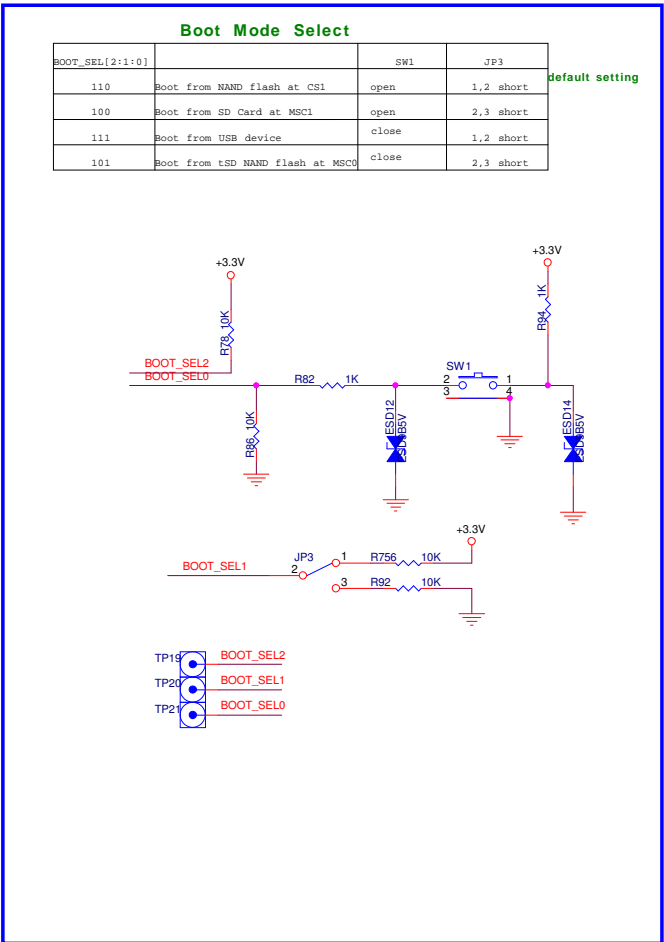
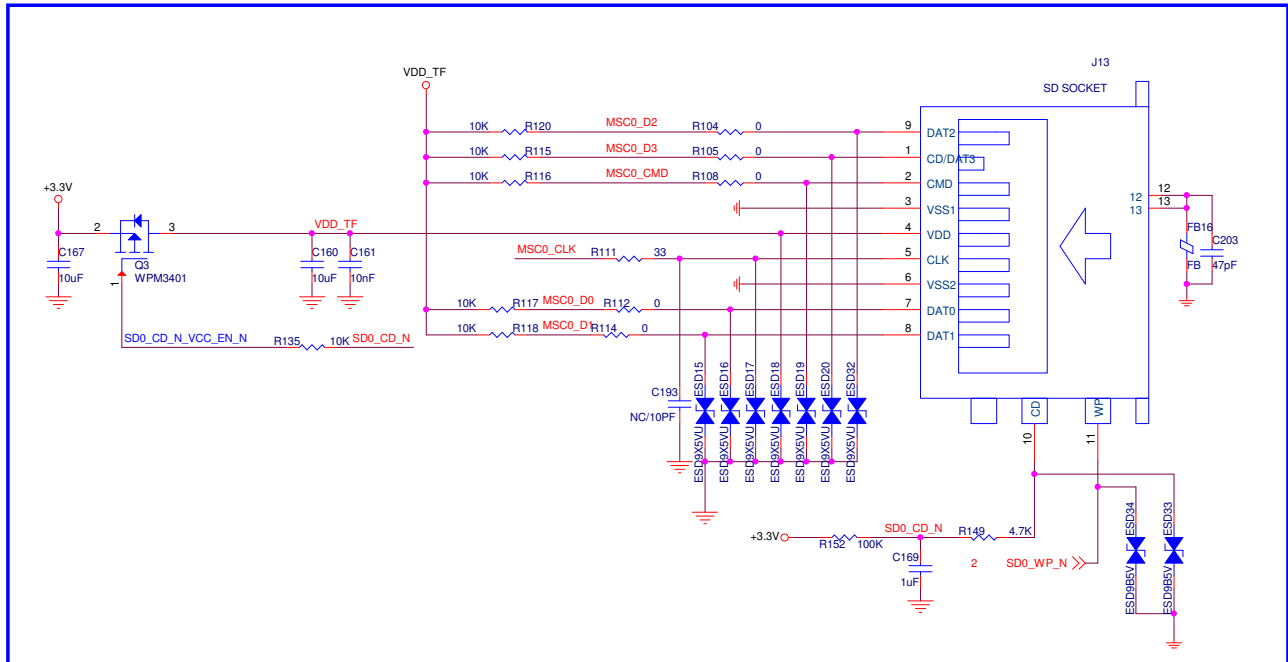
U1C CPU-C

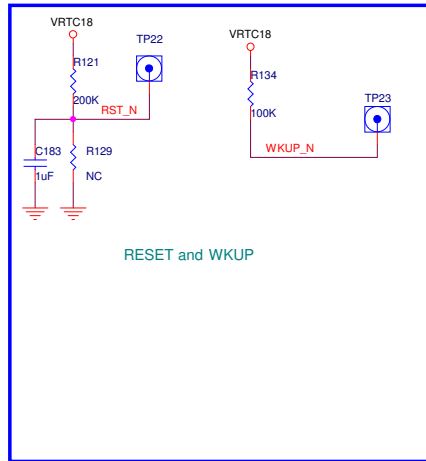
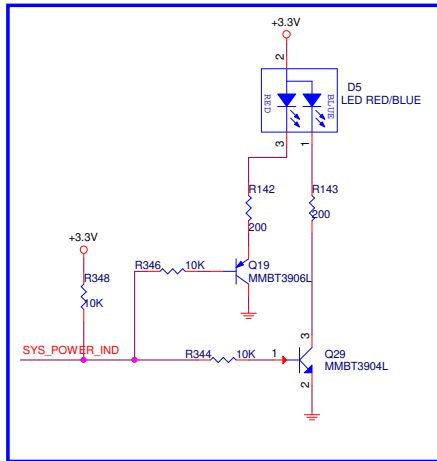
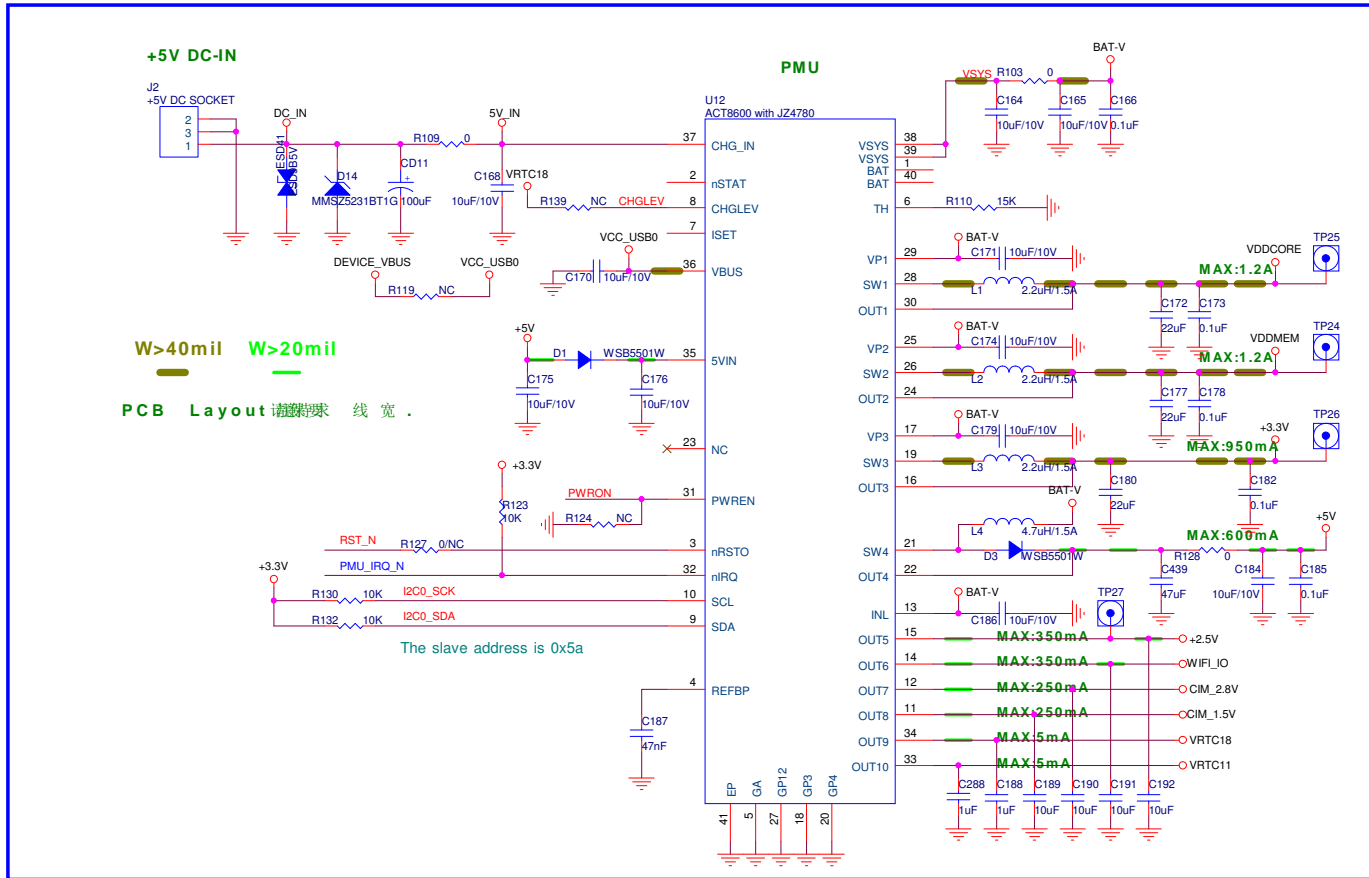


JZ4780_V0.7

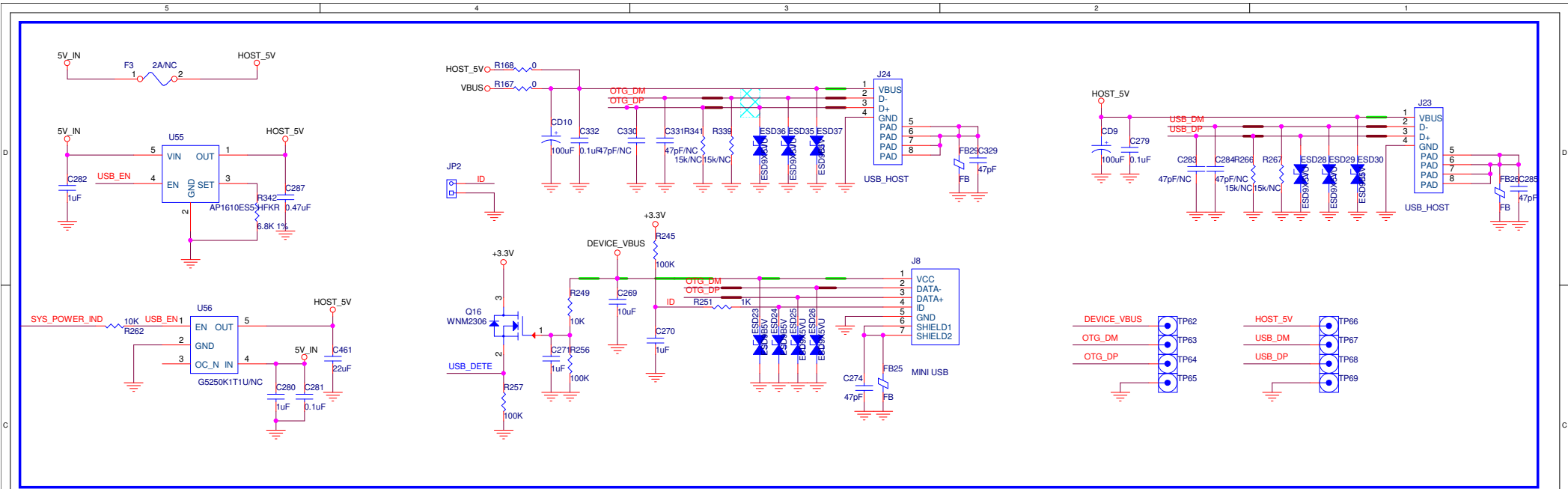


JTAG

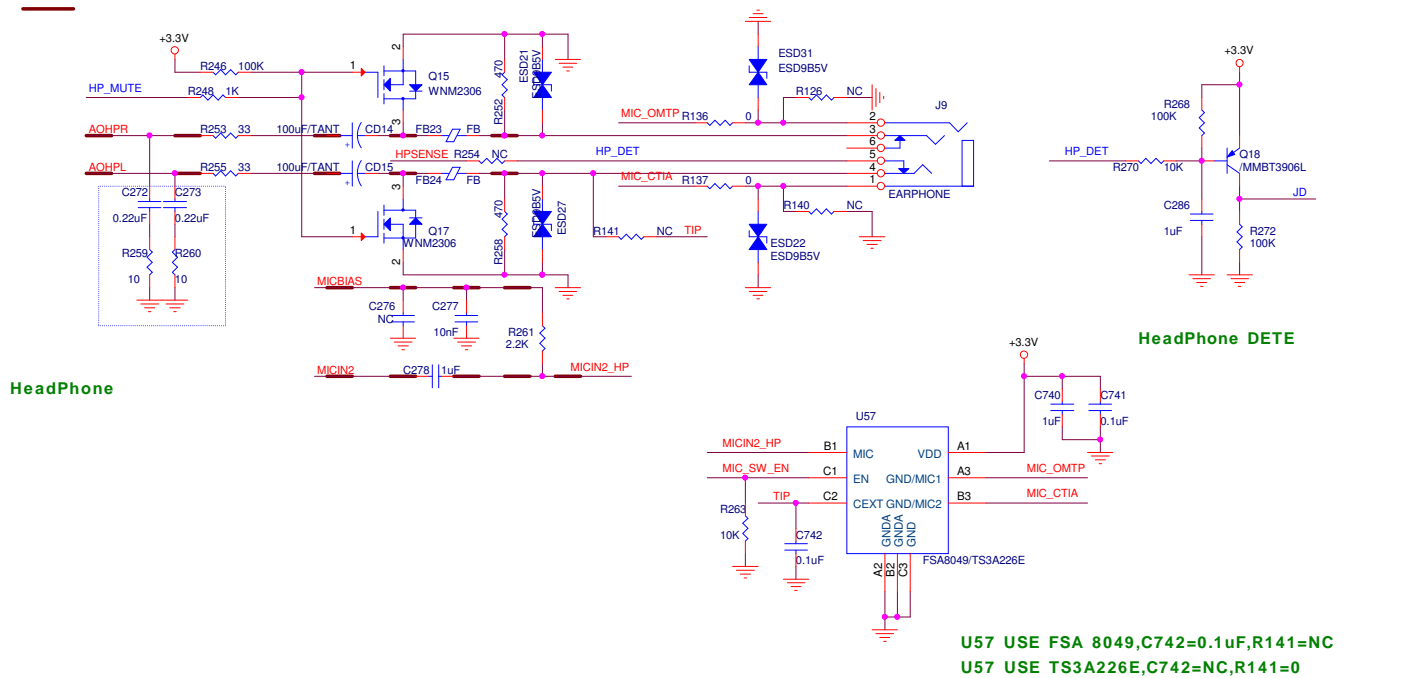




INGENIC SEMICONDUCTOR CO.,LTD		
Title	C120_JZ4780	
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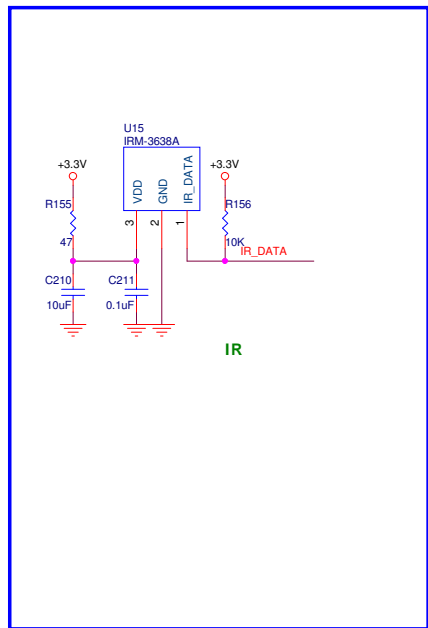
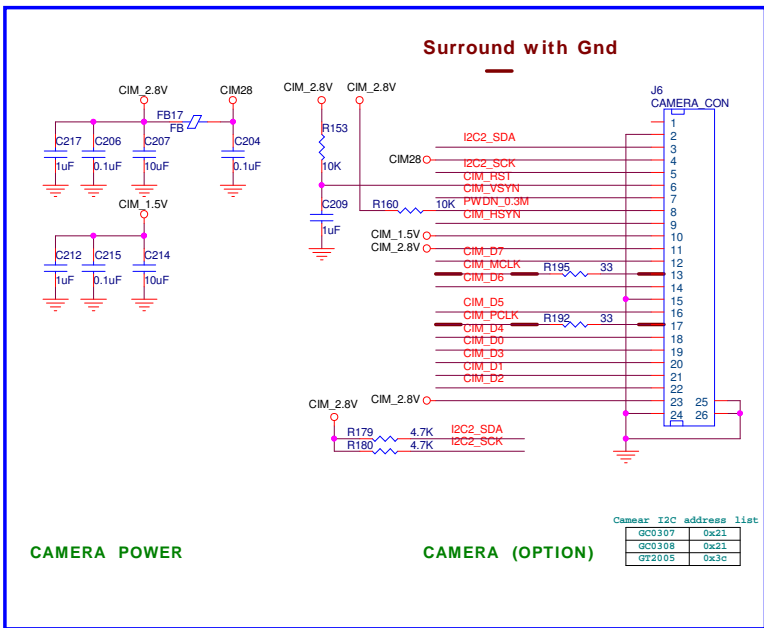
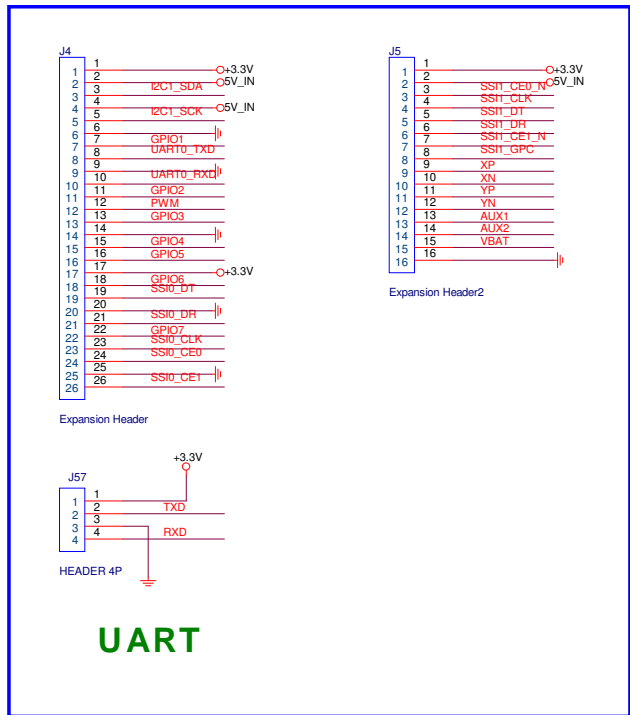
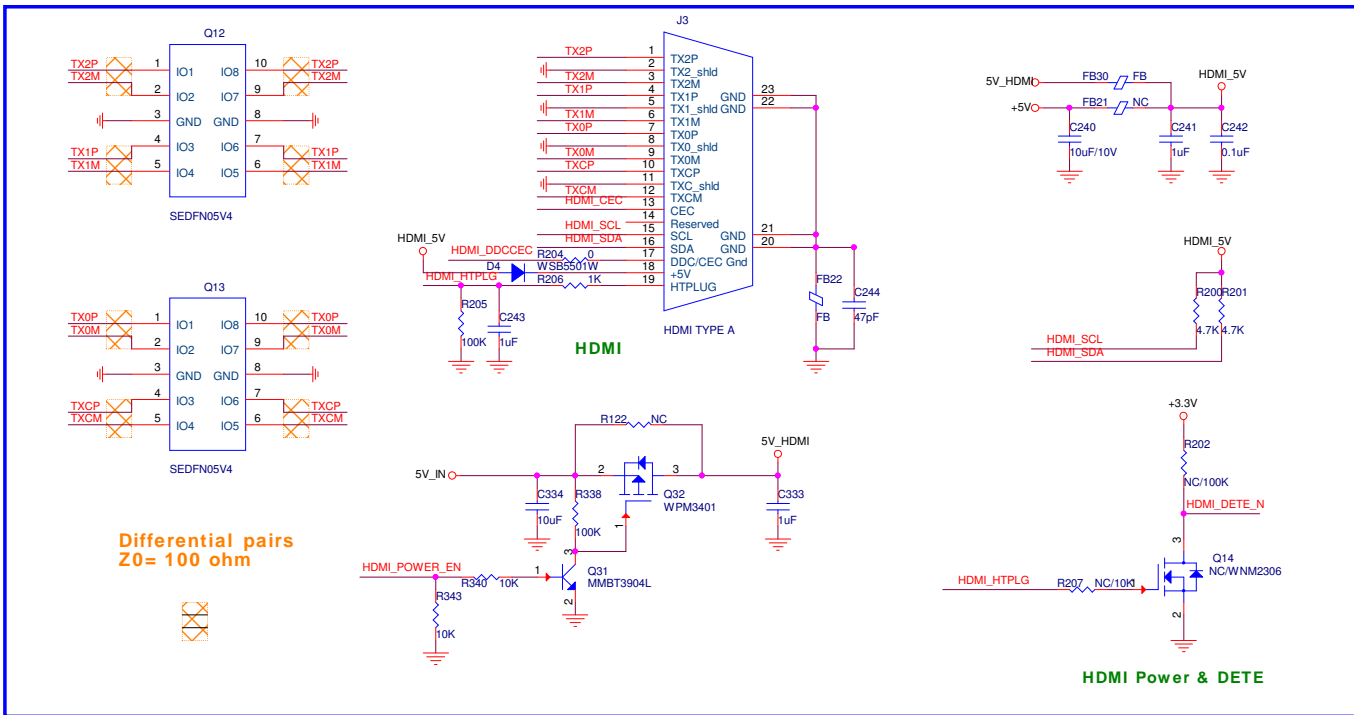
Surround with Gnd



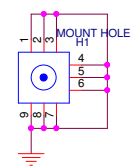
- 3 MICBIAS
- 3 ACHPR
- 3 AOHPL
- 3 HPSNSE
- 3 HP_MUTE
- 2 JD
- 2 USB_DET
- 3 OTG_DM
- 3 OTG_DP
- 2.5 ID
- 3 SYS_POWER_IND
- 3 USB_DM
- 3 USB_DP
- 3 MICIN2
- 2 MIC_SW_EN

U57 USE FSA 8049,C742=0.1uF,R141=NC
 U57 USE TS3A226E,C742=NC,R141=0

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- 2 SSIO_CLK
- 2 SSIO_DT
- 2 SSIO_CE0
- 2 SSIO_CE1
- 2 TDL_RXD
- 2 TDO_TXD
- 2 PWM
- 2 UART0_TXD
- 2 UART0_RXD
- 2 GPIO1
- 2 GPIO2
- 2 GPIO3
- 2 GPIO4
- 2 GPIO5
- 2 GPIO6
- 2 GPIO7
- 2 I2C1_SCK
- 2 I2C1_SDA
- 3 TXGM
- 3 TXCP
- 3 TX0M
- 3 TX1P
- 3 TX1M
- 3 TX2P
- 3 TX2M
- 3 HDMI_CEC
- 3 HDMI_SCL
- 3 HDMI_SDA
- 3 HDMI_HTPLG
- 3 HDMI_DDCCEC
- 2 HDMI_DETE_N
- 2 HDMI_POWER_EN
- 2 PWDN_0.3M
- 2 TXD
- 2 RXD
- 2 CIM_PCLK
- 2 CIM_MCLK
- 2 CIM_HSYN
- 2 CIM_VSYN
- 2 CIM_D0
- 2 CIM_D1
- 2 CIM_D2
- 2 CIM_D3
- 2 CIM_D4
- 2 CIM_D5
- 2 CIM_D6
- 2 CIM_D7
- 2 I2C2_SCK
- 2 I2C2_SDA
- 2 CIM_RST
- 2 IR_DATA
- 2 SS11_DR
- 2 SS11_CE1_N
- 2 SS11_GPC
- 2 SS11_CE0_N
- 2 SS11_CLK
- 2 SS11_DT
- 3 XN
- 3 YN
- 3 XP
- 3 YP
- 3 VBAT
- 3 AUX1
- 3 AUX2

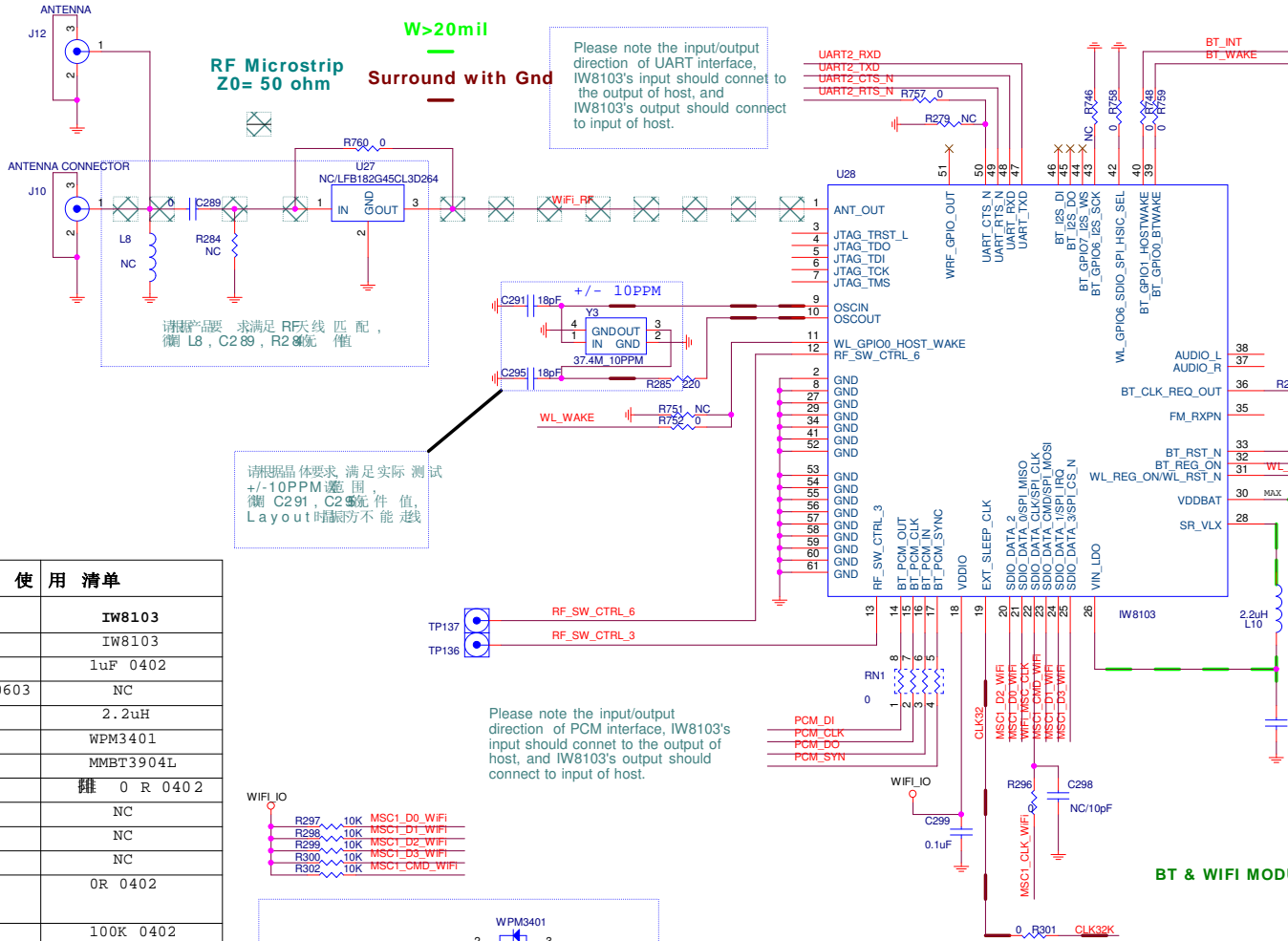


- 2 MSC1_D0_WIFI >>>
- 2 MSC1_D1_WIFI >>>
- 2 MSC1_D2_WIFI >>>
- 2 MSC1_D3_WIFI >>>
- 2 MSC1_CMD_WIFI >>>
- 2 MSC1_CLK_WIFI >>>
- 3 MSC1_CLK32K >>>

- 2 WL_REG_ON >>>
- 2 WLAN_PW_EN >>>

- 2 PCM_DI >>>
- 2 PCM_DO >>>
- 2 PCM_SYN >>>
- 2 PCM_CLK >>>
- 2 BT_WAKE >>>
- 2 WL_WAKE >>>
- 2 BT_INT >>>
- 2 UART2_RTS_N >>>
- 2 UART2_CTS_N >>>
- 2 UART2_RXD >>>
- 2 UART2_TXD >>>

- 2 BT_RST_N >>>
- 2 BT_REG_ON >>>



RF Microstrip
Z0= 50 ohm
Surround with Gnd
W>20mil

Please note the input/output direction of UART interface, IW8103's input should connect to the output of host, and IW8103's output should connect to input of host.

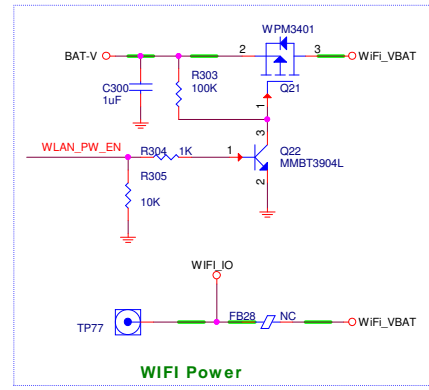
请根据晶振要求满足实际测试
 +/-10PPM范围
 晶振 C291, C295 元件值
 Layout 阻抗不能差

请根据晶振要求满足实际测试
 +/-10PPM范围
 晶振 C291, C295 元件值
 Layout 阻抗不能差

Please note the input/output direction of PCM interface, IW8103's input should connect to the output of host, and IW8103's output should connect to input of host.

BT & WIFI MODULE

EXT_SLEEP_CLK 200- 1800 mV Vp p to avoid additional current consumption and degradation in FM SNR. 3.3V Vp-p maximum.



IW8101/IW8103 器件使用清单		
器件编号	IW8101	IW8103
U28	IW8101	IW8103
C300	NC	1uF 0402
FB28	75@100MHz 0603	NC
L10	1.5uH	2.2uH
Q21	NC	WPM3401
Q22	NC	MMBT3904L
RN1	NC	0R 0402
R279	100K 0402	NC
R746	0R 0402	NC
R751	0R 0402	NC
R757,R748,R759, R752,R753,R754	NC	0R 0402
R303	NC	100K 0402
R305	NC	10K 0402
R304	NC	1K 0402
U27	U27 LFB182G45CL3D264	R760,0
Y3	26MHz +/-10ppm	37.4MHz +/-10ppm

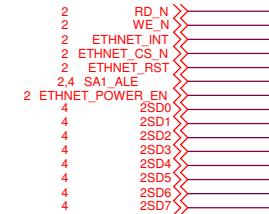
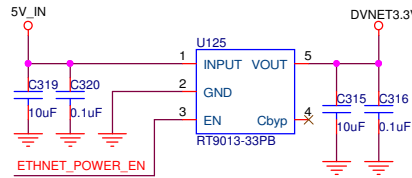
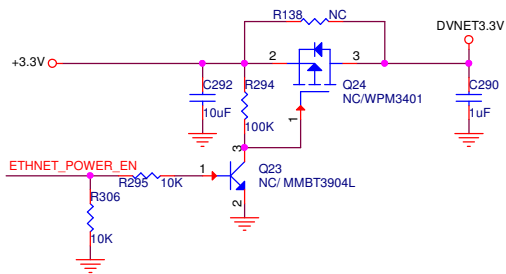
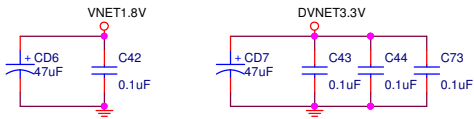
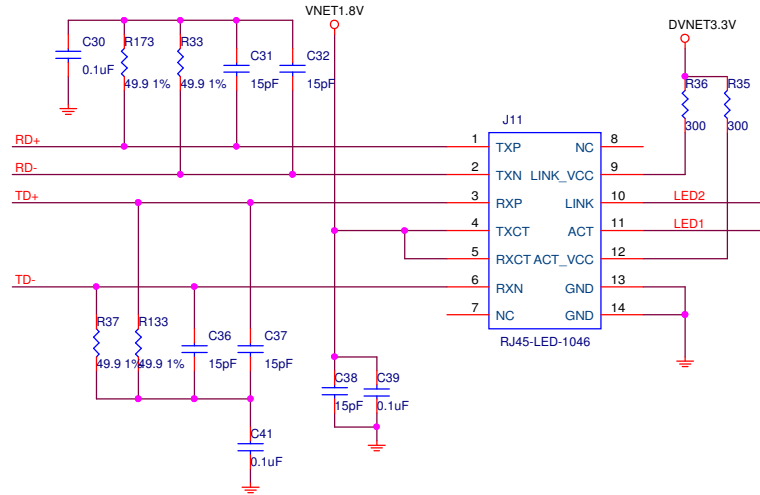
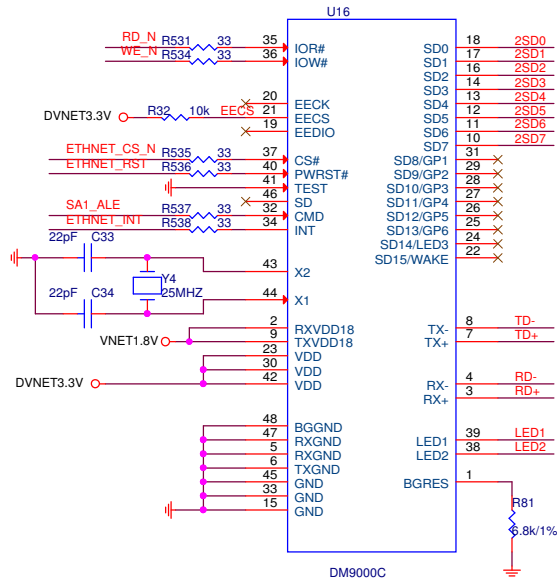
备注:
 1. 本图参考设计为 IW8101 设计。
 2. 清单内无标注器件值为 WIFI 共用物料 请按原理图标注值进行选择。
 3. 如果使用 IW8103, 则 WIFI 电压应调整为 2.8V 或 3.0V, 同时系统电压也相应调整为 2.8V 或 3.0V

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Title: **CI20_JZ4780**

Size: A3 Document Number: **WiFi-IW8101/IW8103** Rev: **V1.0**

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Data

Revision

Change

Data	Revision	Change
June 19 2014	Rev1.0	1. First Revision

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Title		
C120_JZ4780		
Size B	Document Number REVISION HISTORY	Rev V1.0
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