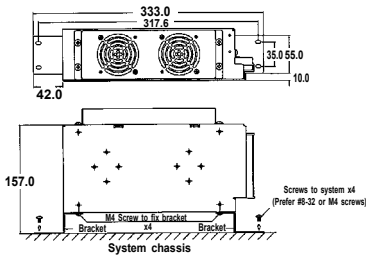
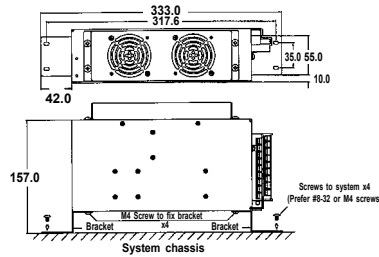


Installation Instruction

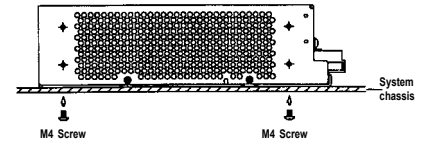
Installation (A) : AC input at lower side
Vertical assembly with bracket



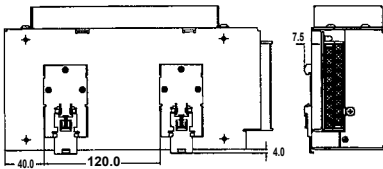
Installation (B) : AC input at upper side
Vertical assembly with bracket



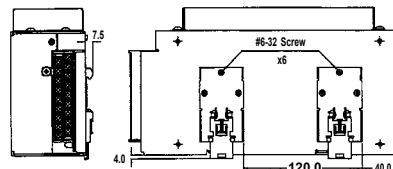
Installation (C) :
Horizontal assembly without bracket



Installation (A) : AC input at lower side
Din rail hook assembly



Installation (B) : AC input at upper side
Din rail hook assembly



Output Specifications:

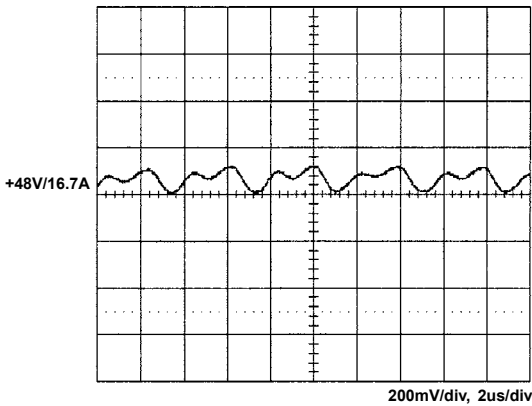
MODEL NO.	OUTPUT RAIL	LOAD				VOLTAGE ACCURACY	RIPPLE NOISE	LINE REG.	LOAD REG.	EFFICIENCY TYPICAL
		MIN.	RATED	MAX.	PEAK					
SNP-F807	+12V	0.2A	66.7A		83.5A	+11.9V~+12.1V	120mVpp	±1%	±1%	86%
SNP-F809	+24V	0.2A	33.5A		42A	+23.9V~+24.1V	240mVpp	±1%	±1%	88%
SNP-F80J	+36V	0.2A	22.2A		27.8A	+35.6V~+36.4V	360mVpp	±1%	±1%	90%
SNP-F80T	+48V	0.2A	16.7A		20.8A	+47.8V~+48.2V	480mVpp	±1%	±1%	90%

Note:

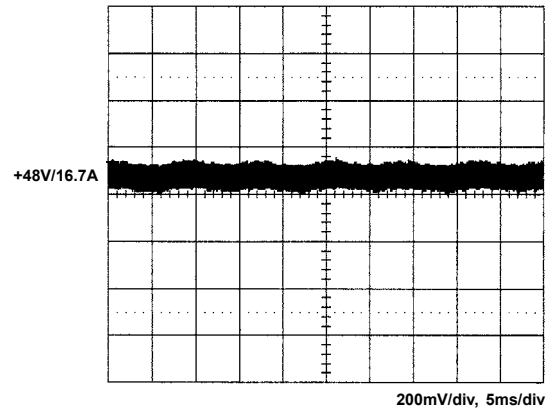
- Each output can provide up to max load separately when the power supply starts up. To exceed the max. output power continuously is not allowed.
- At factory, in 60% rated load condition, each output is checked to be within voltage accuracy.
- Line regulation is defined by changing ±10% of input voltage from nominal line at rated load.
- Load regulation is defined by changing ±40% of measured output load from 60% rated load at another output set to 60% rated load.
- Ripple & noise is measured by using 15MHz bandwidth limited oscilloscope and terminated each output with a 0.47uF capacitor at rated load and nominal line.
- Hold up time is measured from the end of the last charging pulse to the time which the main output drops down to low limit of main output at rated load and nominal line.
- Efficiency is measured at rated load and nominal line.
- EMI filter (Delta 15GEE G3E-R) has to be used for the requirement of EMI.
- Installations (A), (B) and (C) can achieve 100% rated load.

Performance for SNP-F80T:

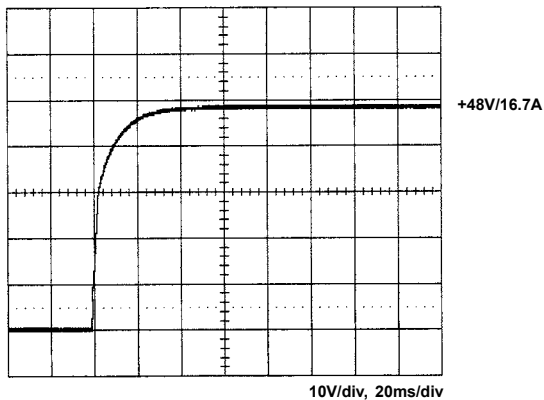
1. Switching frequency ripple



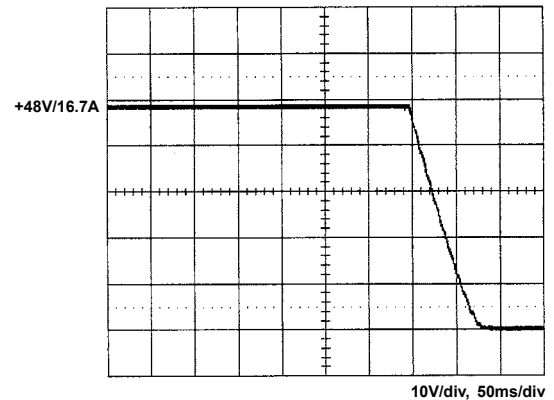
2. Line frequency ripple



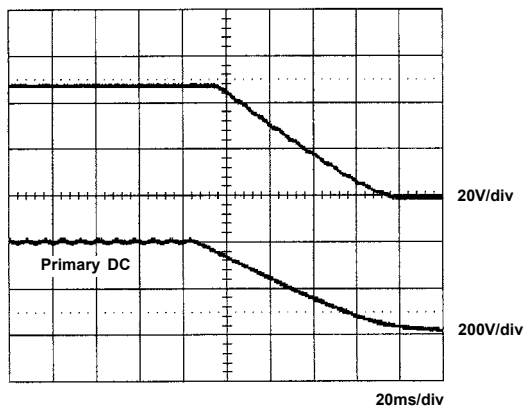
3. Output turn on wave form



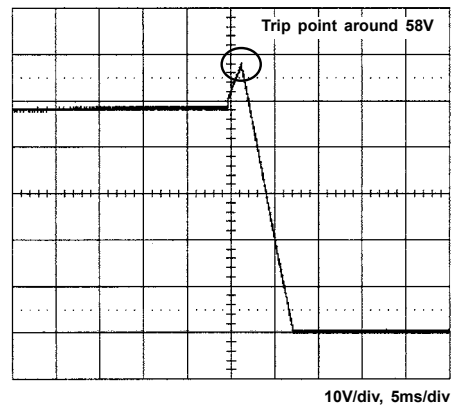
4. Output turn off wave form



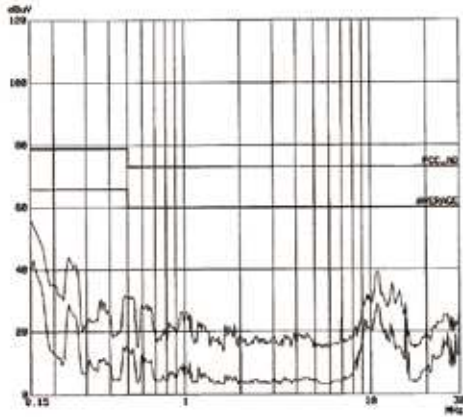
5. Hold-up time



6. Over voltage protection



7. FCC A



8. EN 55022 B

