

# BT137S-600D

4Q Triac

Rev. 03 — 28 March 2011

Product data sheet

## 1. Product profile

### 1.1 General description

Planar passivated very sensitive gate four quadrant triac in a SOT428 (DPAK) surface-mountable plastic package intended for use in general purpose bidirectional switching and phase control applications, where high sensitivity is required in all four quadrants. This very sensitive gate "series D" triac is intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

### 1.2 Features and benefits

- Direct triggering from low power drivers and logic ICs
- High blocking voltage capability
- Low holding current for low current loads and lowest EMI at commutation
- Planar passivated for voltage ruggedness and reliability
- Surface-mountable package
- Triggering in all four quadrants
- Very sensitive gate

### 1.3 Applications

- General purpose motor control
- General purpose switching

### 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{\text{DRM}}$	repetitive peak off-state voltage		-	-	600	V
$I_{\text{TSM}}$	non-repetitive peak on-state current	full sine wave; $T_{\text{j(init)}} = 25\text{ °C}$ ; $t_{\text{p}} = 20\text{ ms}$ ; see <a href="#">Figure 4</a> ; see <a href="#">Figure 5</a>	-	-	65	A
$I_{\text{T(RMS)}}$	RMS on-state current	full sine wave; $T_{\text{mb}} \leq 102\text{ °C}$ ; see <a href="#">Figure 1</a> ; see <a href="#">Figure 2</a> ; see <a href="#">Figure 3</a>	-	-	8	A

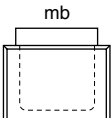
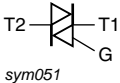


Table 1. Quick reference data ...continued

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static characteristics</b>						
$I_{GT}$	gate trigger current	$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T2+ G+;$ $T_j = 25\text{ }^\circ\text{C}$	-	2.5	5	mA
		$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T2+ G-;$ $T_j = 25\text{ }^\circ\text{C};$ see <a href="#">Figure 7</a>	-	3.5	5	mA
		$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T2- G-;$ $T_j = 25\text{ }^\circ\text{C};$ see <a href="#">Figure 7</a>	-	3.5	5	mA
		$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T2- G+;$ $T_j = 25\text{ }^\circ\text{C};$ see <a href="#">Figure 7</a>	-	6.5	10	mA
$I_H$	holding current	$V_D = 12\text{ V}; T_j = 25\text{ }^\circ\text{C};$ see <a href="#">Figure 9</a>	-	1.5	10	mA

## 2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		 sym051
2	T2	main terminal 2		
3	G	gate		
mb	T2	mounting base; main terminal 2		

SOT428 (DPAK)

## 3. Ordering information

Table 3. Ordering information

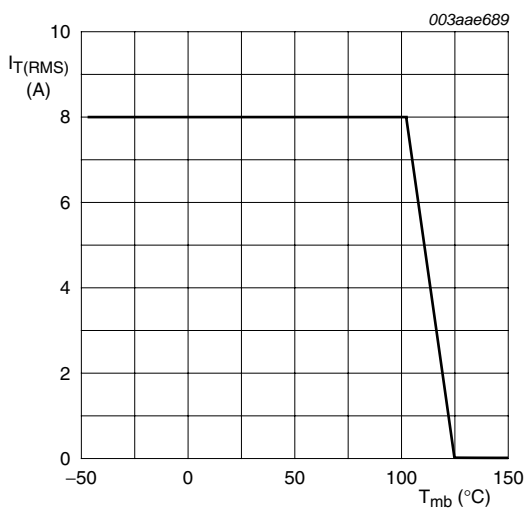
Type number	Package		Version
	Name	Description	
BT137S-600D	DPAK	plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped)	SOT428

## 4. Limiting values

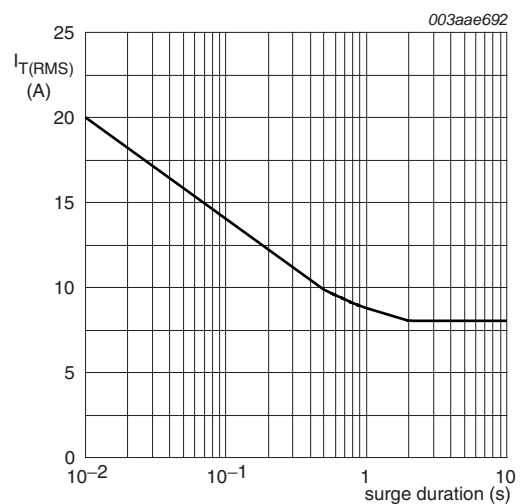
**Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		-	600	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 102\text{ }^{\circ}\text{C}$ ; see <a href="#">Figure 1</a> ; see <a href="#">Figure 2</a> ; see <a href="#">Figure 3</a>	-	8	A
$I_{TSM}$	non-repetitive peak on-state current	full sine wave; $T_{j(\text{init})} = 25\text{ }^{\circ}\text{C}$ ; $t_p = 20\text{ ms}$ ; see <a href="#">Figure 4</a> ; see <a href="#">Figure 5</a>	-	65	A
		full sine wave; $T_{j(\text{init})} = 25\text{ }^{\circ}\text{C}$ ; $t_p = 16.7\text{ ms}$	-	71	A
$I^2t$	$I^2t$ for fusing	$t_p = 10\text{ ms}$ ; sine-wave pulse	-	21	$\text{A}^2\text{s}$
$di_T/dt$	rate of rise of on-state current	$I_T = 12\text{ A}$ ; $I_G = 0.2\text{ A}$ ; $di_G/dt = 0.2\text{ A}/\mu\text{s}$ ; T2+ G+	-	50	$\text{A}/\mu\text{s}$
		$I_T = 12\text{ A}$ ; $I_G = 0.2\text{ A}$ ; $di_G/dt = 0.2\text{ A}/\mu\text{s}$ ; T2+ G-	-	50	$\text{A}/\mu\text{s}$
		$I_T = 12\text{ A}$ ; $I_G = 0.2\text{ A}$ ; $di_G/dt = 0.2\text{ A}/\mu\text{s}$ ; T2- G-	-	50	$\text{A}/\mu\text{s}$
		$I_T = 12\text{ A}$ ; $I_G = 0.2\text{ A}$ ; $di_G/dt = 0.2\text{ A}/\mu\text{s}$ ; T2- G+	-	10	$\text{A}/\mu\text{s}$
$I_{GM}$	peak gate current		-	2	A
$V_{GM}$	peak gate voltage		-	5	V
$P_{GM}$	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
$T_{stg}$	storage temperature		-40	150	$^{\circ}\text{C}$
$T_j$	junction temperature		-	125	$^{\circ}\text{C}$



**Fig 1. RMS on-state current as a function of mounting base temperature; maximum values**



$f = 50\text{ Hz}$   
 $T_{mb} \leq 102\text{ }^{\circ}\text{C}$

**Fig 2. RMS on-state current as a function of surge duration; maximum values**

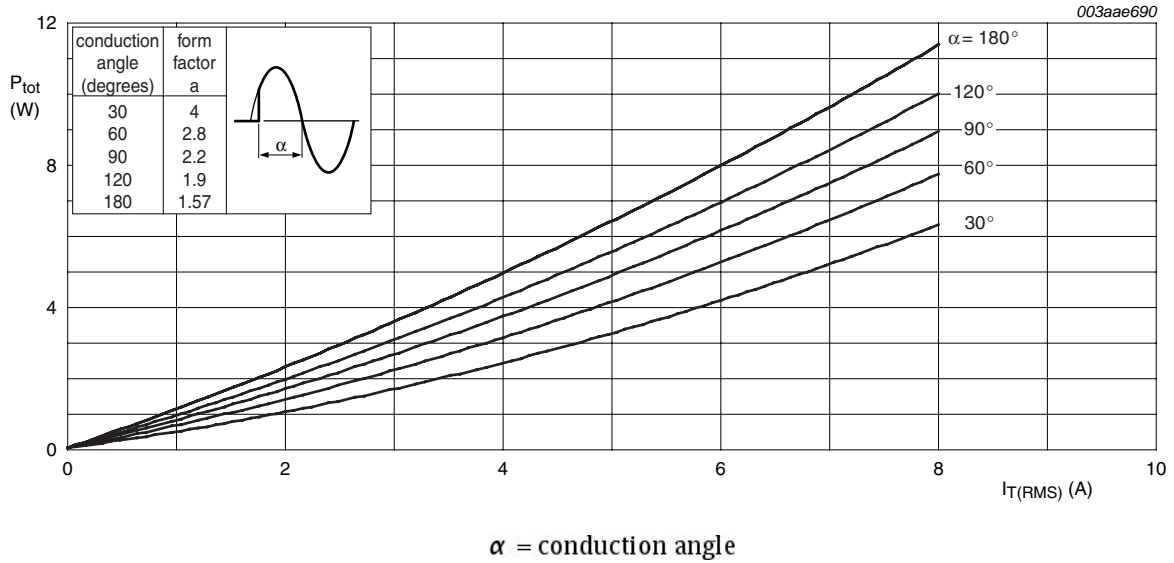


Fig 3. Total power dissipation as a function of RMS on-state current; maximum values

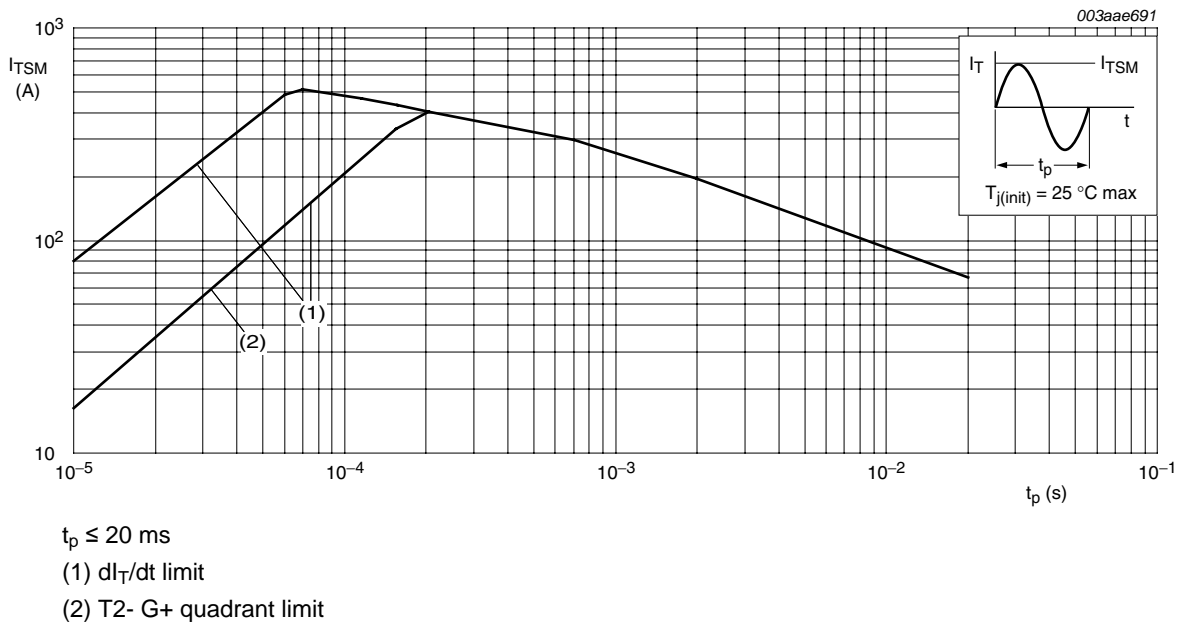
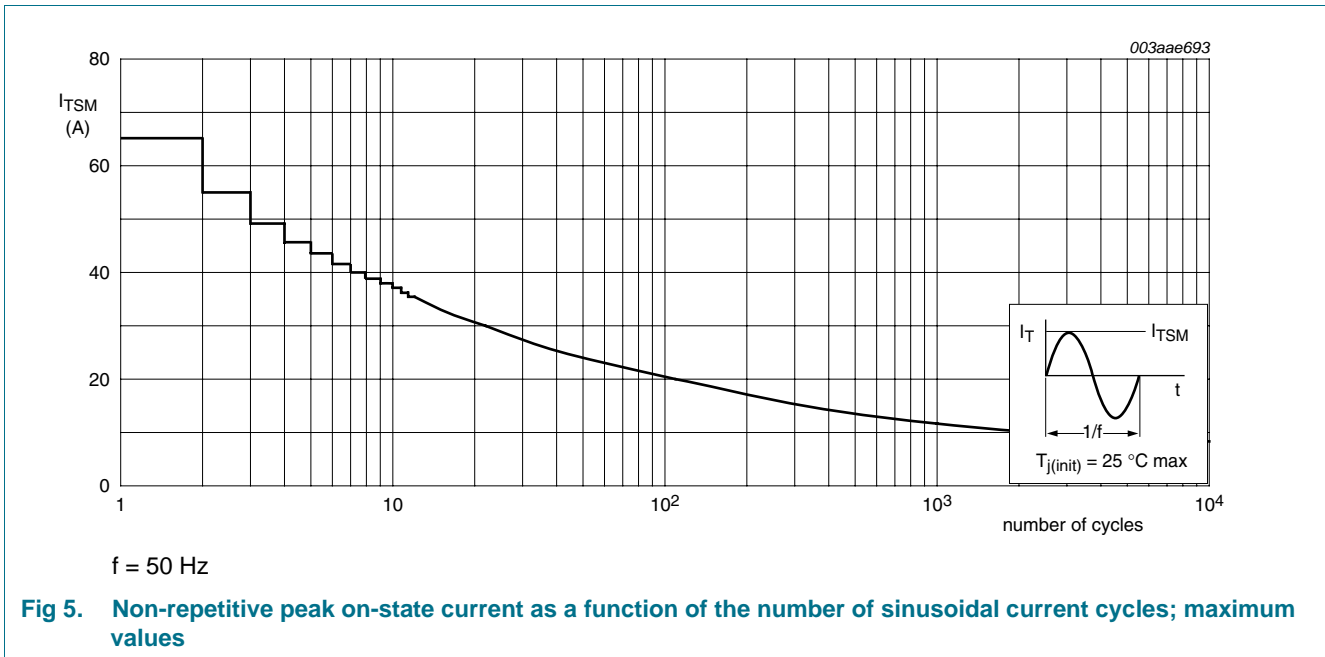


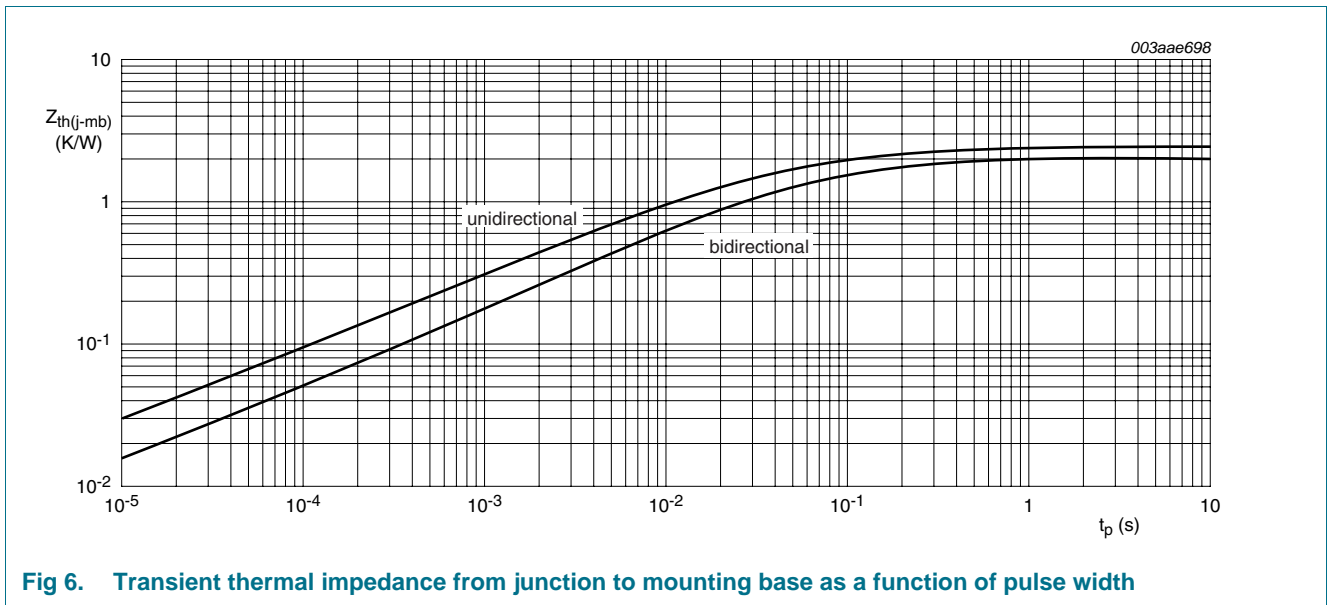
Fig 4. Non-repetitive peak on-state current as a function of pulse width; maximum values



## 5. Thermal characteristics

**Table 5. Thermal characteristics**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	half cycle; see <a href="#">Figure 6</a>	-	-	2.4	K/W
		full cycle; see <a href="#">Figure 6</a>	-	-	2	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	PCB (FR4) mounted; minimum pad sizes	-	75	-	K/W



**Fig 6. Transient thermal impedance from junction to mounting base as a function of pulse width**

## 6. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static characteristics</b>						
$I_{GT}$	gate trigger current	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2+ G+; $T_j = 25\text{ °C}$	-	2.5	5	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2+ G-; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 7</a>	-	3.5	5	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2- G-; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 7</a>	-	3.5	5	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2- G+; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 7</a>	-	6.5	10	mA
$I_L$	latching current	$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2+ G+; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 8</a>	-	1.6	15	mA
		$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2+ G-; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 8</a>	-	8.5	20	mA
		$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2- G-; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 8</a>	-	1.2	15	mA
		$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2- G+; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 8</a>	-	2.5	20	mA
$I_H$	holding current	$V_D = 12\text{ V}$ ; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 9</a>	-	1.5	10	mA
$V_T$	on-state voltage	$I_T = 10\text{ A}$ ; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 10</a>	-	1.3	1.65	V
$V_{GT}$	gate trigger voltage	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 11</a>	-	0.7	1.5	V
		$V_D = 400\text{ V}$ ; $I_T = 0.1\text{ A}$ ; $T_j = 125\text{ °C}$ ; see <a href="#">Figure 11</a>	0.25	0.4	-	V
$I_D$	off-state current	$V_D = 600\text{ V}$ ; $T_j = 125\text{ °C}$	-	0.1	0.5	mA
<b>Dynamic characteristics</b>						
$dV_D/dt$	rate of rise of off-state voltage	$V_{DM} = 402\text{ V}$ ; $T_j = 125\text{ °C}$ ; $R_{GT1} = 1\text{ k}\Omega$ ; exponential waveform; gate open circuit	-	5	-	V/ $\mu$ s
$t_{gt}$	gate-controlled turn-on time	$I_{TM} = 12\text{ A}$ ; $V_D = 600\text{ V}$ ; $I_G = 0.1\text{ mA}$ ; $dI_G/dt = 5\text{ A}/\mu$ s	-	2	-	$\mu$ s

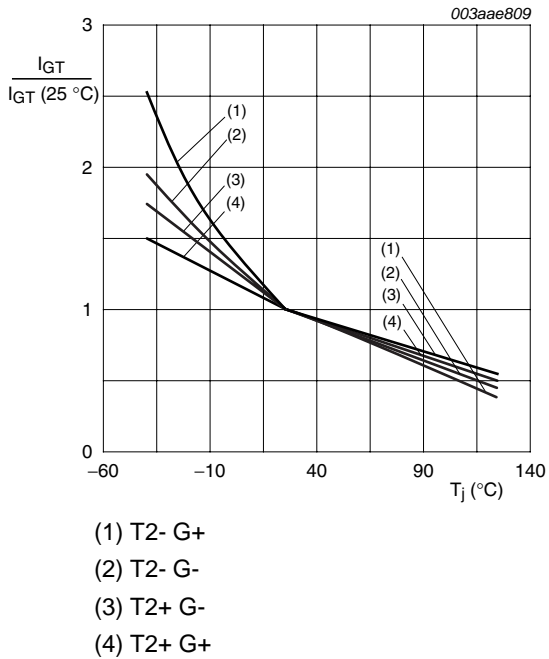


Fig 7. Normalized gate trigger current as a function of junction temperature

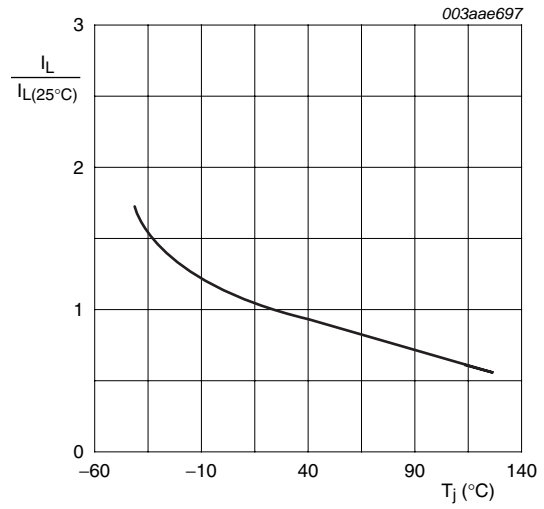


Fig 8. Normalized latching current as a function of junction temperature

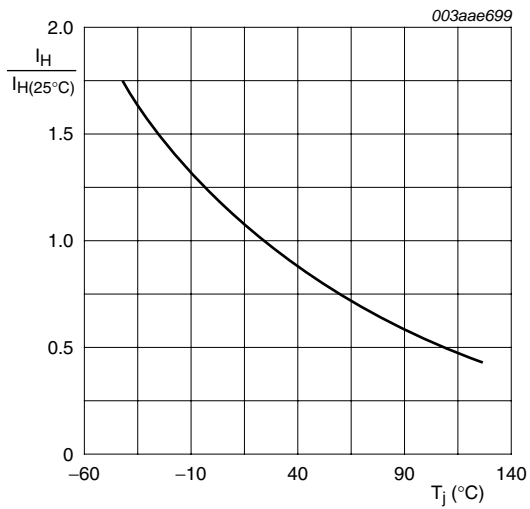
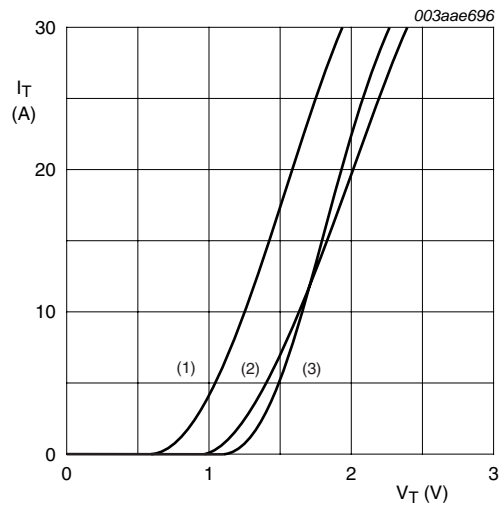


Fig 9. Normalized holding current as a function of junction temperature



$V_o = 1.264 \text{ V}$

$R_s = 0.038 \ \Omega$

(1)  $T_j = 125^\circ\text{C}$ ; typical values

(2)  $T_j = 125^\circ\text{C}$ ; maximum values

(3)  $T_j = 25^\circ\text{C}$ ; maximum values

Fig 10. On-state current as a function of on-state voltage

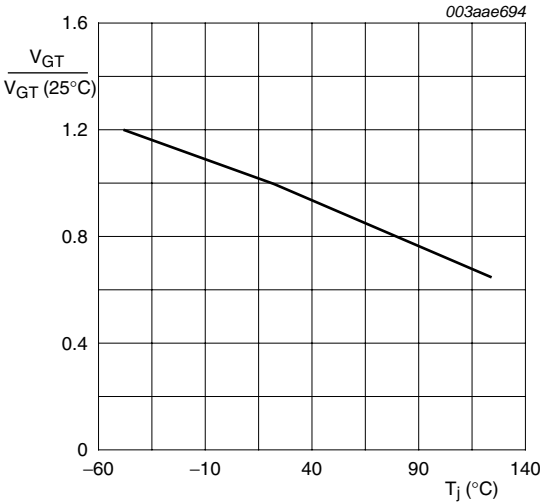


Fig 11. Normalized gate trigger voltage as a function of junction temperature

7. Package outline

Plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped)

SOT428

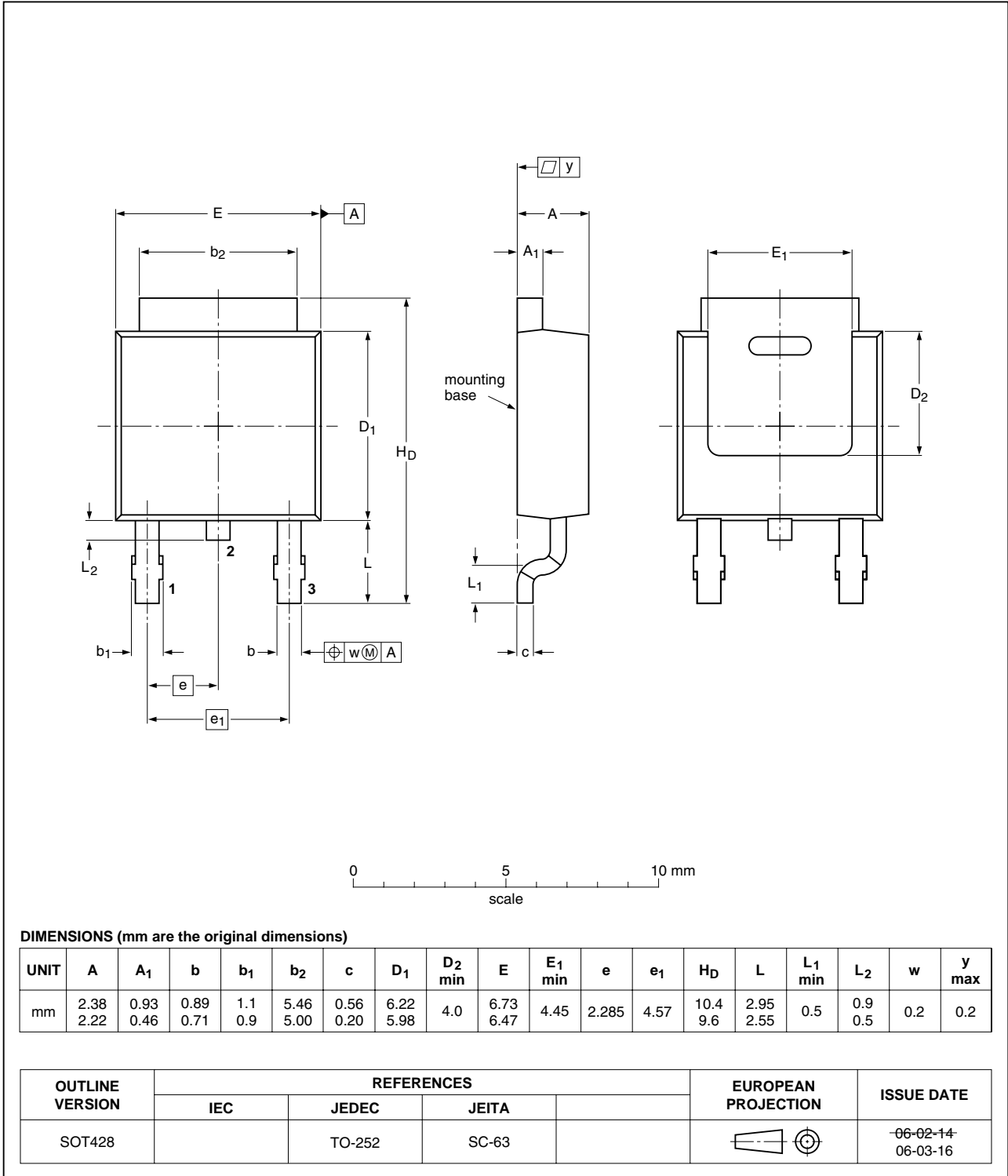


Fig 12. Package outline SOT428 (DPAK)

## 8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BT137S-600D v.3	20110328	Product data sheet	-	BT137S-600D v.2
Modifications:	<ul style="list-style-type: none"><li>• The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li><li>• Legal texts have been adapted to the new company name where appropriate.</li></ul>			
BT137S-600D v.2	20010601	Product specification	-	BT137S-600D v.1

## 9. Legal information

### 9.1 Data sheet status

Document status <sup>[1]</sup> <sup>[2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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## 11. Contents

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<b>1</b>	<b>Product profile</b> . . . . .	<b>1</b>
1.1	General description . . . . .	1
1.2	Features and benefits . . . . .	1
1.3	Applications . . . . .	1
1.4	Quick reference data . . . . .	1
<b>2</b>	<b>Pinning information</b> . . . . .	<b>2</b>
<b>3</b>	<b>Ordering information</b> . . . . .	<b>2</b>
<b>4</b>	<b>Limiting values</b> . . . . .	<b>3</b>
<b>5</b>	<b>Thermal characteristics</b> . . . . .	<b>6</b>
<b>6</b>	<b>Characteristics</b> . . . . .	<b>7</b>
<b>7</b>	<b>Package outline</b> . . . . .	<b>10</b>
<b>8</b>	<b>Revision history</b> . . . . .	<b>11</b>
<b>9</b>	<b>Legal information</b> . . . . .	<b>12</b>
9.1	Data sheet status . . . . .	12
9.2	Definitions . . . . .	12
9.3	Disclaimers . . . . .	12
9.4	Trademarks . . . . .	13
<b>10</b>	<b>Contact information</b> . . . . .	<b>13</b>

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