



CRYSTAL OSCILLATOR

OSC52

Applications

- PC Main board / VGA card / DVD / DVC / DSC / Printer / Scanner / Game Console

Features

- Low height ceramic package / Dimensions (7.0×5.0×1.4)
- CMOS / TTL Output with Tri-state function

Specifications



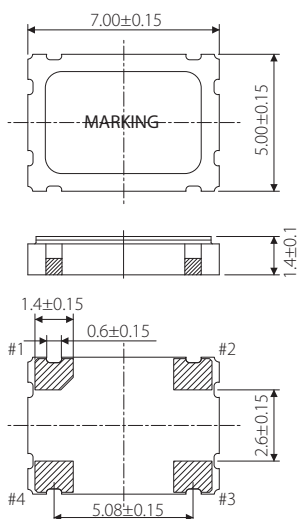
Model	OSC52	
Nominal frequency range	0.500~160.000 MHz	0.500~107.000 MHz
Storage temperature range	-55~+125°C	
Operating temperature range	0~+70°C	
Frequency stability	$\pm 20 \times 10^{-6}$, $\pm 25 \times 10^{-6}$, $\pm 50 \times 10^{-6}$, $\pm 100 \times 10^{-6}$	
Power supply voltage (Vdd)	+3.3V DC $\pm 10\%$	+5.0V DC $\pm 10\%$
Current consumption	10 mA max: 0.5~14.9MHz 15 mA max: 15~29.9MHz 20 mA max: 30~39.9MHz 25 mA max: 40~49.9MHz 30 mA max: 50~59.9MHz 35 mA max: 60~79.9MHz 45 mA max: 80~99.9MHz 50 mA max: 100~125MHz 65 mA max: 125.1~160MHz	10(×20) mA max: 0.5~19.9MHz 10(×35) mA max: 20~29.9MHz 15(×35) mA max: 30~34.9MHz 30(×35) mA max: 35~49.9MHz 30(×60) mA max: 50~65.9MHz 50(×60) mA max: 66~80.0MHz 60 mA max: 80.1~107MHz
Output level	TTL-CMOS (See Table A)	
Tri-state Function	#1: Floating or "H"→Output enable / #1: "L"→Output disable (Hi-Z)	

Table A

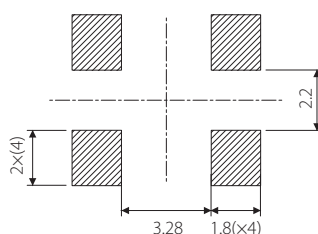
Output level	CMOS	TTL (+5.0V only)
Load	15pF (×50pF)	2TTL, 5TTL, 10TTL
Level	V_{OL} : 10%Vdd max. / V_{OH} : 90%Vdd min.	V_{OL} : +0.4 max. / V_{OH} : 2.4V min.
Rise & Fall time	10ns max: $F \leq 40$ MHz, 7ns max: $F > 40$ MHz, 5ns max: $F > 100$ MHz at 10%~90% Vdd	at +0.4V - +2.4VDC
Duty cycle	45%~55% at 1/2Vdd $F \leq 50$ MHz 40%~60% at 1/2Vdd $F \leq 50$ MHz	40%~60% at +1.4V

Package quantity: 1,000pcs max./Reel. / Please consult with us for more details and Vdd: +1.8V, +2.5V, +2.8V, +3.0V

Outline and Dimensions [unit:mm]



Example of a Terminal Land Pattern



Terminal	Connection
#1	Tri-state
#2	GND
#3	OUTPUT
#4	Vdd

Tri-state Function

Tri-state Pin	Output
High or Floating	Active
Low	Hi-impedance