Mini SIP Passive Delay Lines

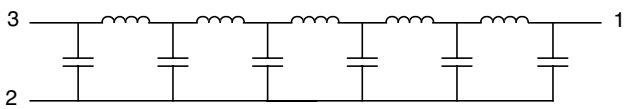
EP9910N-XX

• Operating Temperature : -40°C to +85°C •

Time Delay (nS) Bi-directional	Rise Time (nS Max.)	DCR (Ω Max.)	Impedance (±10%)					
	20%-80%		50Ω	55Ω	75Ω	93Ω	100 Ω	200Ω
1 ± 0.2	1.6	0.20	EP9910N-1A	EP9910N-1H	EP9910N-1F	EP9910N-1 I	EP9910N-1B	EP9910N-1C
2 ± 0.2	1.6	0.25	EP9910N-2A	EP9910N-2H	EP9910N-2F	EP9910N-2 I	EP9910N-2B	EP9910N-2C
3 ± 0.2	1.7	0.35	EP9910N-3A	EP9910N-3H	EP9910N-3F	EP9910N-3 I	EP9910N-3B	EP9910N-3C
4 ± 0.2	1.7	0.45	EP9910N-4A	EP9910N-4H	EP9910N-4F	EP9910N-4 I	EP9910N-4B	EP9910N-4C
5 ± 0.25	1.8	0.55	EP9910N-5A	EP9910N-5H	EP9910N-5F	EP9910N-5 I	EP9910N-5B	EP9910N-5C
6 ± 0.3	2.0	0.70	EP9910N-6A	EP9910N-6H	EP9910N-6F	EP9910N-6 I	EP9910N-6B	EP9910N-6C
7 ± 0.3	2.2	0.80	EP9910N-7A	EP9910N-7H	EP9910N-7F	EP9910N-7 I	EP9910N-7B	EP9910N-7C
8 ± 0.3	2.4	0.85	EP9910N-8A	EP9910N-8H	EP9910N-8F	EP9910N-8 I	EP9910N-8B	EP9910N-8C
9 ± 0.3	2.6	0.90	EP9910N-9A	EP9910N-9H	EP9910N-9F	EP9910N-9 I	EP9910N-9B	EP9910N-9C
10 ± 0.3	2.8	0.95	EP9910N-10A	EP9910N-10H	EP9910N-10F	EP9910N-10 I	EP9910N-10B	EP9910N-10C
11 ± 0.35	3.0	1.00	EP9910N-11A	EP9910N-11H	EP9910N-11F	EP9910N-11 I	EP9910N-11B	EP9910N-11C
12 ± 0.35	3.2	1.05	EP9910N-12A	EP9910N-12H	EP9910N-12F	EP9910N-12 I	EP9910N-12B	EP9910N-12C
13 ± 0.35	3.6	1.10	EP9910N-13A	EP9910N-13H	EP9910N-13F	EP9910N-13 I	EP9910N-13B	EP9910N-13C
14 ± 0.35	3.8	1.15	EP9910N-14A	EP9910N-14H	EP9910N-14F	EP9910N-14 I	EP9910N-14B	EP9910N-14C
15 ± 0.35	4.0	1.20	EP9910N-15A	EP9910N-15H	EP9910N-15F	EP9910N-15 I	EP9910N-15B	EP9910N-15C
16 ± 0.4	4.2	1.25	EP9910N-16A	EP9910N-16H	EP9910N-16F	EP9910N-16 I	EP9910N-16B	EP9910N-16C
17 ± 0.4	4.4	1.30	EP9910N-17A	EP9910N-17H	EP9910N-17F	EP9910N-17 I	EP9910N-17B	EP9910N-17C
18 ± 0.4	4.6	1.35	EP9910N-18A	EP9910N-18H	EP9910N-18F	EP9910N-18 I	EP9910N-18B	EP9910N-18C
19 ± 0.4	4.8	1.40	EP9910N-19A	EP9910N-19H	EP9910N-19F	EP9910N-19 I	EP9910N-19B	EP9910N-19C
20 ± 0.4	5.0	1.45	EP9910N-20A	EP9910N-20H	EP9910N-20F	EP9910N-20 I	EP9910N-20B	EP9910N-20C

DC Electrical Characteristics	Min	Max	Unit
Distortion		±10	%
Temperature Coefficient of Delay		100	PPM/°C
Insulation Resistance @ 100 Vdc	1K		Meg Ohms
Dielectric Strength		100	Vdc

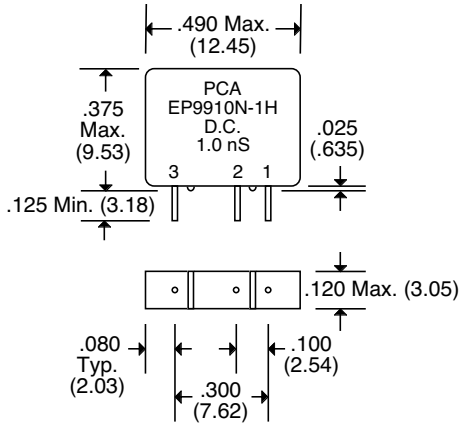
Schematic



Recommended Operating Conditions		Min	Max	Unit
P_W*	Pulse Width % of Total Delay	200		%
D*	Duty Cycle		40	%
T_A	Operating Free Air Temperature	-40	85	°C

**These two values are inter-dependent.*

Package



Input Pulse Test Conditions @ 25°C		
V_{IN}	Pulse Input Voltage	3 Volts
P_W	Pulse Width % of Total Delay	300 %
T_{RI}	Input Rise Time (10 - 90%)	2.0 nS
P_{RR}	Pulse Repetition Rate	1.0 MHz

Leads : #24 TC WIRE

Unless Otherwise Specified Dimensions are in Inches /mm ± .010 /.25

PCA ELECTRONICS, INC.
16799 SCHOENBORN ST.
NORTH HILLS, CA 91343

CS9910N-XX Rev. F 6/8/04 RR

TEL: (818) 892-0761
FAX: (818) 894-5791
http://www.pca.com