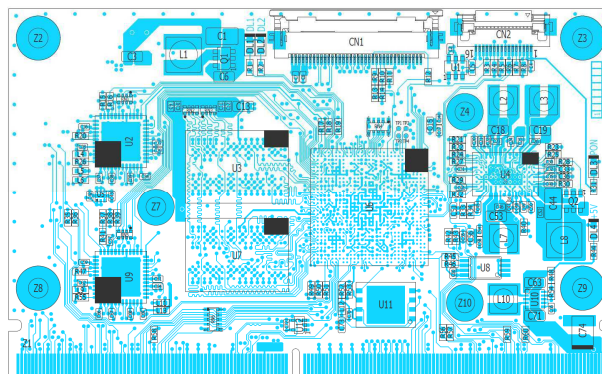


AixSolve SMARC 385

The AixSolve *SMARC 385* bases on the Texas Instruments DaVinci DM 385 and is designed according the SMARC specification of the SGET organization (<http://www.sget.org/standards/smarc.html>). The module is highly applicable for image processing and video encoding applications. A CSI-2 and a 16bit parallel interface for video and an I2S interface for audio are available.



TI DaVinci Digital Media Processor DM385	<ul style="list-style-type: none"> • 1000 MHz ARM Cortex-A8 RISC Processor • Image Subsystem (ISS) • Face Detect (FD) Engine • Programmable High-Definition Video Image Coprocessing (HDVICP v2) Engine • H264 Video Encoding up to 1080p60 • TI DM 385 DaVinci RDK
SMARC – Connector (314 pins 0.5 mm pitch)	<ul style="list-style-type: none"> • 2 x Ethernet • 1 x Camera Input (16 bit parallel + ext. Syncs) • 1 x CAN (not for all) • 1x UART • 1 x SD card • 1 x USB • 1 x HDMI (output) • 3 x I2C (HDMI, CAM, ..) • 1 x I2S (audio input) • 4 x GPIO
ZF – Connector (CN1)	<ul style="list-style-type: none"> • 1 x CSI-2 • 1 x I2C
ZF – Connector (CN2)	<ul style="list-style-type: none"> • 1 x JTAG • 1 x UART
Dimensions	<ul style="list-style-type: none"> • 82mm x 50 mm x 5.7 mm

The content of this datasheet is furnished for informational use only, is subject to change without notice, and should not be construed as a commitment.

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AixSolve Related Product – AixSolve TI Davinci Encoding Software (preloaded)

Codec - Video	<ul style="list-style-type: none"> • Conforms to ITU-T H.264 & ISO/IEC4496-10 (H.264/AVC) High Profile Level 4.0 • 1920 × 1080 × 60p/50p • 1920 × 1080 × 60i/50i/24p • 1280 × 720 × 60i/50i/24p • 720 × 480 × 60i • 720 × 576 × 50i • Pixel format: 4:2:0
GOP Structure	<ul style="list-style-type: none"> • I frames only • IP frames/slices • Variable structure
GOP Length	<ul style="list-style-type: none"> • Number of frames between I frames selectable between 1 and 30
Bit rate	<ul style="list-style-type: none"> • 0,8-20 Mbit @ resolution of 1 kbps • CBR • VBR
CODEC - Audio	<ul style="list-style-type: none"> • MPEG-2 AAC 2-channel encoding
Codec – System	<ul style="list-style-type: none"> • MPEG2-TS encoding processing conforming to ISO/IEC13818-1 and Amd3 • Multiplexing of Video1 stream + Audio1 stream <ul style="list-style-type: none"> ○ Video Stream ID and PID ○ Audio Stream IP and PID ○ Program Clock Reference (PCR) PID • Program Specific Information (PSI) tables (EN 300 468) <ul style="list-style-type: none"> ○ Program Association Table (PAT) ○ Program Map Table (PMT) ○ Network Information Table (NIT) • Service Information (SI) tables <ul style="list-style-type: none"> ○ Service Description Table (SDT) • Input and output of ES user area
Boot time	<ul style="list-style-type: none"> • < 8s
Control	<ul style="list-style-type: none"> • Serial Interface

Remark: Also available in source code

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