

AixSolve QamMegafunction

The AixSolve *QamMegafunction* is suitable for QAM 64 and 256. In conjunction with AixSolve *DirectSynthesesMegafunction* it reaches outstanding performance parameters like MER > 46 and SNR > 60.

Block diagram:



Key facts	<ul style="list-style-type: none"> • J.83 A/C compliant modules • QAM 16, 32, 64, 128, 256 with integrated mux • Symbol clock, sink and source mode • MER > 46 • SNR > 60
Optional Modules	<ul style="list-style-type: none"> • Input interfaces <ul style="list-style-type: none"> ◦ ASI, LVDS, LVTTTL etc • PID filter (drop, pass, rename, replace) • SI packet inserter (e.g. NIT, PAT, PMTs etc.) • Null packet insertion and PCR re-stamp • Multiplexer • AES 128 en-/decryption • FEC

Required resources*:

Altera	Combined ALUTs	Memory ALUTs	ALM	Dedicated Logic Registers	Combinational Logic	M9K	M144K	DSP
1x QAM+ RRC+HBF	4644	691	4094	3726	2939	45	0	0
8x QAM+ RRC+HBF	7345	724	7158	6998	4477	69	0	0

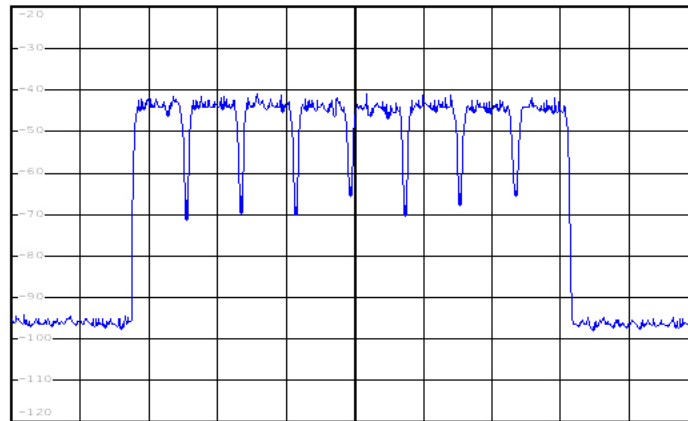
* Based on Stratix IV, Arria2GX similar, actual synthesis might vary, project depended

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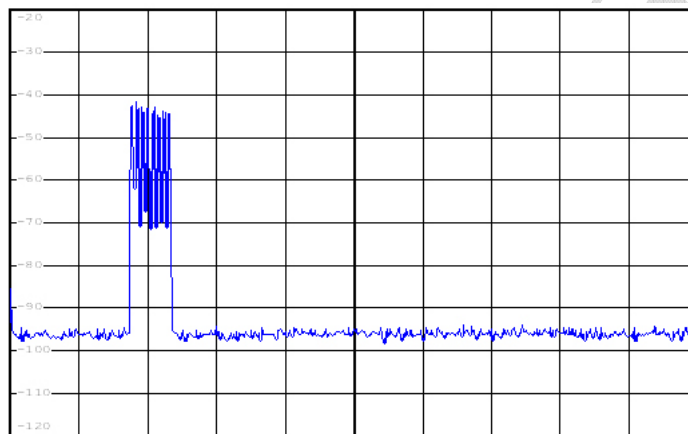
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ver.10/01/2012 14:56:00

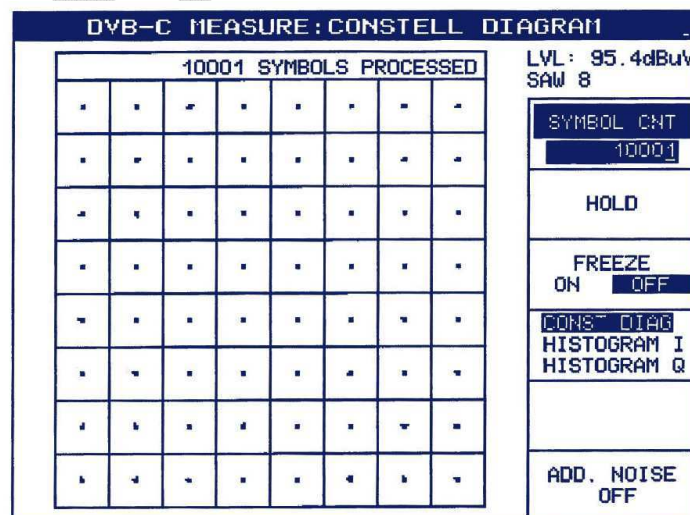
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Screen 1 : 8 channel QAM, 6900MSps, 8MHz channel raster, 177,5 MHz ff



Screen 2 : 8 channel QAM, 6900MSps, 8MHz channel raster, 177,5 MHz ff, full band



Screen 3: Constellation Diagram

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