

# **Terminator 5 Unified Wire Engine**

2x40GbE/4x10GbE, PCIe Gen3, SR-IOV, TCP/IP and UDP/IP, iSCSI, FCoE, RDMA Offload

## **Features**

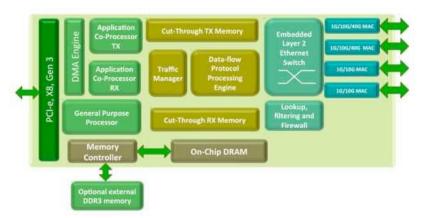
- PCI Express Gen3 x8 host bus
- Quad port 100M/1G/10G and dual port 40G
- Integrated KR4, KX4, CX4, KX, KR, SGMII support
- I<sup>2</sup>C, SMBus, IPMI 2.0 management interface
- Comprehensive Stateless Offloads
- Full TCP Offload and Microsoft TCP Chimney
- Low latency UDP Offload Engine
- Multicast Offload Engine<sup>™</sup>
- iSCSI Full Offload or PDU Offload
- FCoE Full Offload or PDU Offload
- Network Address Translation (NAT) Offload
- Low latency iWARP RDMA Offload
- Line rate Firewall Offload™
- Media/Video Stream Engine™
- iSCSI Routing™ and TCP Proxy™
- Integrated Ethernet switching and routing
- Extensive virtualization support
  - o 128 VF, 8 PF SR-IOV support
  - o Integrated 140-port virtual switch
  - VEB/VEPA/Flex10/VNTAGs support
  - Offloaded 802.1 Qbg/h support
- DCB (CEE) support
- Packet tracing and sniffing
- IEEE 1588 Offload (PTP)
- Fine grain timestamps with PTP Integration
- Mem-free operation capability
- Software compatible with T4
- FCBGA 31x31mm package

# **Benefits**

- Scalable to 1 million connections
- High performance architecture
- Unified Wire interconnect solution for server networking, storage networking and compute clustering on a single fabric
- Designed and tuned for low latency, high packet rate and high bandwidth
- Enables flexible 40G/10G/1G combinations
- Reduces host CPU utilization by up to 95%
- Highly programmable and manageable solution
- Robust, stable and proven fifth generation technology

# **Applications**

- Networking TCP & UDP Offload, IPv4 and IPv6
- Storage iSCSI & FCoE
- High performance computing RDMA & user-space
- Security and firewalls
- Network analytics and monitoring
- Web 2.0 and load balancing
- Virtualization and multi-tenancy
- Network traffic management
- Rich media streaming
- Application servers



**Terminator 5 Block Diagram** 

# **Specifications**

## **Host Interface**

- PCI Express Gen3 x8
- MSI-X, MSI and support for legacy pin interrupts

#### **Network Interfaces**

- Two 100M/1G/10G/40G and two 100M/1G/10G ports independently configurable
- Two ports exposed with KR4, KX4, CX4, KX, KR, SGMII
- Two ports exposed with KX, KR & SGMII

#### **Ethernet**

- IEEE 802.3ae (10 GbE)
- IEEE 802.3z (1 GbE) compliant
- IEEE 802.lp Priority
- IEEE 802.IQ VLAN tagging
- IEEE 802.3x Flow Control
- IEEE 802.3ad Link Aggregation
- IEEE 802.3ba (40GbE)
- IEEE 802.1Qau Congestion Notification
- IEEE 802.1Qbg/h Virtual Bridging
- Ether II and 802.3 encapsulated frames
- Multiple MAC addresses per interface
- Jumbo Frames up to 9.6Kbytes

#### **Stateless Offloads**

- TCP/UDP checksum offload for IPv4 & IPv6
- Flexible receive side steering (RSS) for IPv4 & IPv6
- Large/Giant Send Offload (LSO) for IPv4 & IPv6
- VLAN filtering, insertion & extraction
- Line rate packet filtering and attack protection
- Fine granularity time stamping (2 nsec)
- Ethernet Routing (packet header rewrite)
- Packet Tracing and Sniffing

# TCP/IP Full Offload

- Full TCP implementation including exceptions IPv4 & IPv6
- Extensive RFC compliance, fully featured stack
- Full TCP Proxy between any set of connections (encapsulation, bridging)
- Direct Data Placement
- 1 million simultaneous connections
- 1.5 μsec latency, line rate bandwidth

# StreamEngine™

- Up to 32K streams
- Multiple simultaneous transmit rates
- Precise rate control minimizes jitter
- Video segmentation offload with RTP support

Sales Contact: <a href="mailto:sales@chelsio.com">sales@chelsio.com</a>
Support Contact: <a href="mailto:support@chelsio.com">support@chelsio.com</a>

Ordering Info: T5ASIC40G

# Firewall Offload™

- Rule-based packet steering and filtering capability
- Tens of thousands of steering and filtering rules
- Flexible drop/steer/switch & rewrite actions

## **High Availability**

- Port Bonding with failover between and across cards for all modes NIC/TOE/iSCSI/iWARP/FCOE
- T10-DIF and DIX extensions

#### iSCSI Offload

- Full iSCSI initiator and target mode stack
- T10 DIF/DIX support
- CRC32C offload generation & verification
- Direct Data Placement
- iSCSI proxy switching based on SCSI CDB

### **FCoE Offload**

- Full offload HBA FCoE (Initiator or Target)
- T10 DIF/DIX support
- Open-FCoE Offload (Initiator)
- CRC32 offload generation & verification
- Ingress & Egress ACL (Access Control List)
- Direct Data Placement

### **High Performance RDMA**

- 1.5 μsec latency, line rate bandwidth
- iWARP support on Linux OFED
- Network Direct support
- Socket user-space I/O (TCP and UDP, raw Ethernet)
- Transport for Microsoft SMB-Direct (SMB 3.0)

# **UDP & Multicast Offload**

- UDP Sockets API
- 1.5 μsec user-to-user latency
- Multicast replication on ingress or egress

## Virtualization

- VMQueue and NetQueue support
- PCI-SIG SR-IOV, 128 VFs/8PFs
- Integrated 140 port virtual switch (VEB)
- Multicast/broadcast replication
- VEPA
- Flex10
- 512 MAC addresses
- Offloaded 802.1 Qbg/h support
- Complete statistics per VF

