# " **Ze**feer" ®

## Evaluation Boards & Development Kits

- □ CPU module based on ARM920T Cirrus Logic
- □ Expansion Bus fully available
- □ Linux 2.6.xx + native component drivers (ZELK)
- □ Windows CE BSP (ZWCK)
- □ ECos (ZECK)
- □ Evaluation Board available with exhaustive documentation

*Ze*feer Evaluation Boards & Development Kits support ready-to-use Zefeer CPU modules based on Cirrus Logic EP93XX CPUs. Evaluation Boards and kits are devoted to show the potentiality of the CPU modules, providing ready-to-use drivers for native interfaces allowed by microprocessors. All *native* buses and interfaces of the microprocessor are on the board or routed to separate connectors. Therefore users may perform full on-boards tests or think flexible design integrations.

Three environments (Linux, Windows CE and eCos) are available as well as two classes of microprocessors. A summary of all possibilities, features and names are listed below, as well as the Order Codes for Kits.

| Environment | Kit (order code) | Eval. Board | Module on board | Featuring also for… |
|---|---|---|---|---|
| Linux | ZELKL | EVBZL | DZB (9302) | DZA (9301) |
| Linux | ZELKH | EVBZH | DZQ (9315) | DZN (9312) DZG (9307) |
| eCos | ZECKL | EVBZL | DZB (9302) | DZA (9301) |
| eCos | ZECKH | EVBZH | DZQ (9315) | DZN (9312) DZG (9307) |
| Windows CE | ZWCK | EVBZH | DZQ (9315) | DZN (9312) DZG (9307) |

Two lower classes (DZA and DZB) have been clustered around EVBZL Evaluation Board. In such a way, with simple operations, EVBZL is featuring also for DZA modules. Similarly, EVBZH gathers around DZQ (highest module model) all high-ranked microprocessor type.

This has been made possible due to "one-module-fits-all" property, by which lowest models fit also in highest module's connectors. In such a way customers may enlarge performances on same design by simply plugging different models.

All Kits are supported by the well known Support Service that gives to the final user all needed information for starting its own design easily and quickly.

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**EVBZL**

| | |
|---|---|
| **CPU Module** | DZB4100C Zefeer Module w/ EP9302 @200MHz, 4MB Flash, 16MB SDRAM |
| **Memory on Host** | Flash NAND 32 Mbytes |
| **Others** | RTC via I2C bus + backup lithium battery |
| | Dip switches for bootstrapping options, reset pushbutton |
| **Interfaces** | Ethernet RJ45 connector |
| | UARTs nr.1 full (5+3) + nr.1 Rx-Tx |
| | USB host (type A) |
| | External Bus 16-bit, 26 Address Bits -5 direct Chip Select |
| | I/O Controller (number tbd) |
| **Debug Interfaces** | JTAG IEEE 1149.1 Test Access Port |
| **Mechanical** | Physical 100 (w) x 160 (l) mm$^2$ |
| **PSU** | 7-12V d.c. |
| **Agency approvals** | CE Mark (EN 55022, EN 61000-4-3, EN 61000-4-4, EN 61000-4-6 ) |

**EVBZH**

| | |
|---|---|
| **CPU Module** | DZQ3600C Zefeer Module w/ EP9315 @200MHz, 32MB Flash, 64MB SDRAM |
| **Memory on Host** | Flash NAND 32 Mbytes |
| **Others** | RTC via I2C bus + backup lithium battery |
| | Dip switches for bootstrapping options, reset pushbutton |
| **Interfaces** | Ethernet RJ45 connector |
| | UARTs nr.1 full (5+3) + nr.1 Rx-Tx |
| | USB host (type A) |
| | External Bus 16-bit, 26 Address Bits -5 direct Chip Select |
| | IDE 20 x 2 pin connector |
| | LCD * |
| | PCMCIA Type II connector |
| | I/O Controller (number tbd) |
| **Debug Interfaces** | JTAG IEEE 1149.1 Test Access Port |
| **Mechanical** | Physical  140 (w) x  195 (l) mm$^2$ |
| **PSU** | ATX standard, 20 pins minifit connector w/ start pushbutton |
| **Agency approvals** | CE Mark (EN 55022, EN 61000-4-3, EN 61000-4-4, EN 61000-4-6 ) |

**\* LCD software is part included in the kit even if LCD module is sold as a separate code**

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