Inventek Systems

Firmware Specification

Inventek Systems Flash Based Receivers

ISM300F2-C4

Dual UART with SiRF Binary on Port A at 57,600 and Port B NMEA-0183 at 4,800 baud rate

Updated at 1Hz

The ISM300F2-C4 is a Firmware build that configures the GPS modules to output the information as shown below on serial port A and B, upon initial power on. This is a Flashed based module that can be flashed in system. The user may change the baud rate, enable SiRF binary , enable SBAS or other features via a command to the UART.

- ❖ Updates position, velocity and time once per second without interpolation
- ❖ Eliminates the need for writing complicated code for commanding receiver specific messages
- ❖ Ideal for most applications and is considered our standard build

Serial Port A – Tx A output	Serial Port B – Tx B output
* 57,600 baud rate	* 4,800 baud rate
* SiRF Binary output	* NMEA-0183
* SBAS enabled	* SBAS enabled
* 1 Hz update rate	* 1 Hz update rate

ISM300F2-C4 comprises a 20-channel GPS receiver in a compact surface-mountable module. Based on the fast and deep search capabilities of SiRFstarIII™, it features world class performance with TTFF (time-to-first-fix) under open sky in just 35 s from a cold start, 1 s from a hot start. Reacquisition time is 1 s.

Its surface mount technology (SMT) allows for fully automatic assembly and reflow soldering. It receives all of its data, power and RF signals through its surface mount pads. Being lead-free, it complies with the European Union's RoHS directive. Its EMI/EMC shield not only meets FCC/CE standards, but also assures peak performance in noisy environments.

Ordering Information

ISM300F2-C4 (Release Date 10/1/2007)

Supports Documents

ISM300F2 Specification

GPS Evaluation Board

18mm x 18mm