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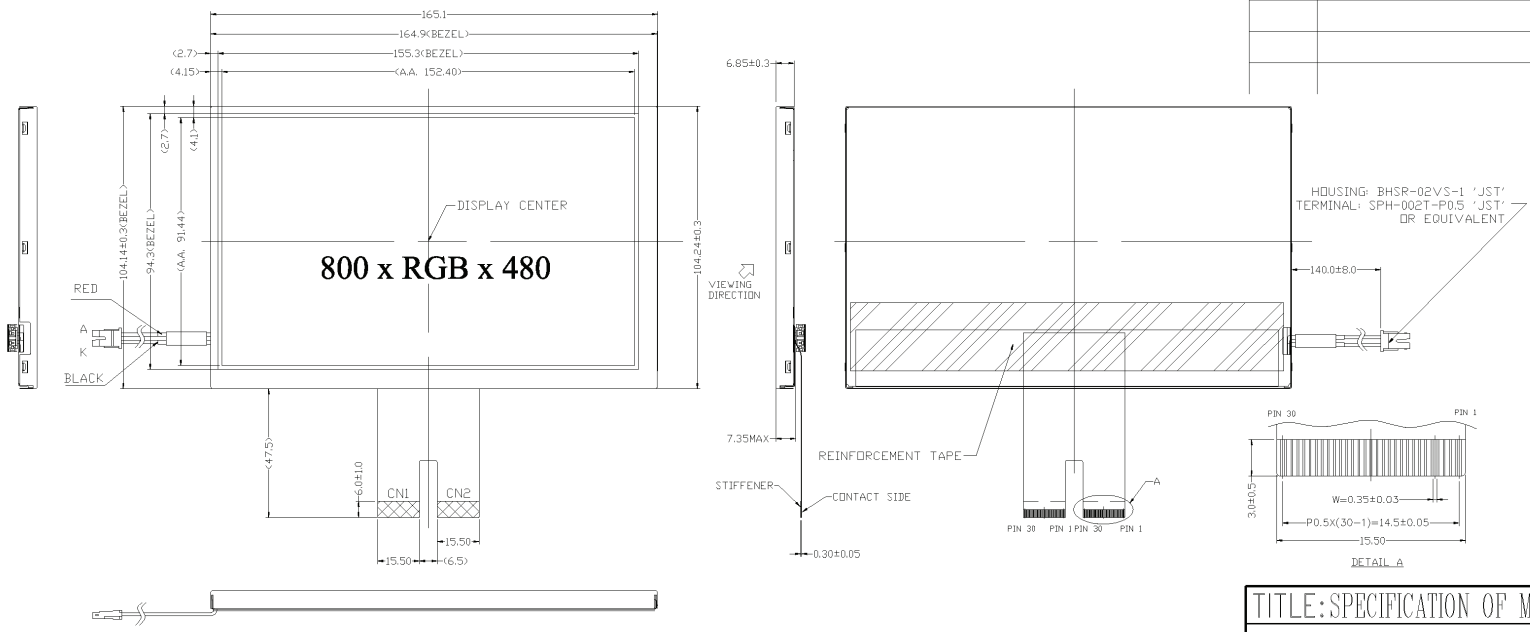
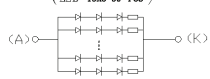
ISSUE	AMENDMENT	DATE



BACKLIGHT CIRCUIT DIAGRAM:
(LED 13X3=39 PCS)



PINS ASSIGNMENT:

CN1

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
DIO1	**VSS1**	**VDD1**	**CLK**	**VSS1**	**R/L**	**R0**	**R1**	**R2**	**R3**	**R4**	**R5**	**VSS1**	**G0**	**G1**
16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
G2	**G3**	**G4**	**G5**	**VSS1**	**B0**	**B1**	**B2**	**B3**	**B4**	**B5**	**LD**	**REV**	**POL**	**DIO2**

CN2

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
VSS2	**V1**	**V2**	**V3**	**V4**	**V5**	**V6**	**V7**	**VSS2**	**V8**	**V9**	**V10**	**V11**	**V12**	**V13**
16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
V14	**VSS2**	**VDD2**	**VCOM**	**XON**	**OE**	**U/D**	**CKV**	**STVU**	**STVD**	**VGG**	**GND**	**VCC**	**GND**	**VEE**

DISPLAY TYPE	TFT, RGB, +VE, TRANSMISSIVE		
DISPLAY RESOLUTION	800 x RGB x 480		
VIEWING ANGLE	6 O'CLOCK		
MUX RATIO AND BIAS LEVEL	1/480 DUTY		
LOGIC VOLTAGE-DIGITAL (VDD1)	3.3V		
LOGIC VOLTAGE-ANALOG (VDD2)	9.5V		
GATE ON VOLTAGE (VGG)	17.0V		
GATE OFF VOLTAGE (VEE)	-10.0V		
VOLTAGE FOR LOGIC CIRCUIT (VCC)	3.3V		
OPERATING TEMPERATURE	−30°C TO +70°C		
STORAGE TEMPERATURE	−40°C TO +80°C		
BACKLIGHT (@25°C)	TYPE	N/A	
	COLOR	WHITE	
	WAVELENGTH	--	
	DRIVING VOLTAGE	~9.6V(@260mA)	
	LUMINANCE(cd/m2) (ON THE BACKLIGHT SURFACE)	MIN.	--
		TYP.	6500
		MAX.	--

NOTES:

1. ◇ : SPECIAL CHARACTERISTIC
2. + : SAFETY CHARACTERISTIC
3. ▽ : CRITICAL DIMENSION
4. (..) : REFERENCE DIMENSION

VARITRONIX LIMITED

TITLE: SPECIFICATION OF MODULE

PROJECT NO: TLM-T700T0120-01-R0

CUSTOMER NO:

TOLERANCE UNLESS OTHERWISE SPECIFIED: X.X ±0.3, X.XX ±0.1

DIMENSIONS IN MM

MATERIAL: N/A | FINISH: N/A

SCALE: DO NOT ON SCALE | OVERALL THK: N/A

THIRD ANGLE PROJECTION

	NAME	SIGN	DATE
DRAWN:	KWOK KA LOK		08.05.08
CHECKED:	PHILIP HO		
APPROVED:	CYRUS CHEUNG		

ITEM NO.: TLM-T700T0120

DESCRIPTION: TLM-T700T0120-01

FILE NO: TLM-T700T0120-01 | REV: 0

SHEET 1 OF 1