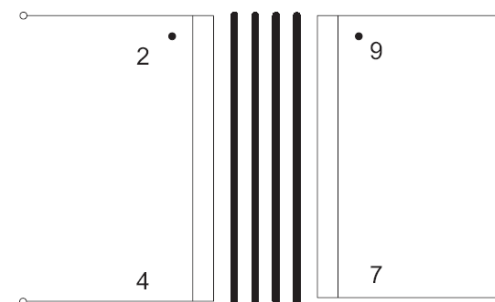


Pin view
Suggested PCB layout



Electrical schematic

Electrical specification:

- Ratio: 2:1
- DC resistance (Ω +/-15%):
- Primary: 1.03
- Secondary: 0.63
- Primary inductance (1 - 4): 10mH min. (@100kHz, 0.1V)
- Primary leakage inductance: 18uH nom.
- Interwinding capacitance: 25pF nom.
- Isolation: winding to winding: 1.5kVrms for 2 seconds
- Creepage and clearance: 1.4mm min. (basic insulation)



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DESCRIPTION	ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
Specification for PT10SM	1	15/08/07	CS		PT10SM
	2	11/02/08	CS		
	3	05/03/08	CS		
	4	16/07/08	CS		

Scale: 2 to 1

All dimensions in mm unless stated otherwise