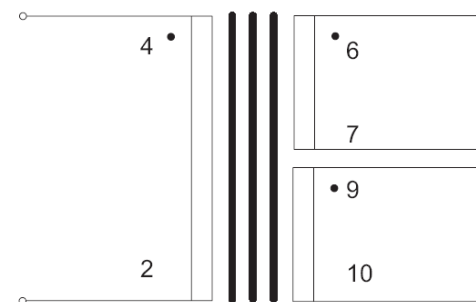


Pin view
Suggested PCB layout



Electrical schematic

Electrical specification:

Ratio: 1:1:1
DC resistance (Ω +/-15%):
Primary: 0.55
Secondary 1: 0.48
Secondary 2: 0.63
Primary inductance: 2.5mH min. (@100kHz, 0.1V)
Primary leakage inductance: 1.8uH nom.
Interwinding capacitance: 30pF nom.
Isolation: winding to winding: 1.5kVrms for 2 seconds
Creepage and clearance: 1.4mm min. (basic insulation)

Materials:

Bobbin: Nylon 46, 30% glass reinforced, e.g. Stanyl TE250 F6 UL file number E47960 class H or phenolformaldehyde glass-reinforced to UL94V-0, UL file number E41429(M) class H or equivalent.

Winding wire: grade 2 solderable class F minimum to IEC BS 60317-21e.g.Nexans Magnesol or equivalent.

Tape: Polyester film, e.g. 3M's No. 56 or No. 1350: thickness 0.06mm or Jingjiang Yahua type CT-280, UL file number E165111

 Walters OEP Ltd. Unit 5, Oxonian Park, Langford Locks, Kidlington, Oxfordshire. OX5 1FP Tel: (01865) 855085 Fax: (01865) 855075 Website: www.oep.co.uk	DESCRIPTION	ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
	Specification for PT6SM	1	15/08/07	CS		PT6SM
		2	11/02/08	CS		
		3	05/03/08	CS		
		4	16/07/08	CS		
Scale: 2 to 1	All dimensions in mm unless stated otherwise					